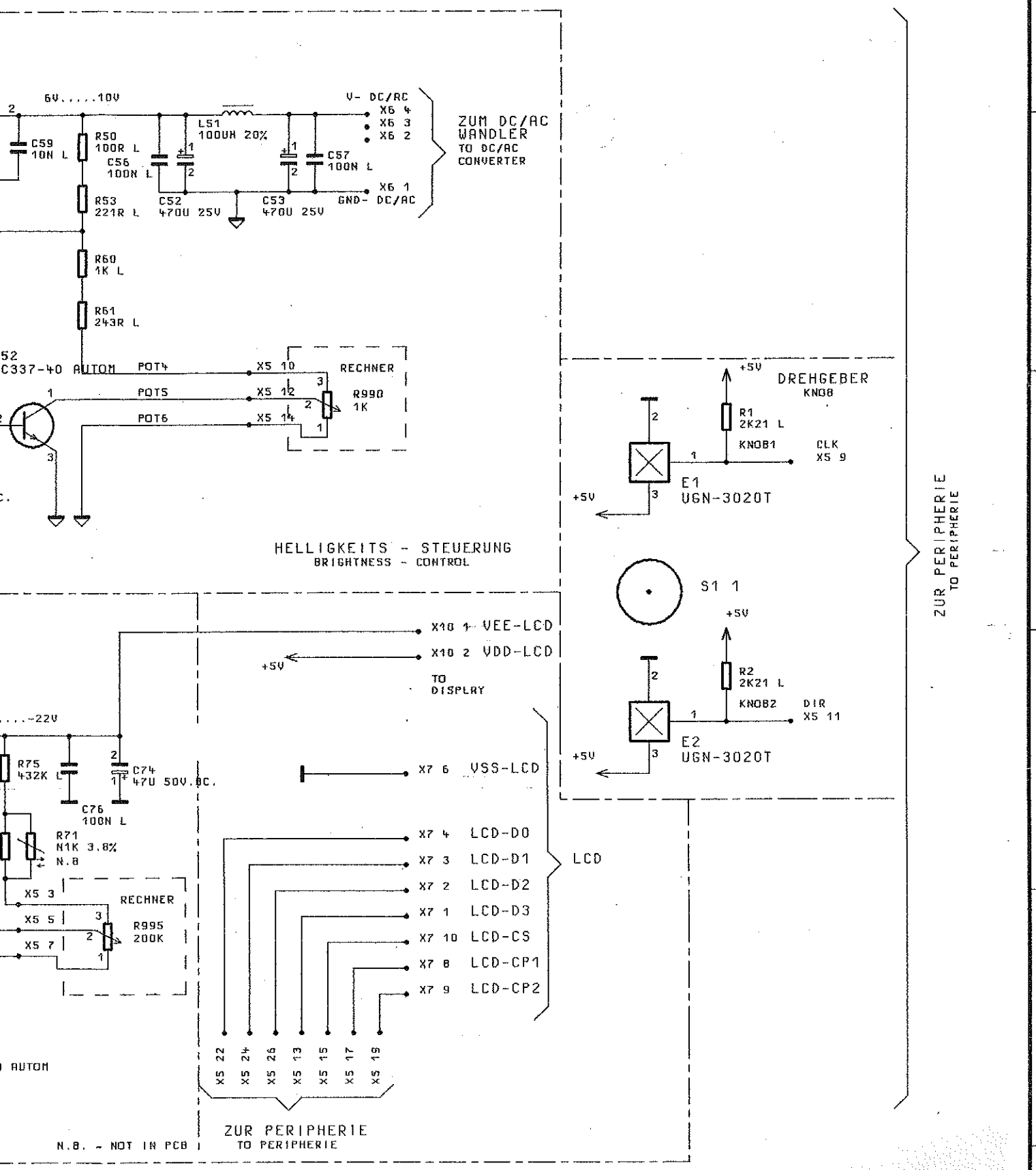
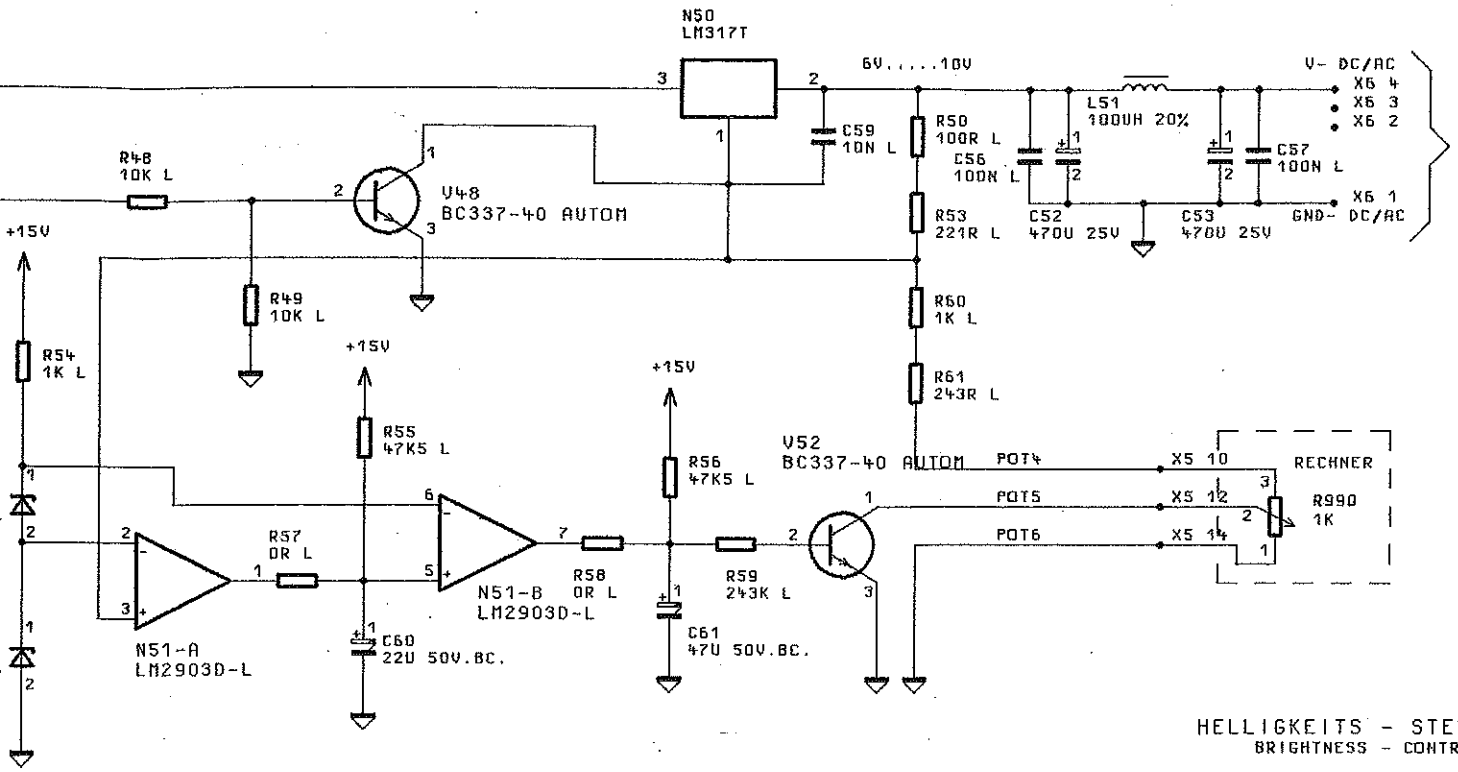
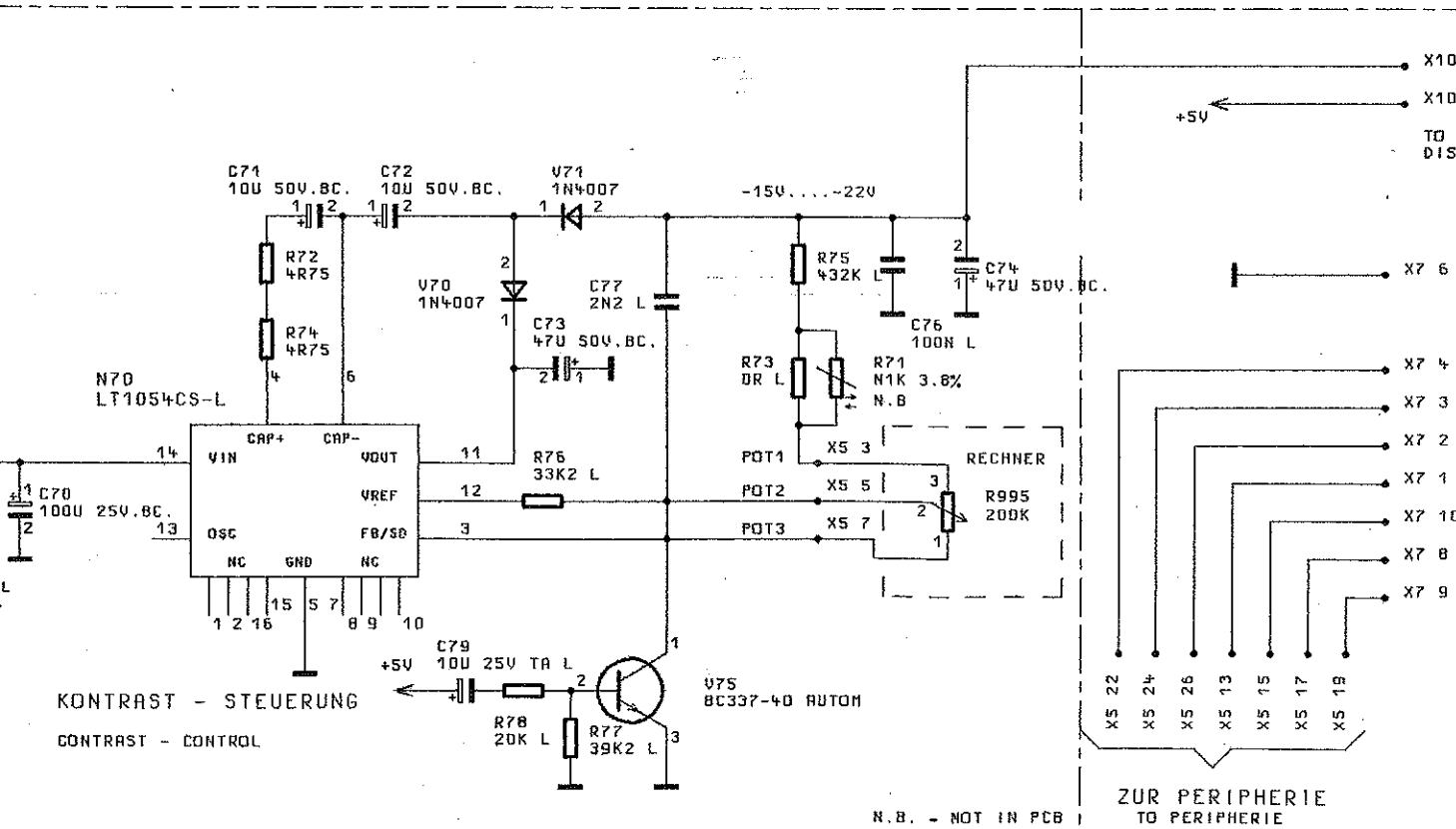


04/	48730 90	07.04.94	JN	16PK	TAG	NAME	BENENNUNG
				BEARB.		JN	DREHGEBER
				GEPR.		DR	KNOB ASSEMBLY
				NORM			
				PLOTT	07.04.94		
/							
AEND. IND.	AENDERUNGS-MITTEILUNG.	DATUM	NAME	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.5592.01S
				ZU GERAET SMP			BLATT-NR. 1
				REG.T.V. 1035.5005			ERSTE Z. 1035.5440



HELLIGKEITS - STEUERUNG
BRIGHTNESS - CONTROL



STROMLAUF GILT FUER VAR.02

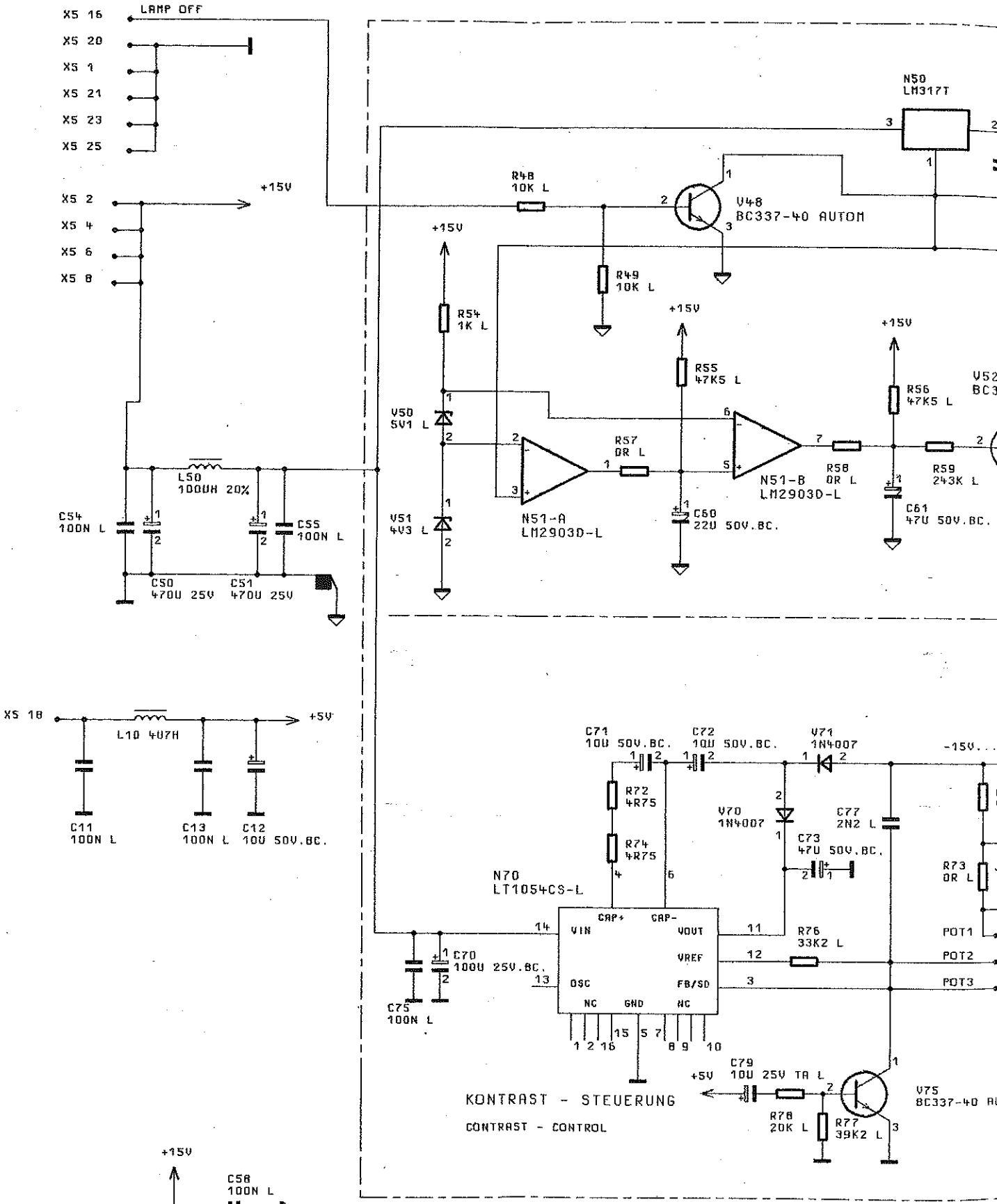
CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

04/	48730 90	07.04.94	JN	1GPK	TAG	NAM
				BEARB.		JN
				GEPR.		DR
				NORM		
				PLOTT	07.04.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG.	DATUM	NAME	ZU GERAET	SMP	

ROHDE&SCHWA

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

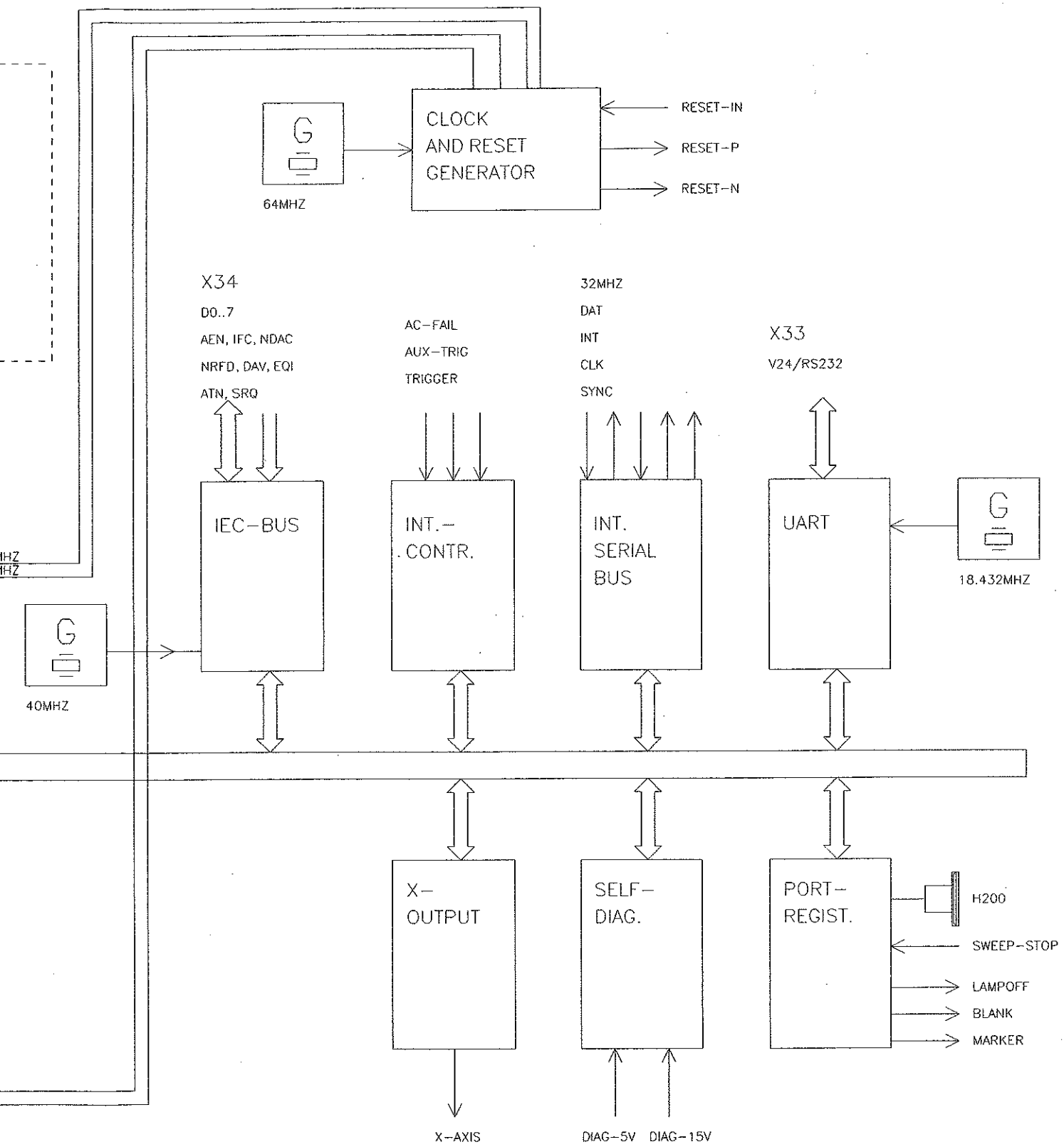


STROMLAUF GILT FUER VAR.02

CIRCUIT DIAGRAM IS VALID FOR MOD.02

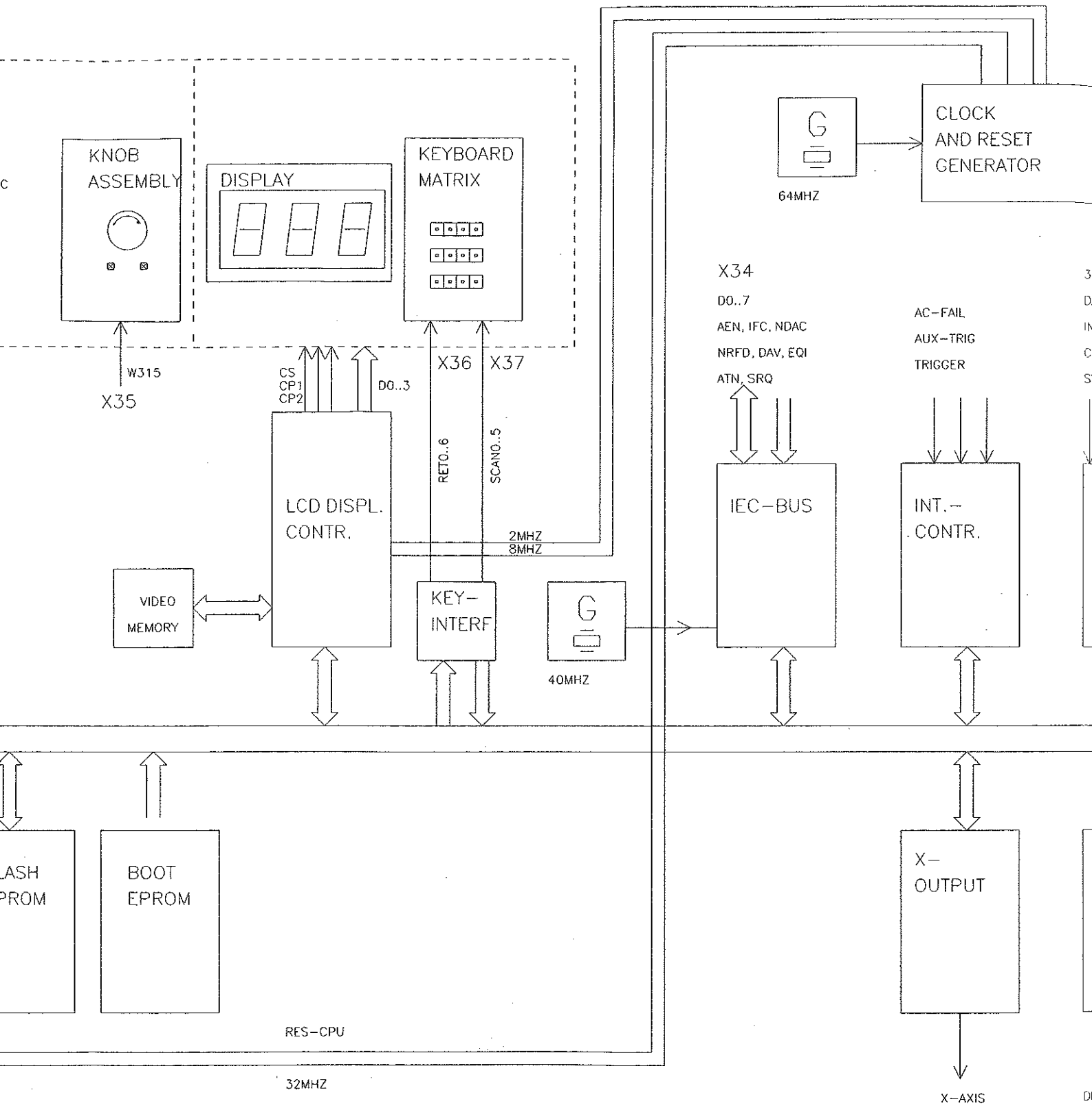
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

ZEICHN.-NR.



02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.1
				PLOTT	9.6.97	WEH	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

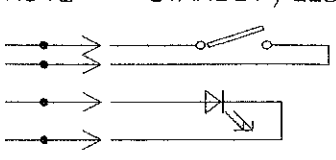
BLATT-NR. 1 + 47



X31 FROM MOTHERBOARD

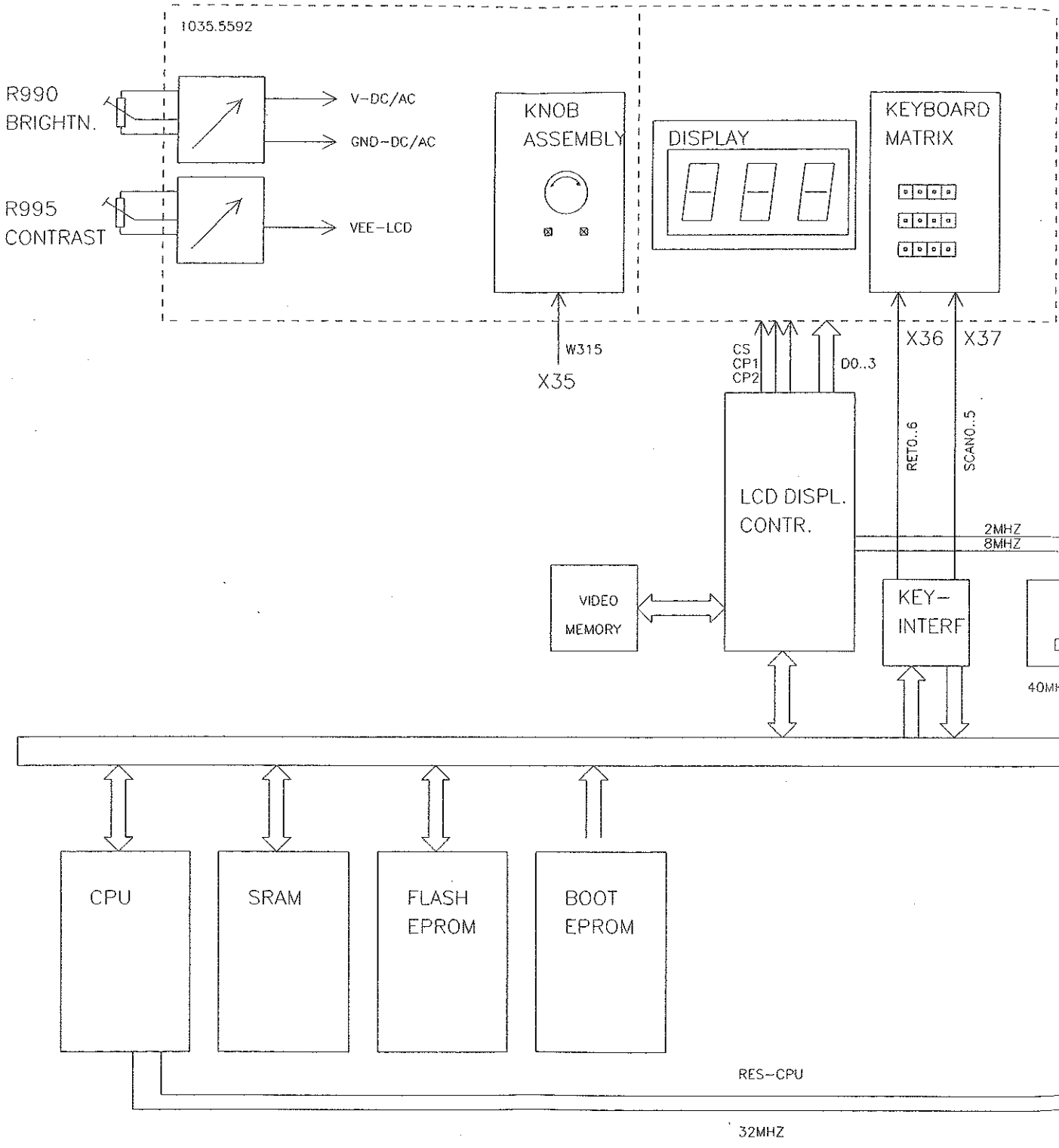
W31 → +15V
→ −15V
→ VD5-P

X312 STANDBY /LED



02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

BEHALTEN WIR UNS ALLE RECHTE VOR



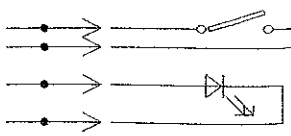
Bindende Angaben ueber Varianten, Trimmwerte, Bauteile und nicht bestueckte Bauteile siehe SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST

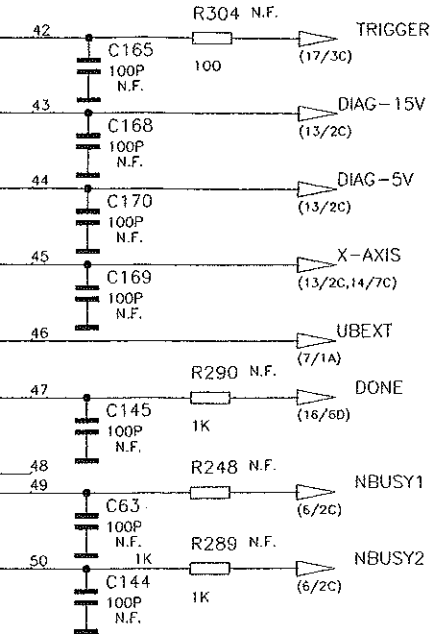
X31 FROM MOTHERBOARD

W31 → +15V
→ −15V
→ VD5−P

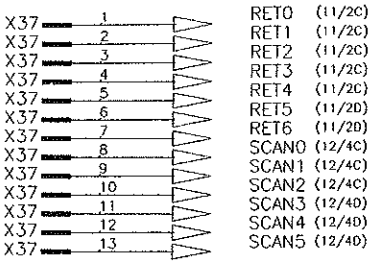
X312 STANDBY /LE



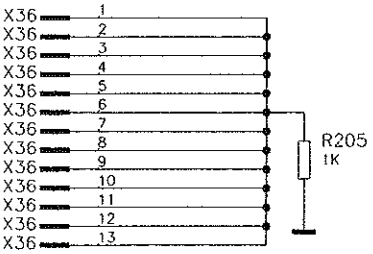
⊖ R995, R990



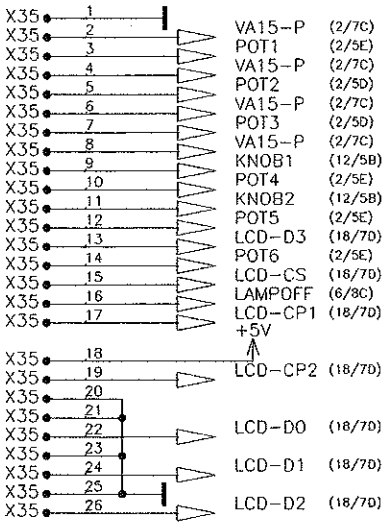
TO KEYBOARD X37



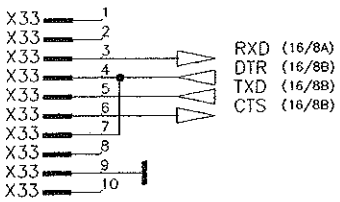
TO KEYBOARD X36



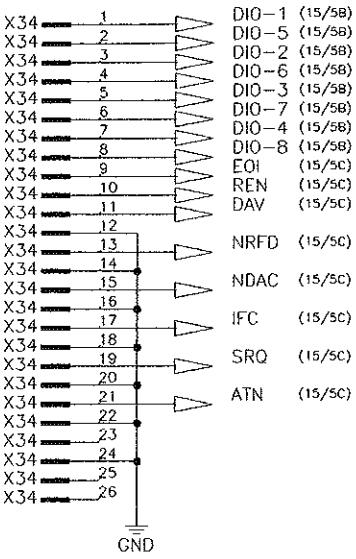
TO KNOB INTERFACE



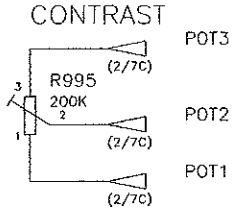
TO RS232/V24 INTERFACE



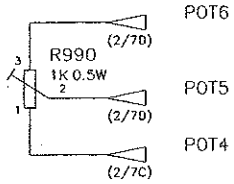
TO IEC625-BUS

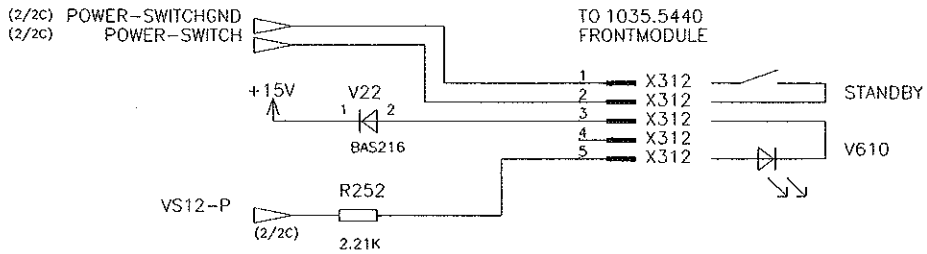


CONTRAST



BRIGHTNESS





02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG	
				BEARB.		EI	RECHNER	
				GEPR.			CPU	
				NORM				
				PLOTT	10.6.97	WEH	TOP/TOP.2	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S	BLATT-NR. 2 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005	ERSTE Z. 1035.5440.01

● P21, P22
P23, P24

⊖ R995, R990

X31 30 — R300 N.F. — C156 100P N.F. — 1K — R301 N.F. — MODCTRL-OUT (6/7B)
X31 31 — C157 100P N.F. — 1K — R177 — MODCTRL-IN (6/2C)
X31 32 — C167 100P N.F. — 1K — R101 — SWEEP-STOP (6/2B)
X31 33 — C2 10P — 1K — R100 — MARKER (6/8C)
X31 34 — C1 10P — 1K — BLANK (6/8C)

X31 35 — R302 N.F. — C158 100P N.F. — 1K — R303 N.F. — AC-FAIL (11/5B,17/3D)
X31 36 — C159 100P N.F. — 100 — SYSRESET (4/2B)
X31 37 — C160 47P N.F. — SERBUS-SYNC (17/8C)
X31 38 — C161 47P N.F. — SERBUS-INT (17/8C)
X31 39 — C162 47P N.F. — SERBUS-DAT (17/8C)
X31 40 — C163 47P N.F. — SERBUS-CLK (17/8C)
X31 41 — R305 N.F. — C164 100P N.F. — 100 — AUX-TRIG (17/3C)

X31 42 — R304 N.F. — C165 100P N.F. — 100 — TRIGGER (17/3C)
X31 43 — C168 100P N.F. — DIAG-15V (13/2C)
X31 44 — C170 100P N.F. — DIAG-5V (13/2C)
X31 45 — C169 100P N.F. — X-AXIS (13/2C,14/7C)
X31 46 — UBEXT (7/1A)
X31 47 — R290 N.F. — C145 100P N.F. — 1K — DONE (16/6D)
X31 48 / X31 49 — R248 N.F. — C63 100P N.F. — 1K — NBUSY1 (6/2C)
X31 50 — R289 N.F. — C144 100P N.F. — 1K — NBUSY2 (6/2C)

TO KEYBOARD X37

X37 1 RET0 (11,
X37 2 RET1 (11,
X37 3 RET2 (11,
X37 4 RET3 (11,
X37 5 RET4 (11,
X37 6 RET5 (11,
X37 7 RET6 (11,
X37 8 SCAN0 (12,
X37 9 SCAN1 (12,
X37 10 SCAN2 (12,
X37 11 SCAN3 (12,
X37 12 SCAN4 (12,
X37 13 SCAN5 (12,

TO KEYBOARD X36

X36 1
X36 2
X36 3
X36 4
X36 5
X36 6
X36 7
X36 8
X36 9
X36 10
X36 11
X36 12
X36 13
R2 1K

TO KNOB INTERFACE

X35 1
X35 2 VA15-P (2,
X35 3 POT1 (2,
X35 4 VA15-P (2,
X35 5 POT2 (2,
X35 6 VA15-P (2,
X35 7 POT3 (2,
X35 8 VA15-P (2,
X35 9 KNOB1 (12
X35 10 POT4 (2,
X35 11 KNOB2 (12
X35 12 POT5 (2,
X35 13 LCD-D3 (18
X35 14 POT6 (2,
X35 15 LCD-CS (16
X35 16 LAMPOFF (6,
X35 17 LCD-CP1 (18
+5V
X35 18
X35 19 LCD-CP2 (18
X35 20
X35 21
X35 22 LCD-D0 (18
X35 23
X35 24 LCD-D1 (18
X35 25
X35 26 LCD-D2 (16

CONTRAST

R995 200K
3 — POT3 (2/7C)
2 — POT2 (2/7C)
1 — POT1 (2/7C)

BRIGHTNESS

R990 1K 0.5W
3 — POT6 (2/7D)
2 — POT5 (2/7D)
1 — POT4 (2/7C)

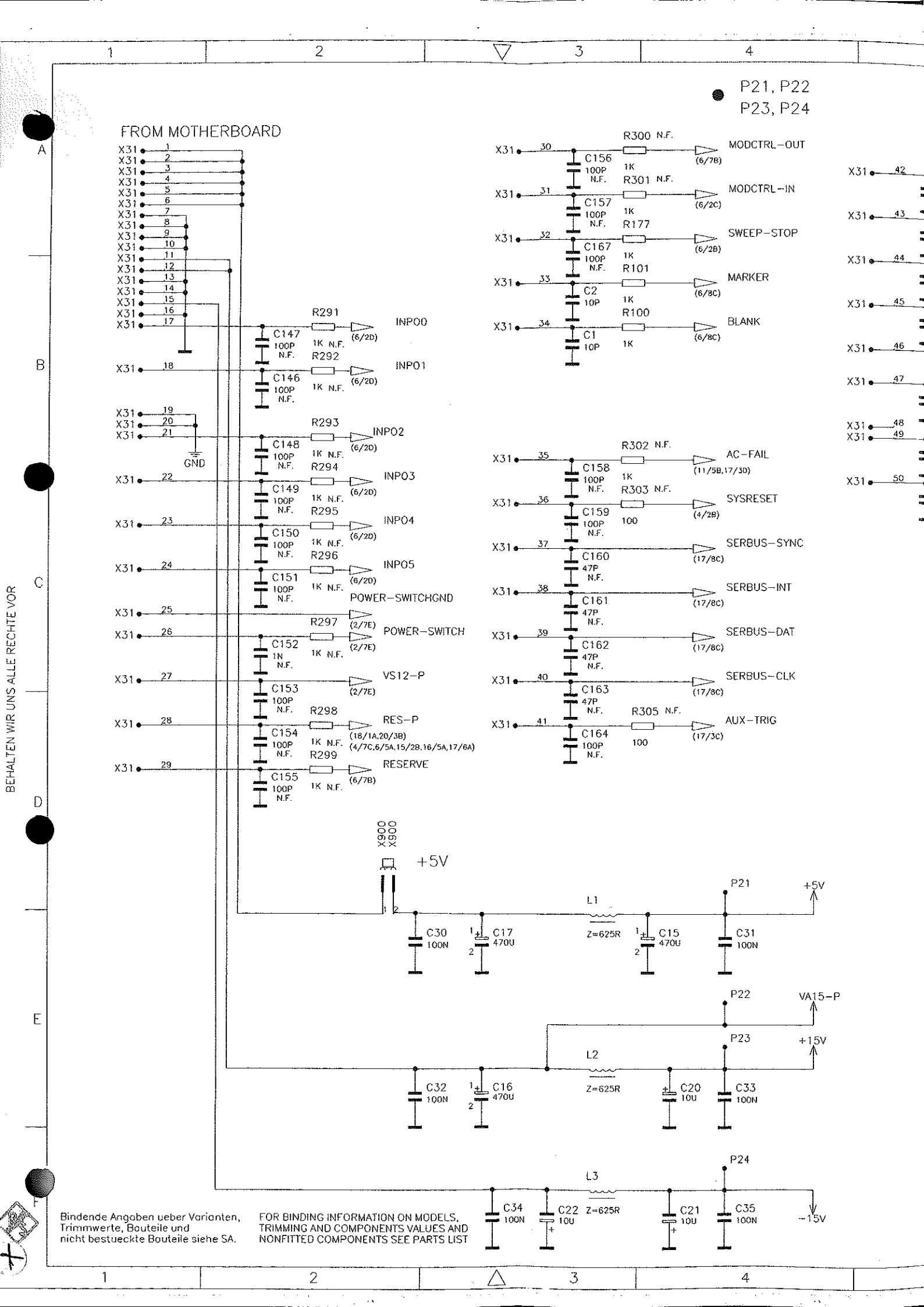
(2/2C) POWER-SWITCHGND
(2/2C) POWER-SWITCH

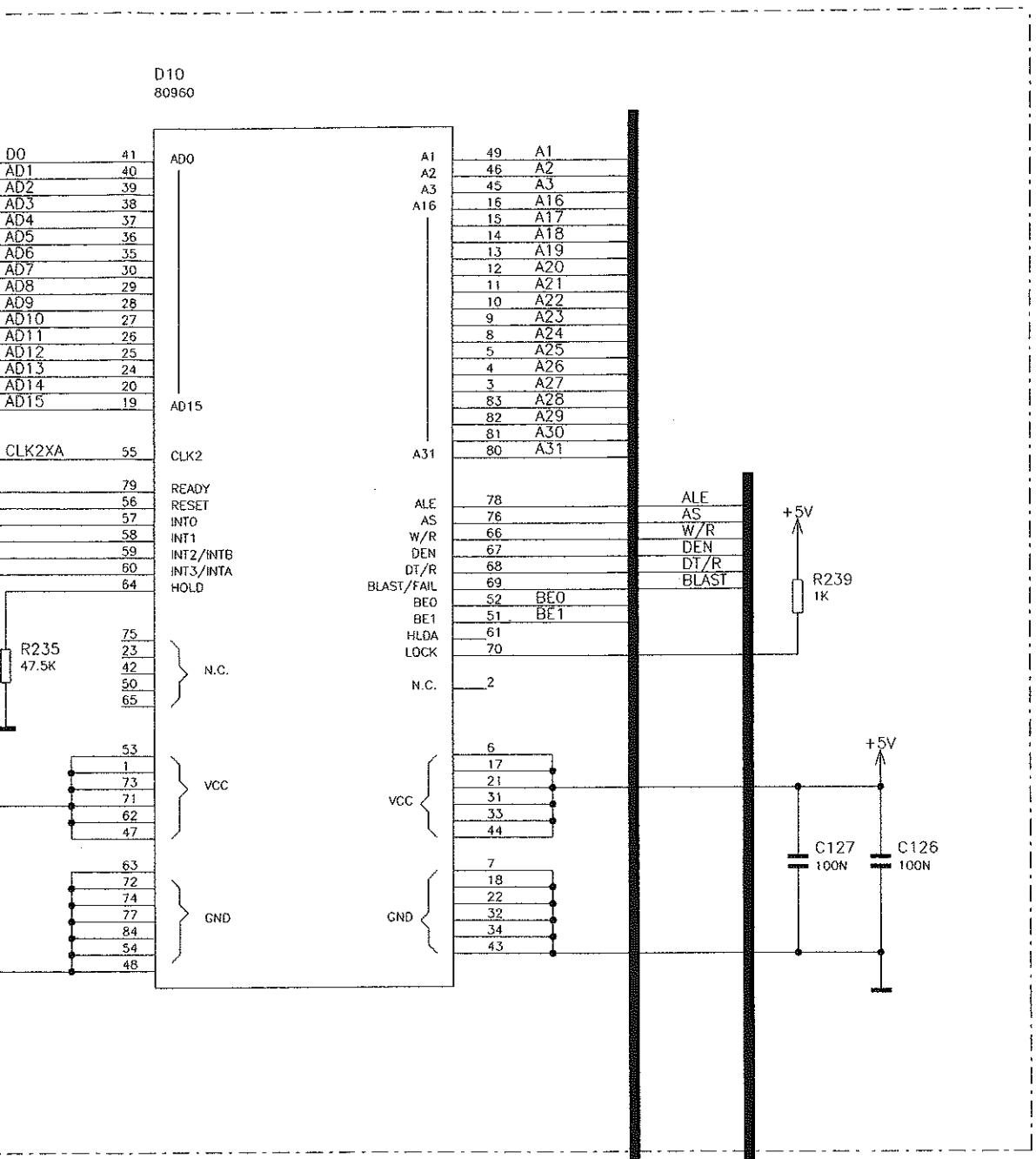
+15V — V22 BAS216

VS12-P — R252 2.21K (2/2C)

P21 — +5V — L1 Z=625R — C17 470U — C15 470U — C31 100N
P22 — VA15-P
P23 — +15V — L2 Z=625R — C16 470U — C20 10U — C33 100N
P24 — L3 Z=625R — C34 100N — C22 10U — C21 10U — C35 100N — -15V

02		27.05.97	EI	MENP	DATUM	NAME	BENE
				BEARB.		EI	RE
				GEPR.			CPU
				NORM			
				PLOTT	10.6.97	WEH	TOP/T
							ZEICH
01		24.06.96	DR	ROHDE&SCHWARZ			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP		REG.I.V


P21, P22
P23, P24
FROM MOTHERBOARD
X31 1
X31 2
X31 3
X31 4
X31 5
X31 6
X31 7
X31 8
X31 9
X31 10
X31 11
X31 12
X31 13
X31 14
X31 15
X31 16
X31 17
X31 18
X31 19
X31 20
X31 21
X31 22
X31 23
X31 24
X31 25
X31 26
X31 27
X31 28
X31 29
GND
R291
C147 100P N.F.
1K N.F.
INPO0
(6/2D)
R292
C146 100P N.F.
INPO1
R293
C148 100P N.F.
INPO2
R294
C149 100P N.F.
INPO3
R295
C150 100P N.F.
INPO4
R296
C151 100P N.F.
INPO5
POWER-SWITCHGND
R297
C152 1N N.F.
POWER-SWITCH
(2/7E)
C153 100P N.F.
VS12-P
R298
C154 100P N.F.
RES-P
(18/1A,20/3B)
(4/7C,6/5A,15/2B,16/5A,17/6A)
R299
C155 100P N.F.
RESERVE
X31 30
X31 31
X31 32
X31 33
X31 34
R300 N.F.
C156 100P N.F.
1K
MODCTRL-OUT
(6/7B)
R301 N.F.
C157 100P N.F.
MODCTRL-IN
(6/2C)
R177
C167 100P N.F.
SWEEP-STOP
(6/2B)
R101
C2 10P
MARKER
(6/8C)
R100
C1 10P
BLANK
X31 35
X31 36
X31 37
X31 38
X31 39
X31 40
X31 41
R302 N.F.
C158 100P N.F.
AC-FAIL
(11/5B,17/3D)
R303 N.F.
C159 100P N.F.
100
SYSRESET
(4/2B)
C160 47P N.F.
SERBUS-SYNC
(17/8C)
C161 47P N.F.
SERBUS-INT
C162 47P N.F.
SERBUS-DAT
C163 47P N.F.
SERBUS-CLK
R305 N.F.
C164 100P N.F.
AUX-TRIG
(17/3C)
X31 42
X31 43
X31 44
X31 45
X31 46
X31 47
X31 48
X31 49
X31 50
X900
X900
+5V
L1
C30 100N
C17 470U
Z=625R
C15 470U
C31 100N
P21
+5V
P22
VA15-P
L2
C32 100N
C16 470U
Z=625R
C20 10U
C33 100N
P23
+15V
L3
C34 100N
C22 10U
Z=625R
C21 10U
C35 100N
P24
-15V
BEHALTEN WIR UNS ALLE RECHTE VOR
Bindende Angaben ueber Varianten, Trimmwerte, Bauteile und nicht bestueckte Bauteile siehe SA.
FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST



02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME			

BENENNUNG

RECHNER

CPU

TOP/TOP.3

ROHDE&SCHWARZ

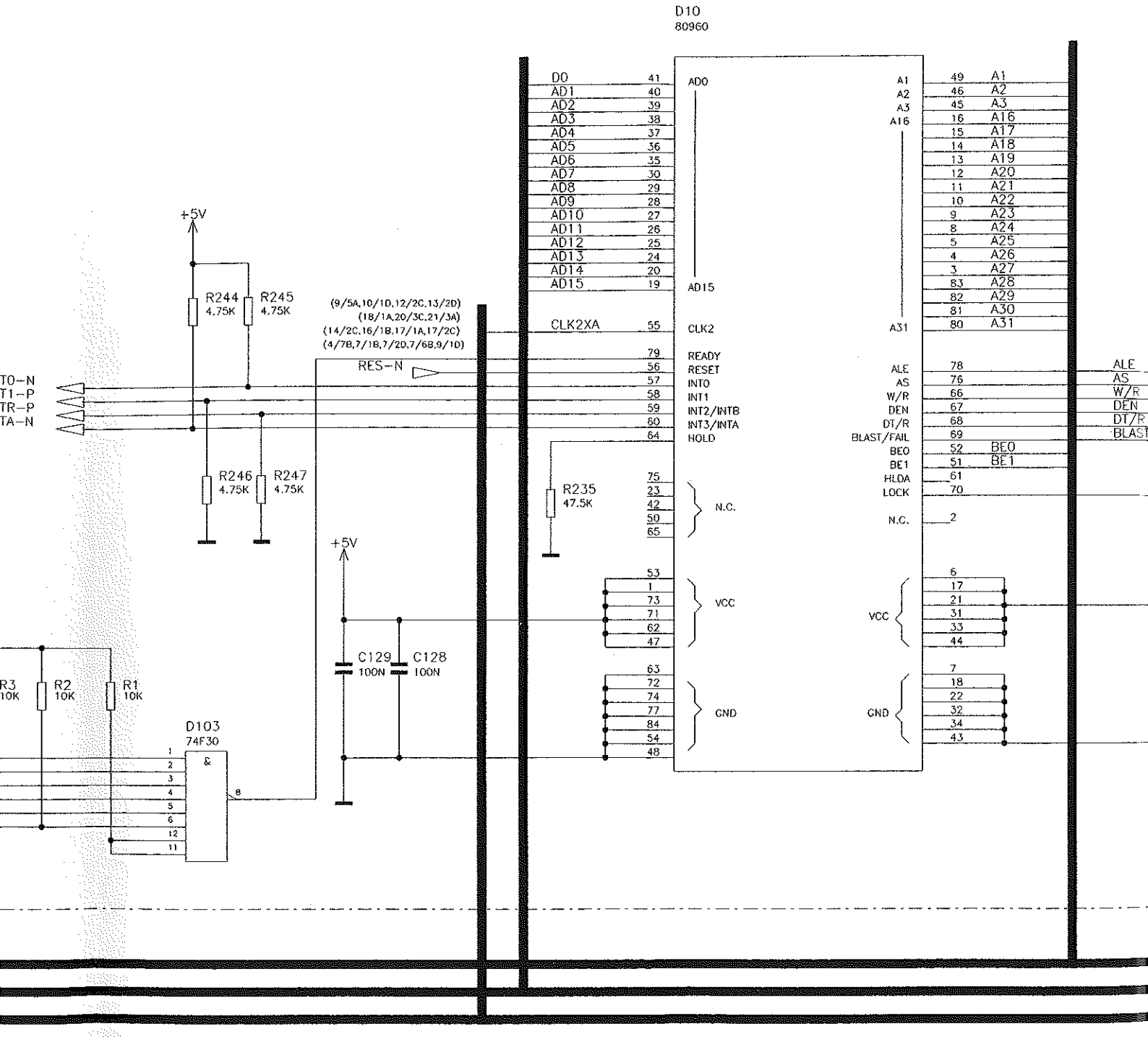
ZEICHN.-NR. 1035.7766.01 S

BLATT-NR. 3 + 47

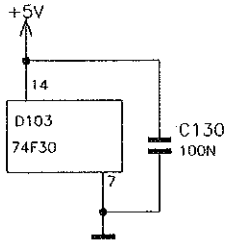
ZU GERAET SMP

REG.I.V. 1035.5005

ERSTE Z. 1035.5440.01

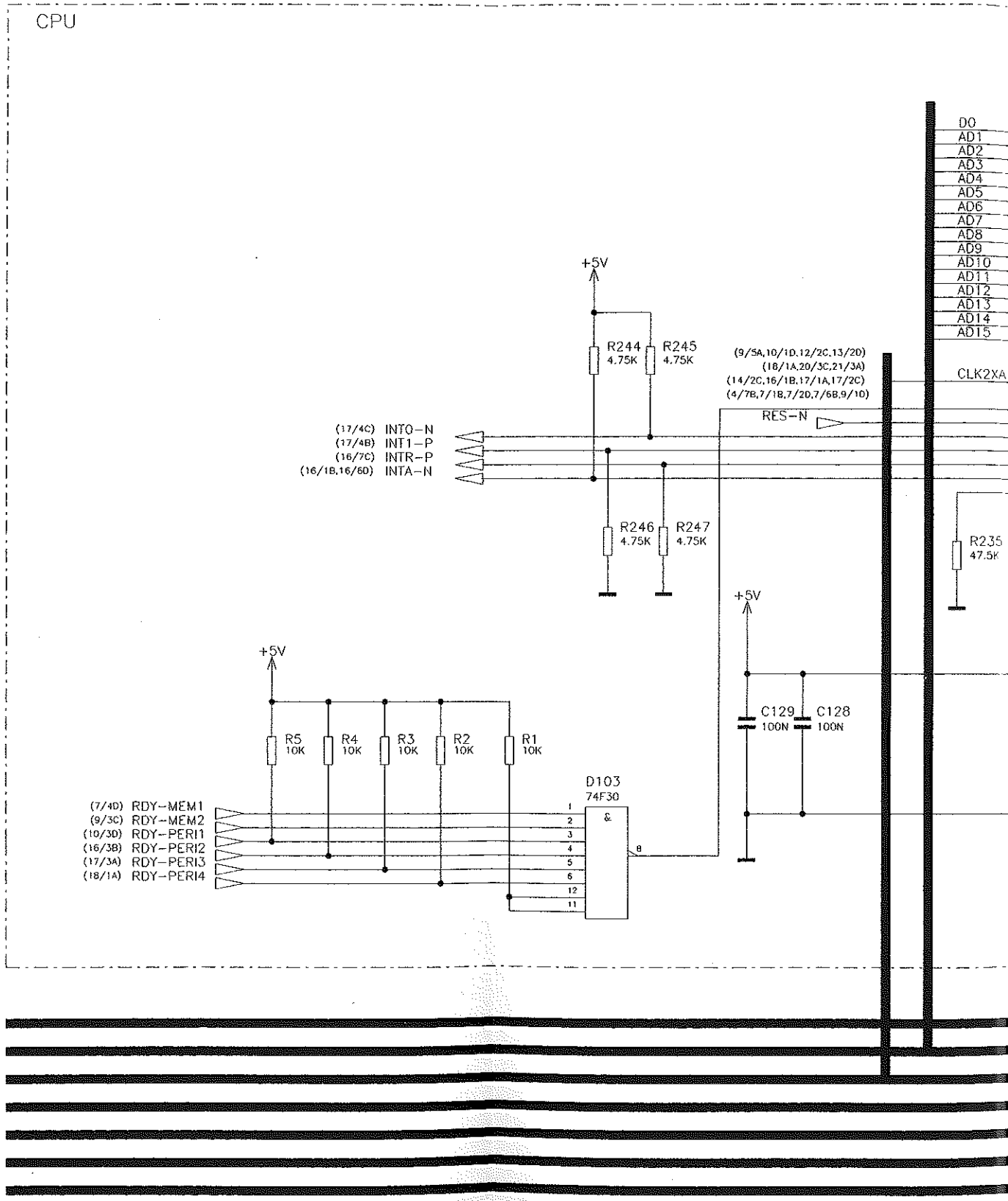


ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING



02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

BEHALTEN WIR UNS ALLE RECHTE VOR

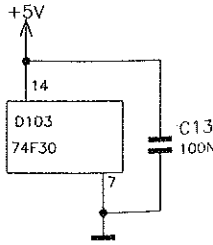


Bindende Angaben ueber Varianten,
Trimmwerte, Bauteile und
nicht bestueckte Bauteile siehe SA.

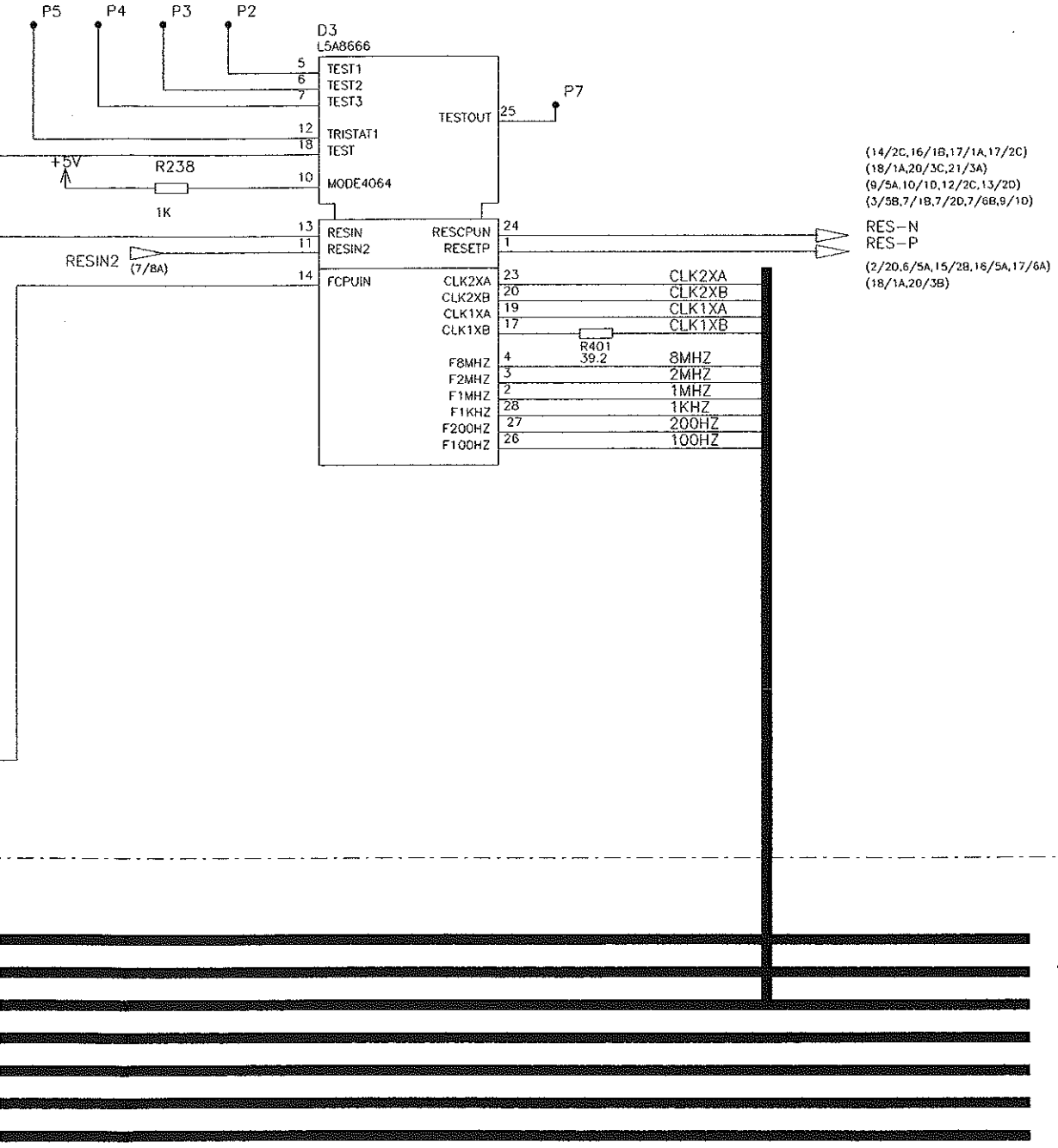
FOR BINDING INFORMATION ON MODELS,
TRIMMING AND COMPONENTS VALUES AND
NONFITTED COMPONENTS SEE PARTS LIST

ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE
BAUELEMENTE ERFORDERN EINE
BESONDERE HANDHABUNG.

ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES
REQUIRE A SPECIAL HANDLING



● P6, P5, P4, P3, P2 ● P7

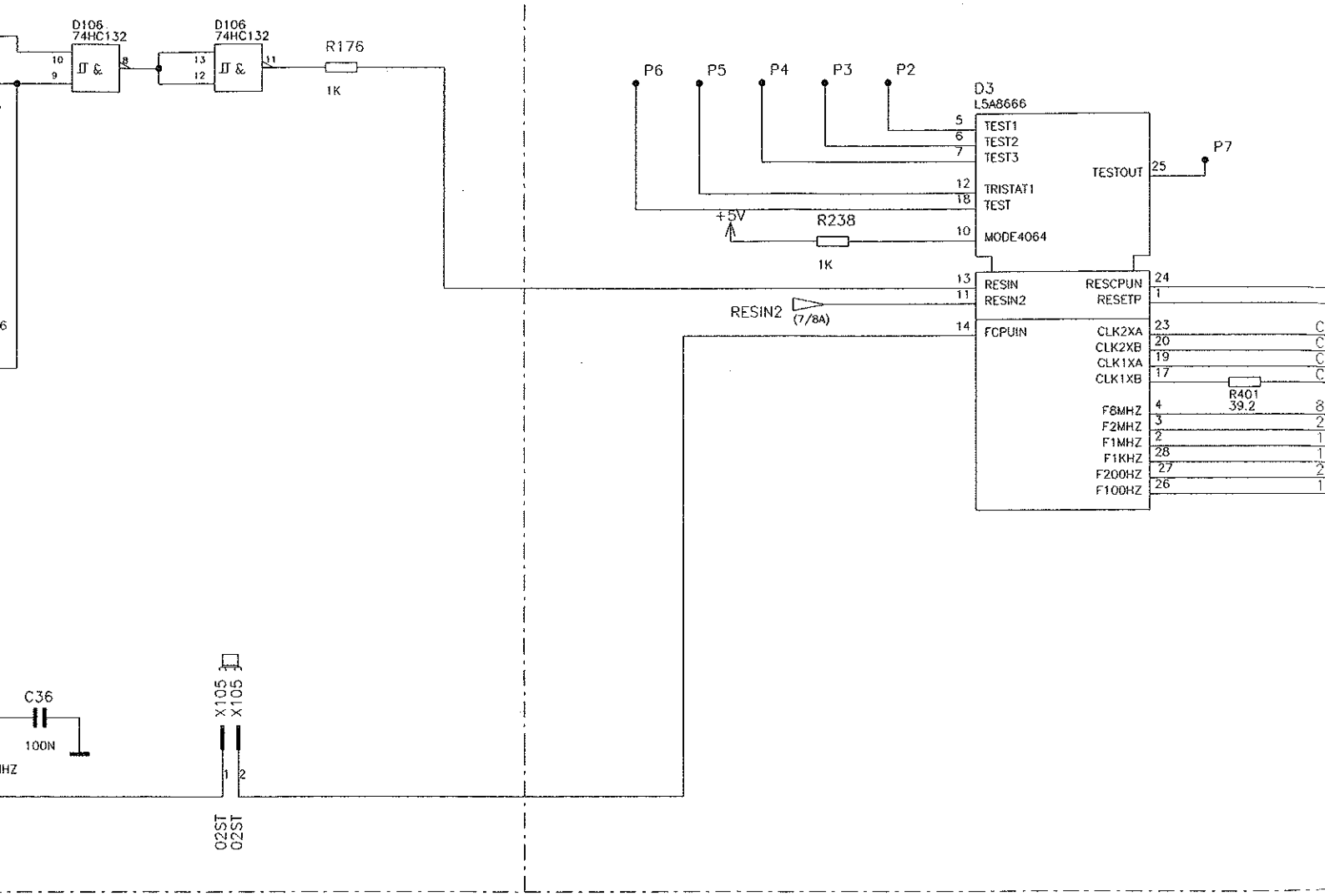


A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.4
				PLOTT	9.6.97	WEH	
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR				BLATT-NR. 4 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

● P6, P5, P4, P3, P2 ● P7

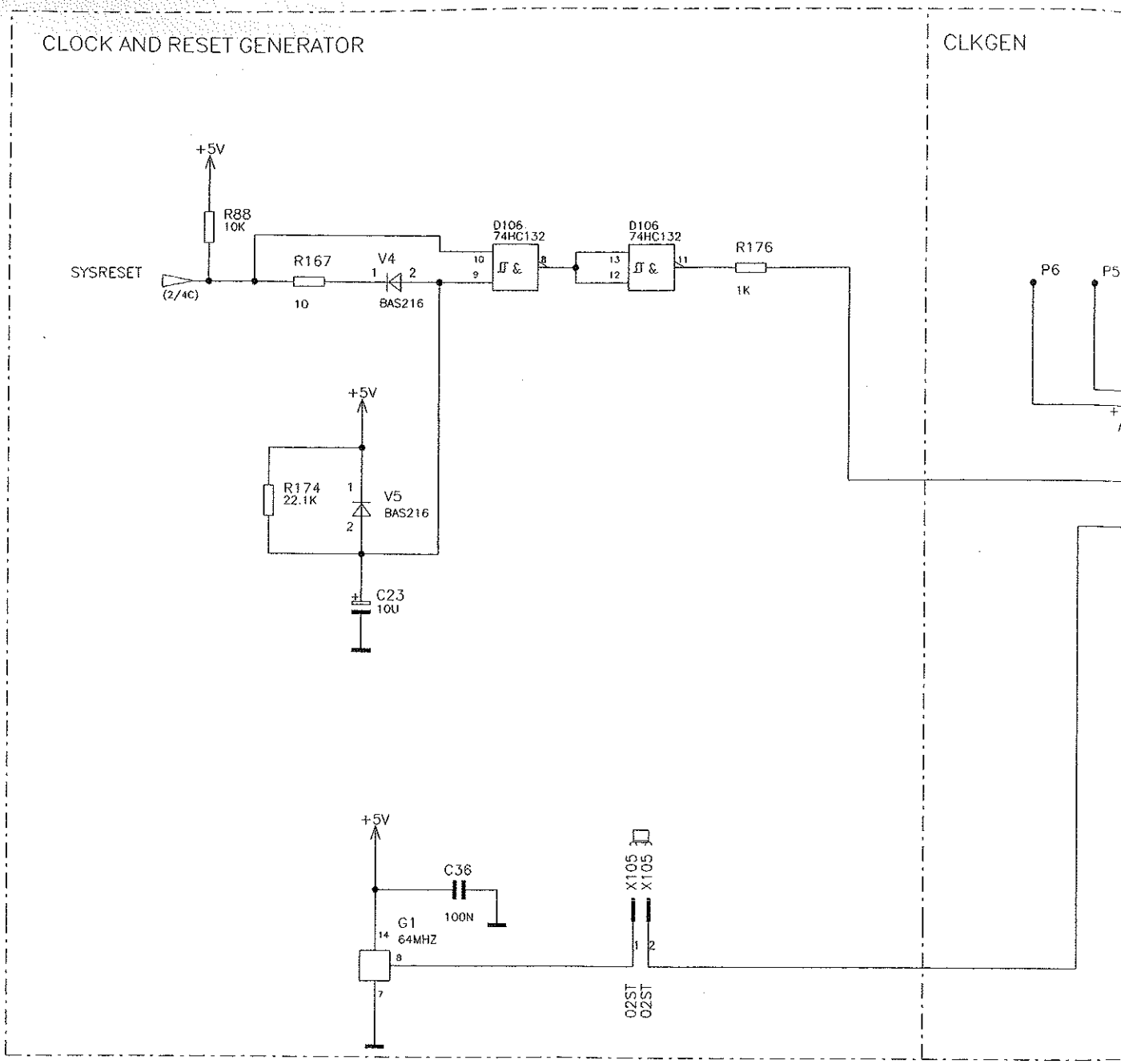
CLKGEN

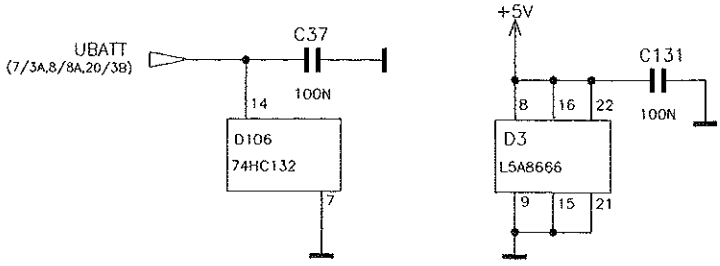


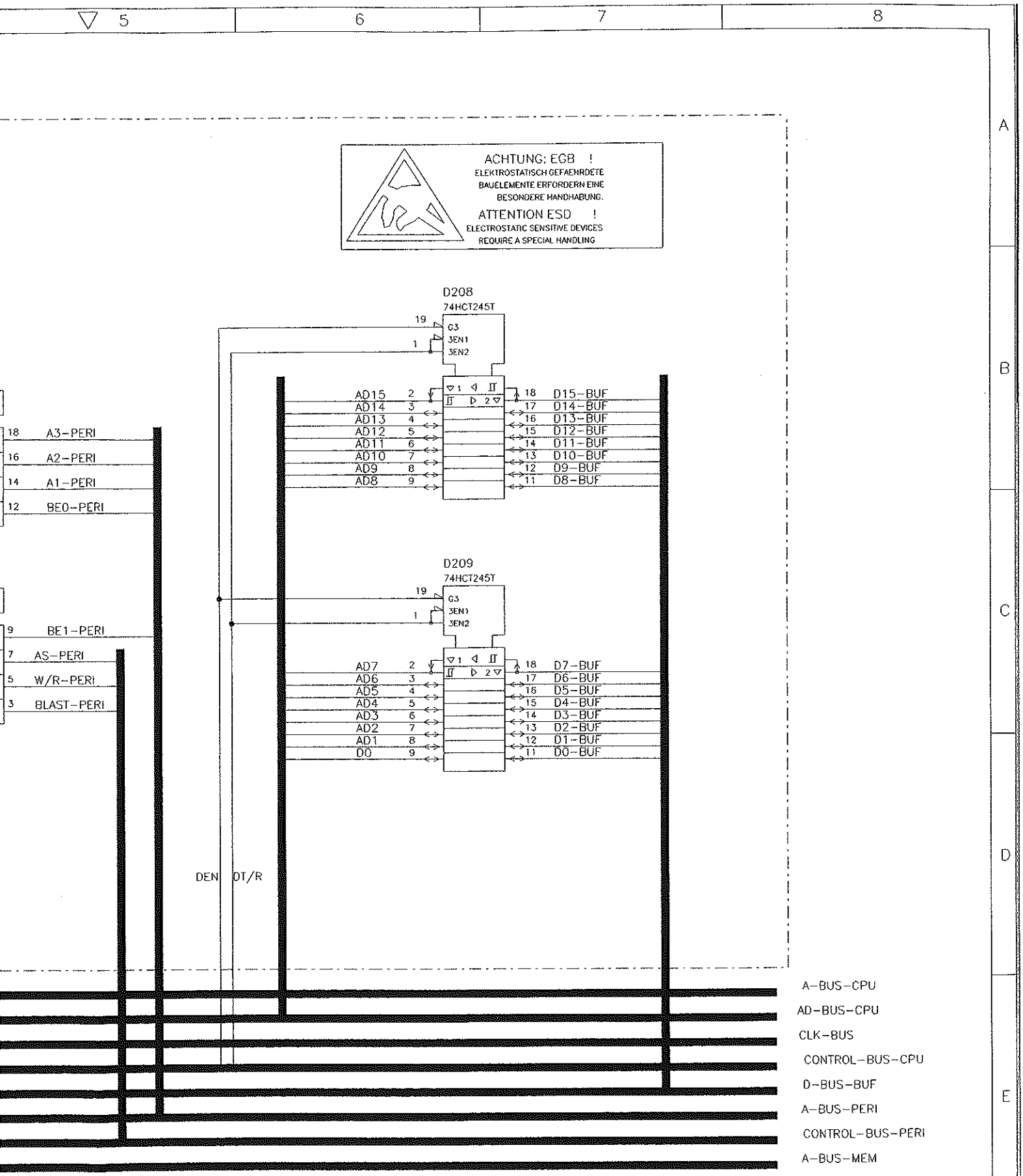
C131
100N

02		27.05.97	EI	MENP	DATUM	NA
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WE
01		24.06.96	DR	ROHDE&SCHWA		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR







02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME			

BENENNUNG

RECHNER

CPU

TOP/TOP.5

ROHDE&SCHWARZ

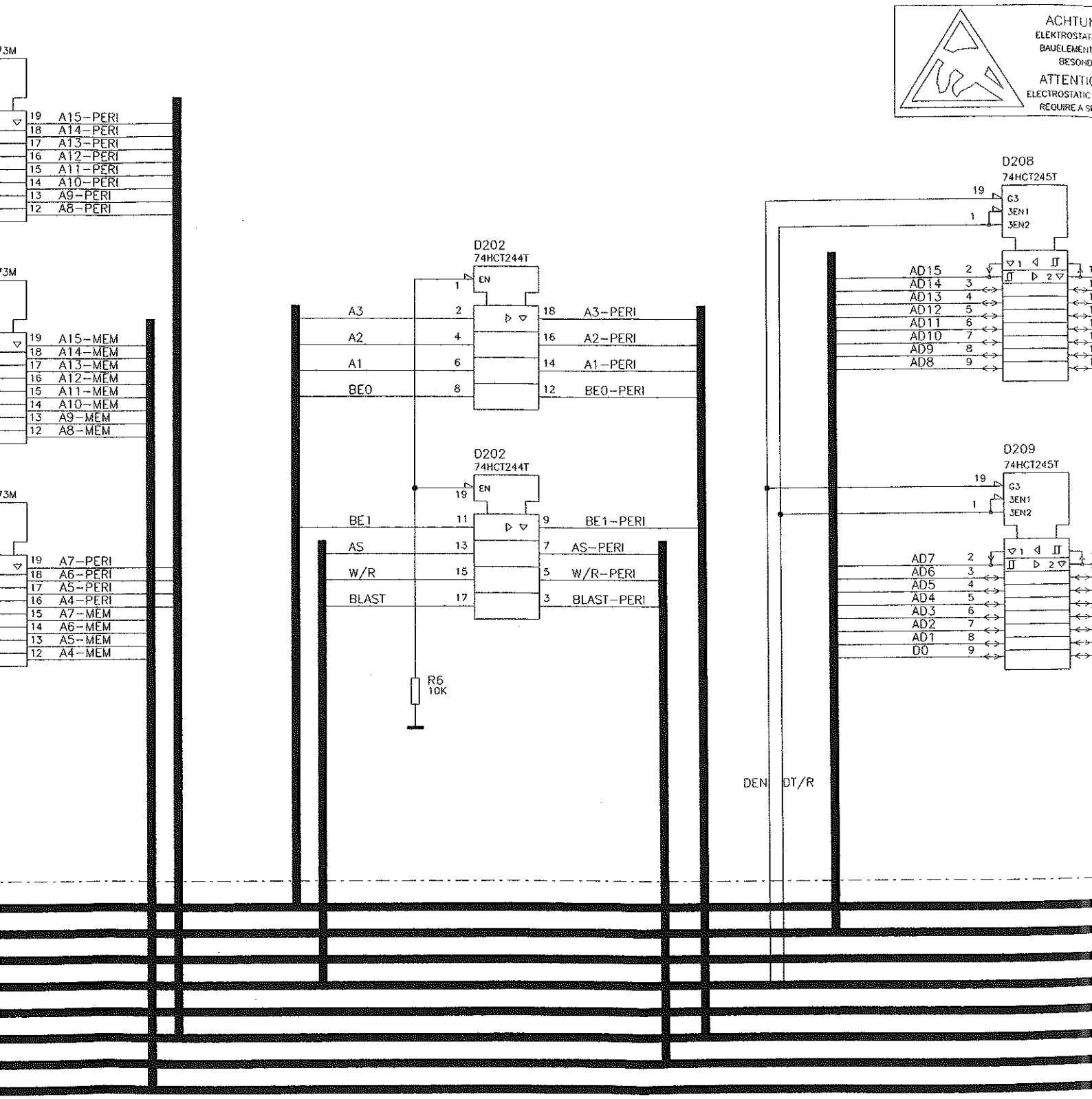
ZEICHN.-NR. 1035.7766.01 S

BLATT-NR. 5 + 47

ZU GERAET SMP

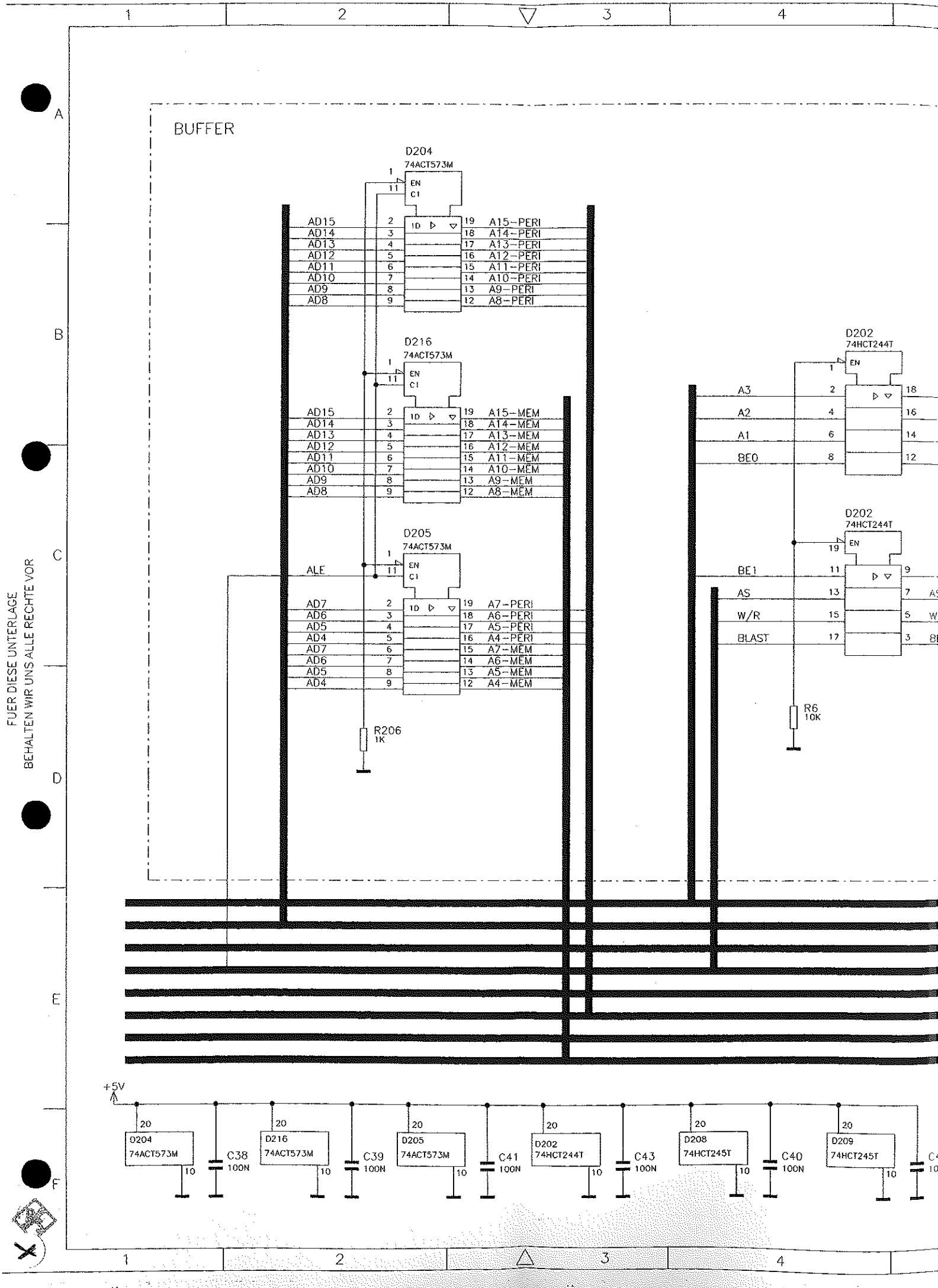
REG.I.V. 1035.5005

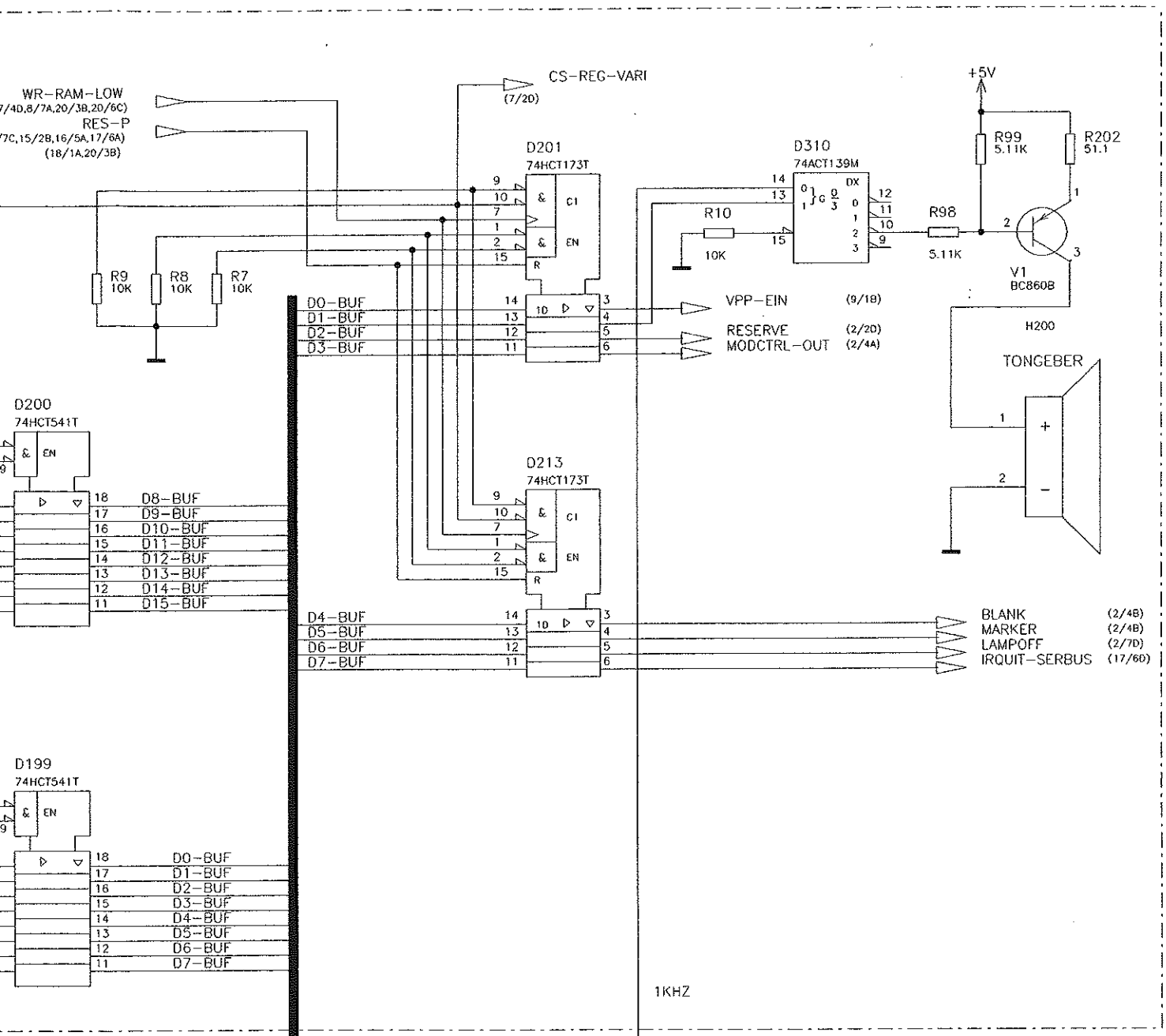
ERSTE Z. 1035.5440.01



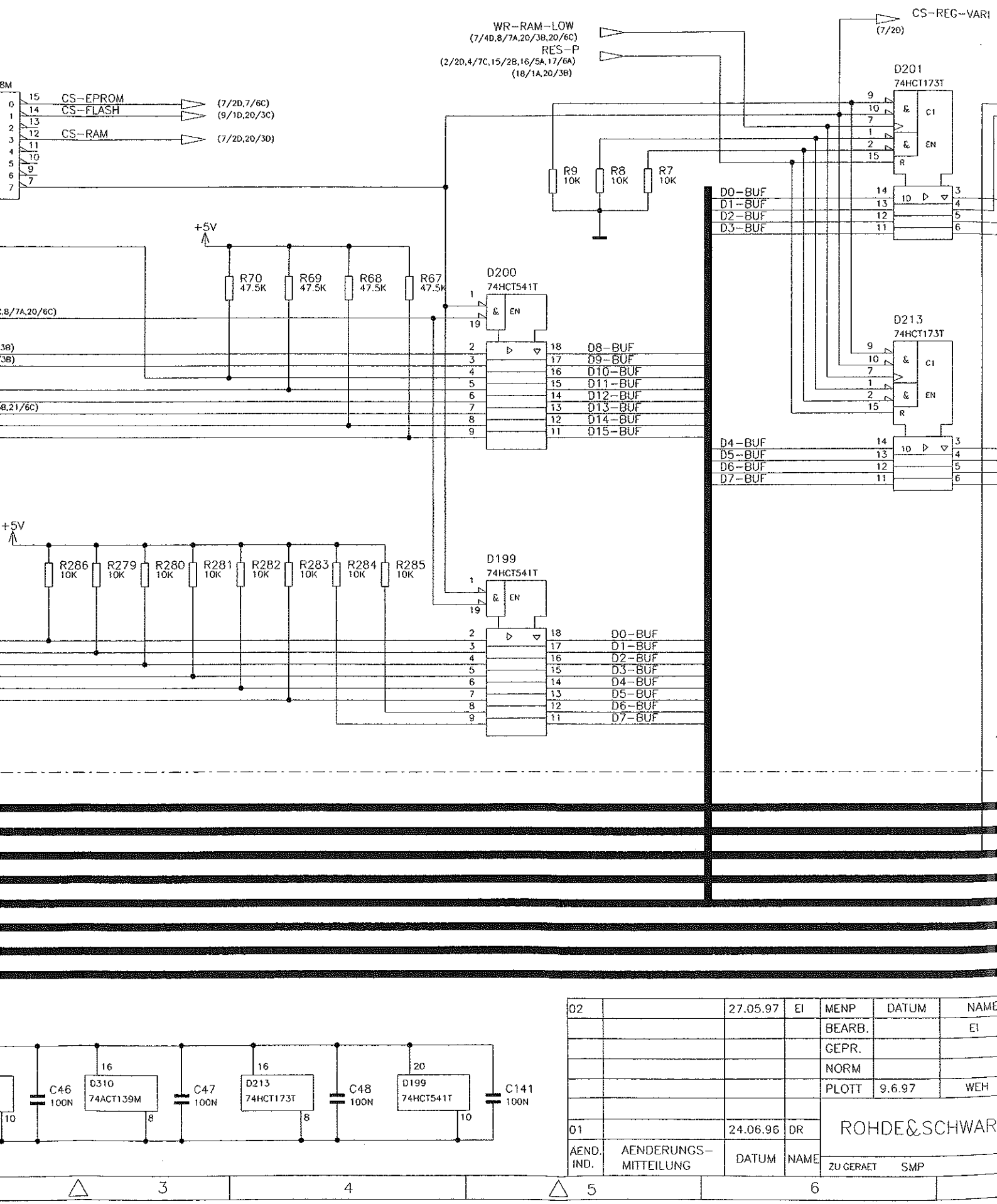
02		27.05.97	EI	MENP	DATUM	N
				BEARB.		E
				GEPR.		
				NORM		
				PLOTT	9.6.97	W
01		24.06.96	DR	ROHDE&SCHW		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR





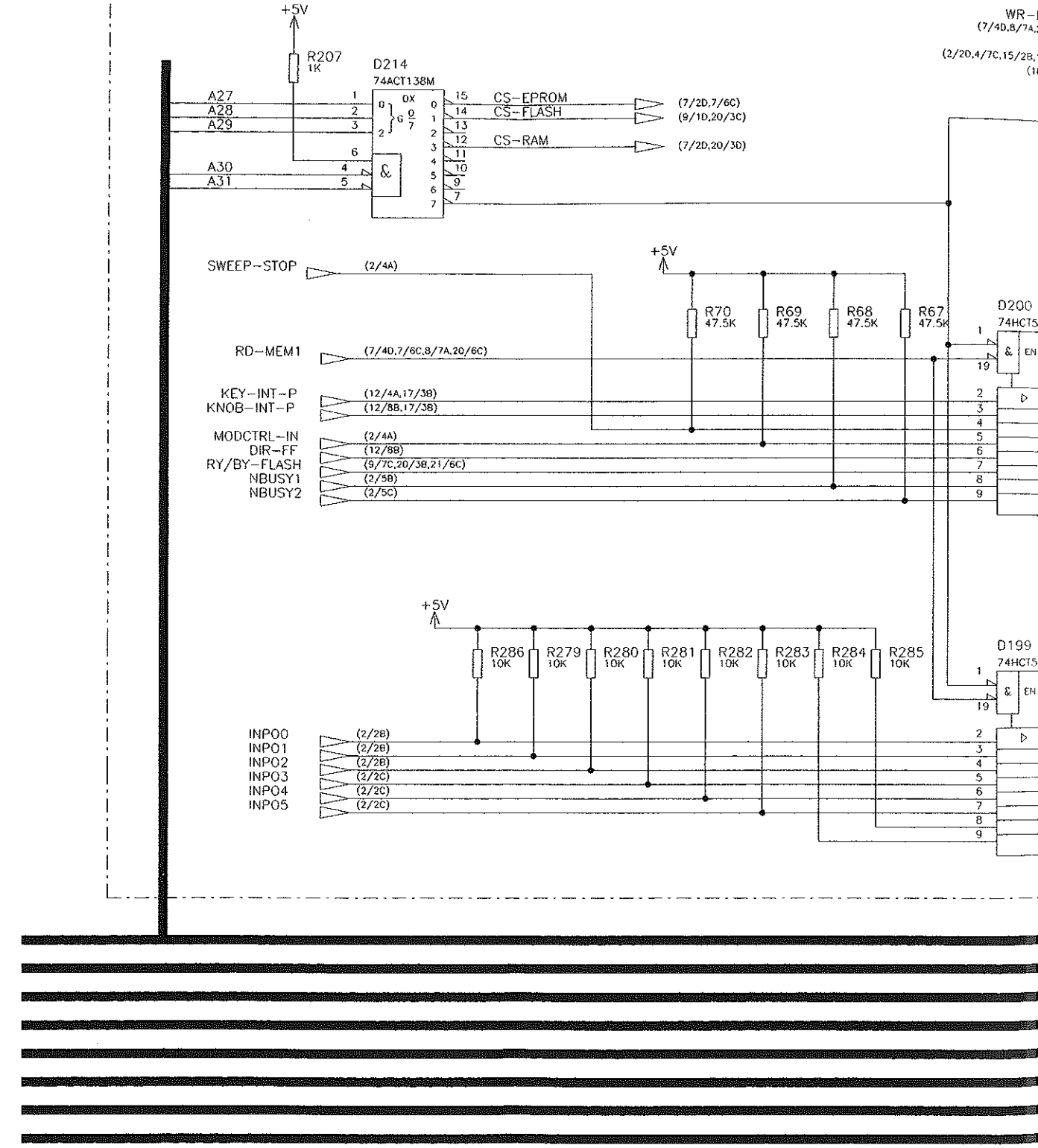
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.6
				PLOTT	9.6.97	WEH	
							ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR	ROHDE&SCHWARZ			BLATT-NR. 6 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		REG.I.V. 1035.5005	ERSTE Z. 1035.5440.01

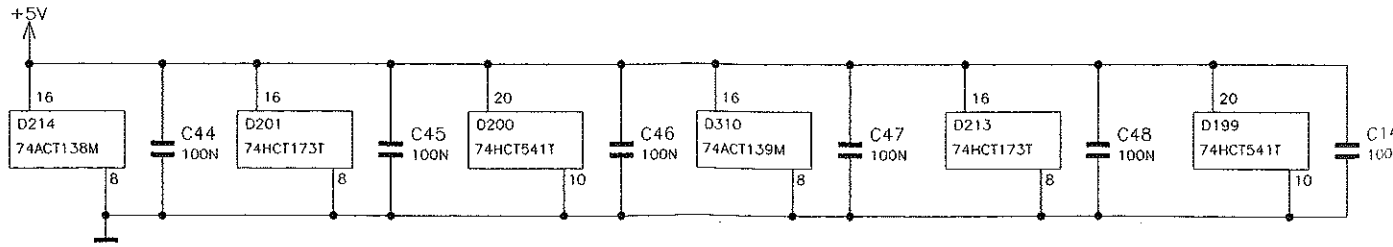


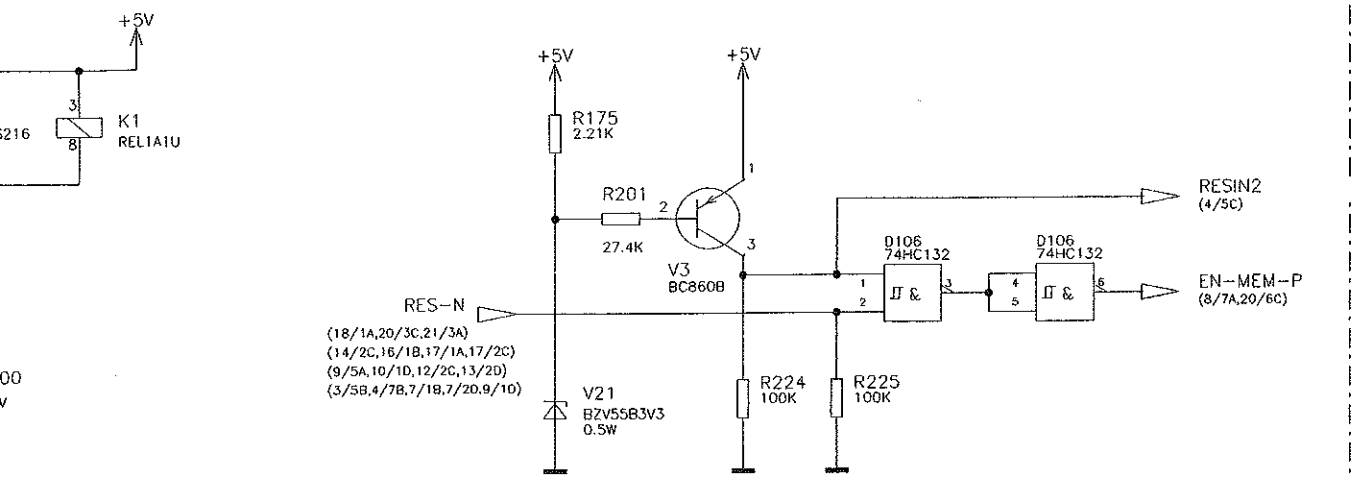
02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWAR		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

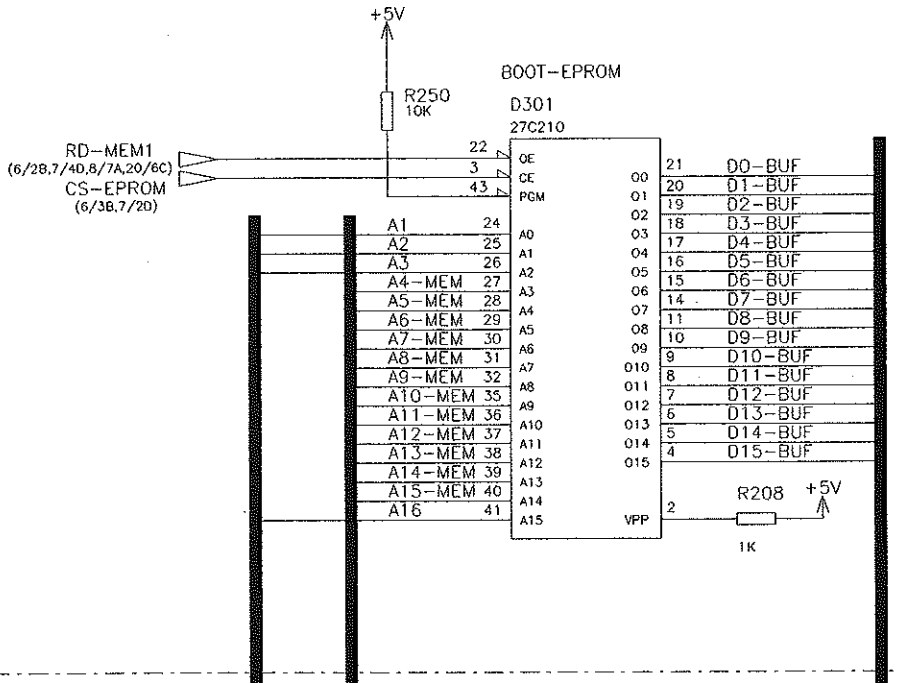
PORTREGISTER







BOOTEPROM



A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.7
				PLOTT	10.6.97	WEH	
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR				BLATT-NR. 7 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

P1

V9
BZV55B5V1
0.5W

V7
BAS216

P1

UBATT
(4/1F,8/8A,20/3B)

X300
X300

V14
HSMS2800

R203
221

K1
REL1A1U

UBATT-TST
(13/2C)

R222
39.2K

TST-BATT
(13/4D)

V6
BAS216

+5V

K1
REL1A1U

O2

G300
3.4V

O1

+5V

R175
2.21K

R201
27.4K

V3
BC860B

RES-N
(18/1A,20/3C,21/3A)
(14/2C,16/1B,17/1A,17/2C)
(9/5A,10/1D,12/2C,13/2D)
(3/5B,4/7B,7/1B,7/2D,9/1D)

V21
BZV55B3V3
0.5W

R224
100K

+5V

D299
74ACT138M

R71 47.5K
R72 47.5K
R236 47.5K
R237 47.5K

CS-RAM0 (8/7A,20/6C)
CS-RAM1 (8/7A)
CS-RAM2 (8/7A)
CS-RAM3 (8/7A)
CS-RAM4 (20/3D)
CS-RAM5 (20/3D)
CS-RAM6 (20/3D)
CS-RAM7 (20/3D)

+5V

D300
85C220

R73 47.5K
R74 47.5K
R75 47.5K

RDY-MEM1 (3/2D)

WR-RAM-HIGH (8/7A,20/3B)
WR-RAM-LOW (6/5A,8/7A,20/3B,20/6C)
RD-MEM1 (6/2B,7/6C,8/7A,20/6C)

R13
10K

BOOTEPROM

+5V

R250
10K

D301
27C210

RD-MEM1
(6/2B,7/4D,8/7A,20/6C)

CS-EPROM
(6/3B,7/2D)

A1	24
A2	25
A3	26
A4-MEM	27
A5-MEM	28
A6-MEM	29
A7-MEM	30
A8-MEM	31
A9-MEM	32
A10-MEM	35
A11-MEM	36
A12-MEM	37
A13-MEM	38
A14-MEM	39
A15-MEM	40
A16	41

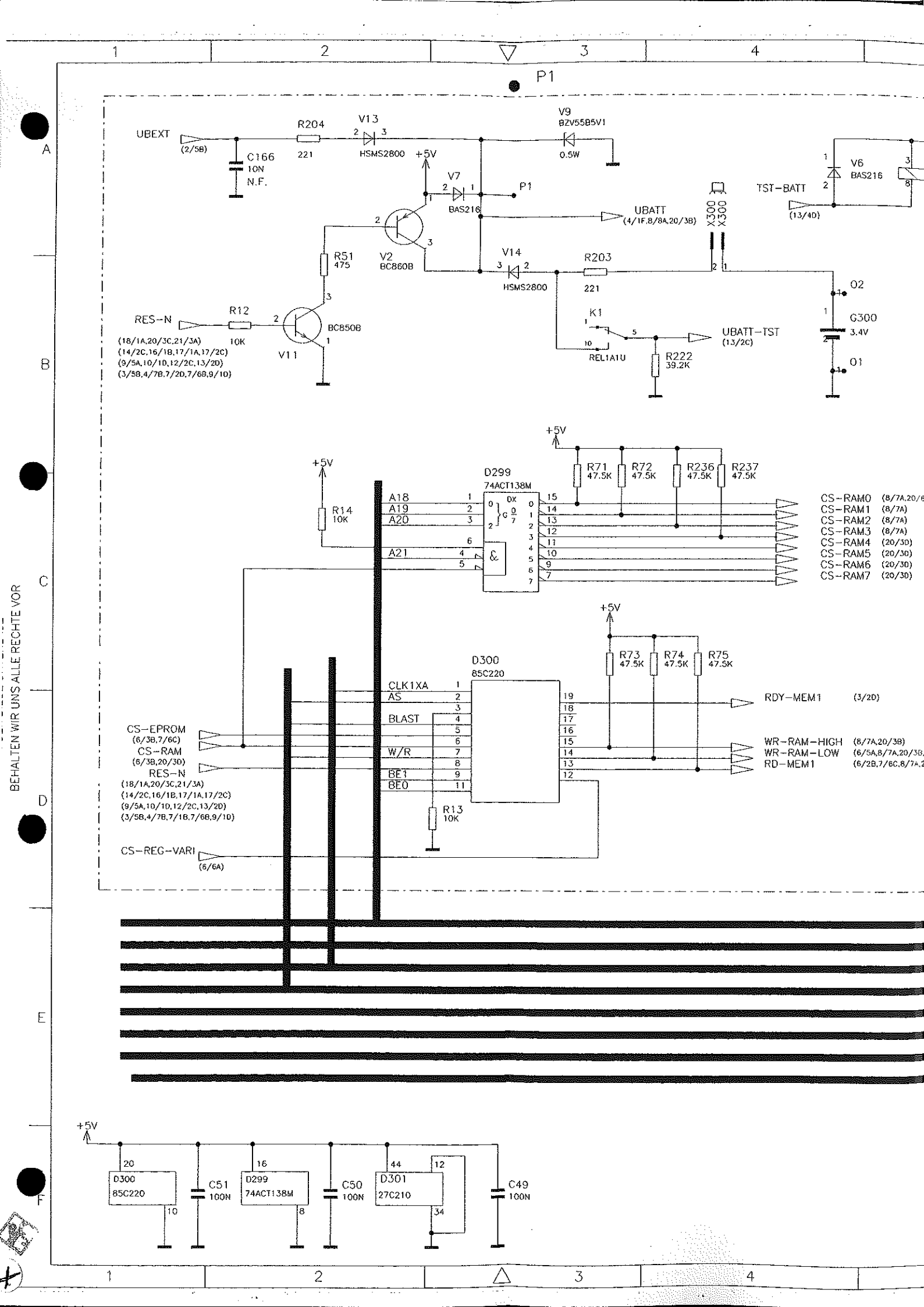
C49
100N

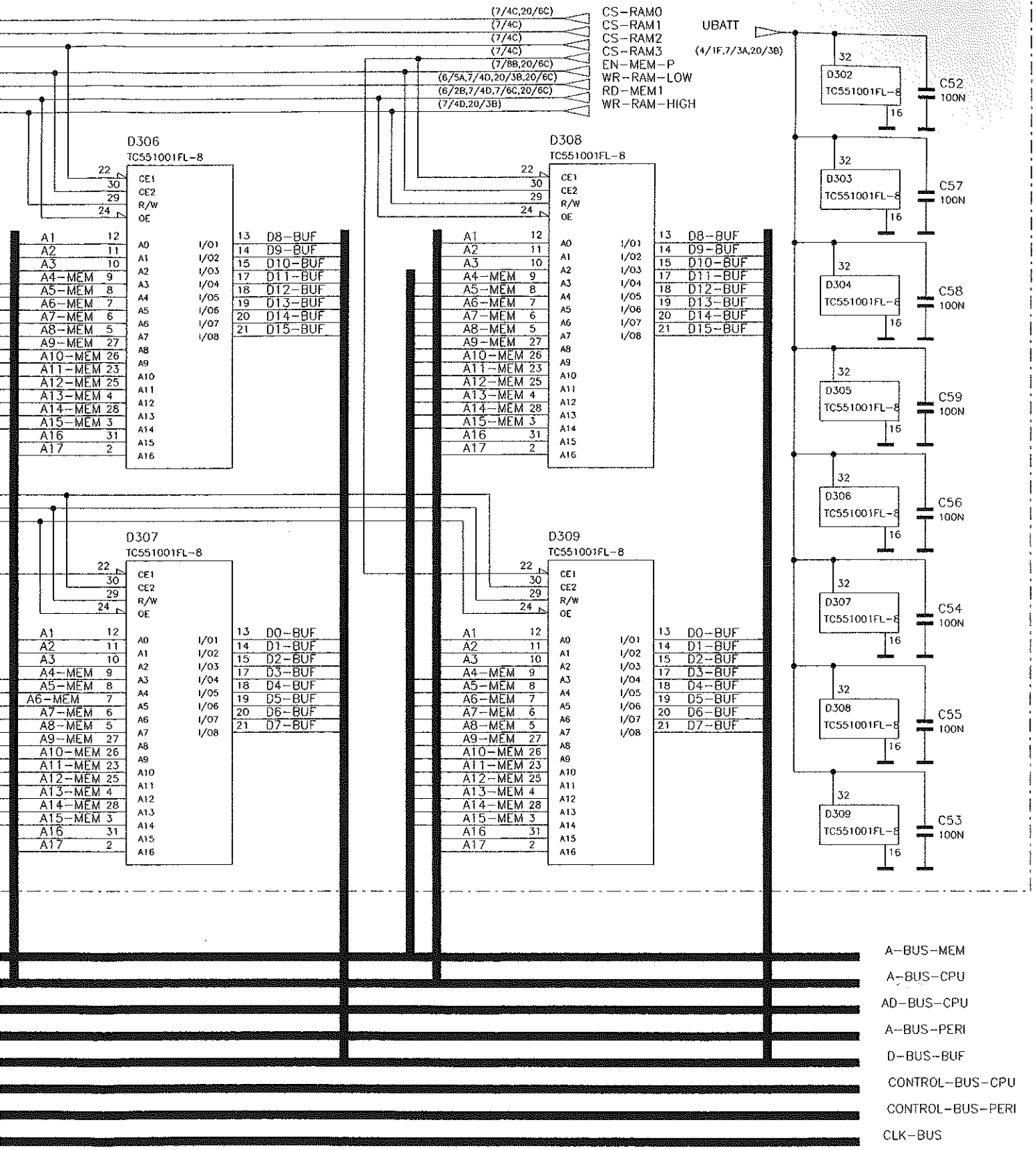
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME
02		27.05.97	EI
01		24.06.96	DR

	DATUM	NAME
MENP		
BEARB.		EI
GEPR.		
NORM		
PLOTT	10.6.97	WEH

ROHDE&SCHWAR

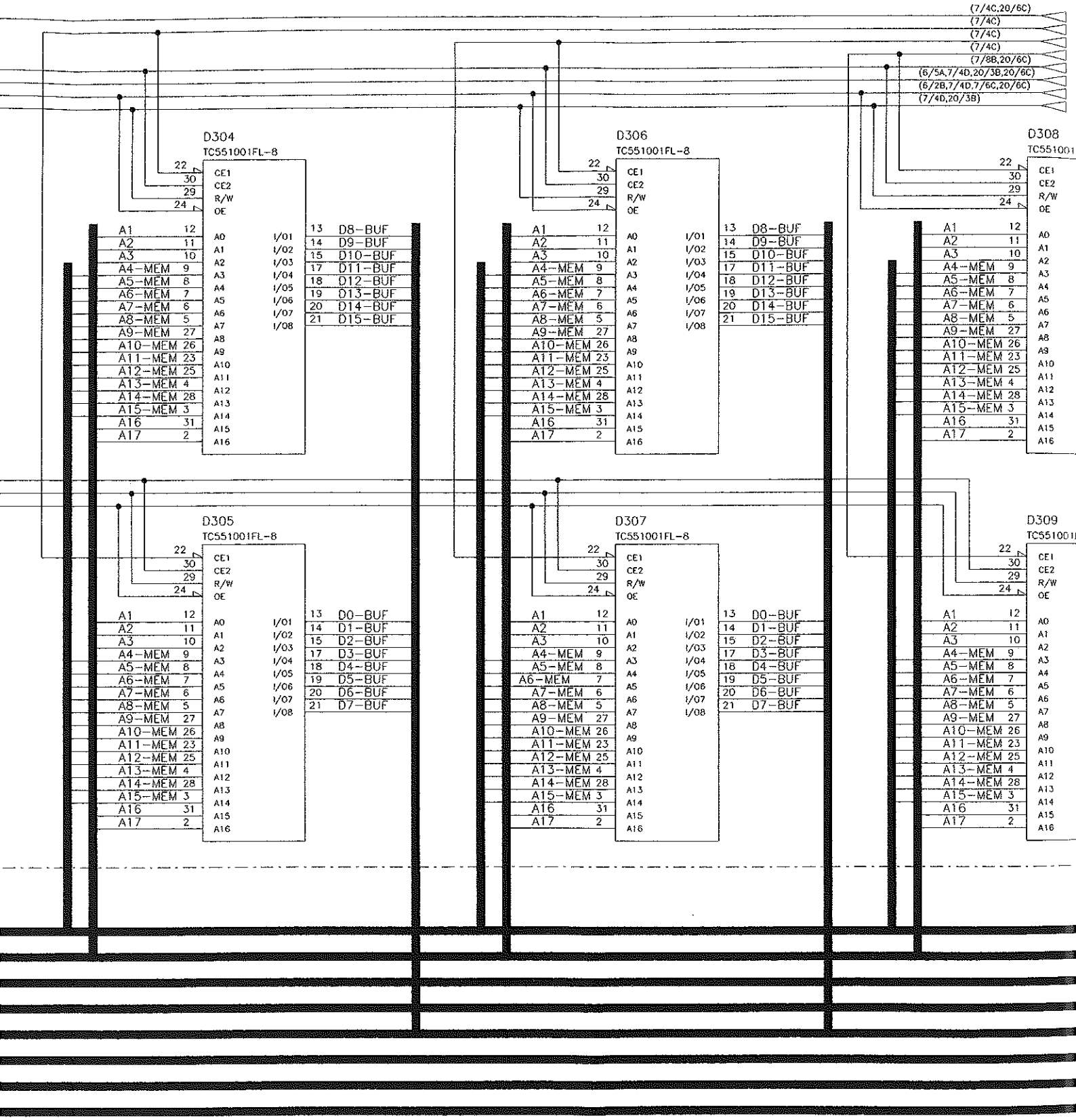
ZU GERAET SMP


P1
UBEXT
(2/5B)
C166
10N
N.F.
R204
221
V13
HSMS2800
+5V
V9
BZV55B5V1
0.5W
V7
BAS216
P1
UBATT
(4/1F,8/8A,20/3B)
X300
X300
V6
BAS216
TST-BATT
(13/4D)
V2
BC860B
R51
475
V14
HSMS2800
R203
221
O2
G300
3.4V
O1
K1
REL1A1U
UBATT-TST
(13/2C)
R222
39.2K
RES-N
(18/1A,20/3C,21/3A)
(14/2C,16/1B,17/1A,17/2C)
(9/5A,10/1D,12/2C,13/2D)
(3/5B,4/7B,7/2D,7/6B,9/10)
R12
10K
V11
BC850B
+5V
R14
10K
D299
74ACT138M
A18
A19
A20
A21
R71
47.5K
R72
47.5K
R236
47.5K
R237
47.5K
CS-RAM0 (8/7A,20/6
CS-RAM1 (8/7A)
CS-RAM2 (8/7A)
CS-RAM3 (8/7A)
CS-RAM4 (20/3D)
CS-RAM5 (20/3D)
CS-RAM6 (20/3D)
CS-RAM7 (20/3D)
+5V
D300
85C220
CLK1XA
AS
BLAST
W/R
BE1
BE0
R73
47.5K
R74
47.5K
R75
47.5K
RDY-MEM1 (3/2D)
WR-RAM-HIGH (8/7A,20/3B)
WR-RAM-LOW (6/5A,8/7A,20/3D,
RD-MEM1 (6/2B,7/6C,8/7A,2
CS-EPROM
(6/3B,7/6C)
CS-RAM
(6/3B,20/3D)
RES-N
(18/1A,20/3C,21/3A)
(14/2C,16/1B,17/1A,17/2C)
(9/5A,10/1D,12/2C,13/2D)
(3/5B,4/7B,7/1B,7/6B,9/10)
R13
10K
CS-REG-VARI
(6/6A)
+5V
D300
85C220
C51
100N
D299
74ACT138M
C50
100N
D301
27C210
C49
100N
BEHALTEN WIR UNS ALLE RECHTE VOR

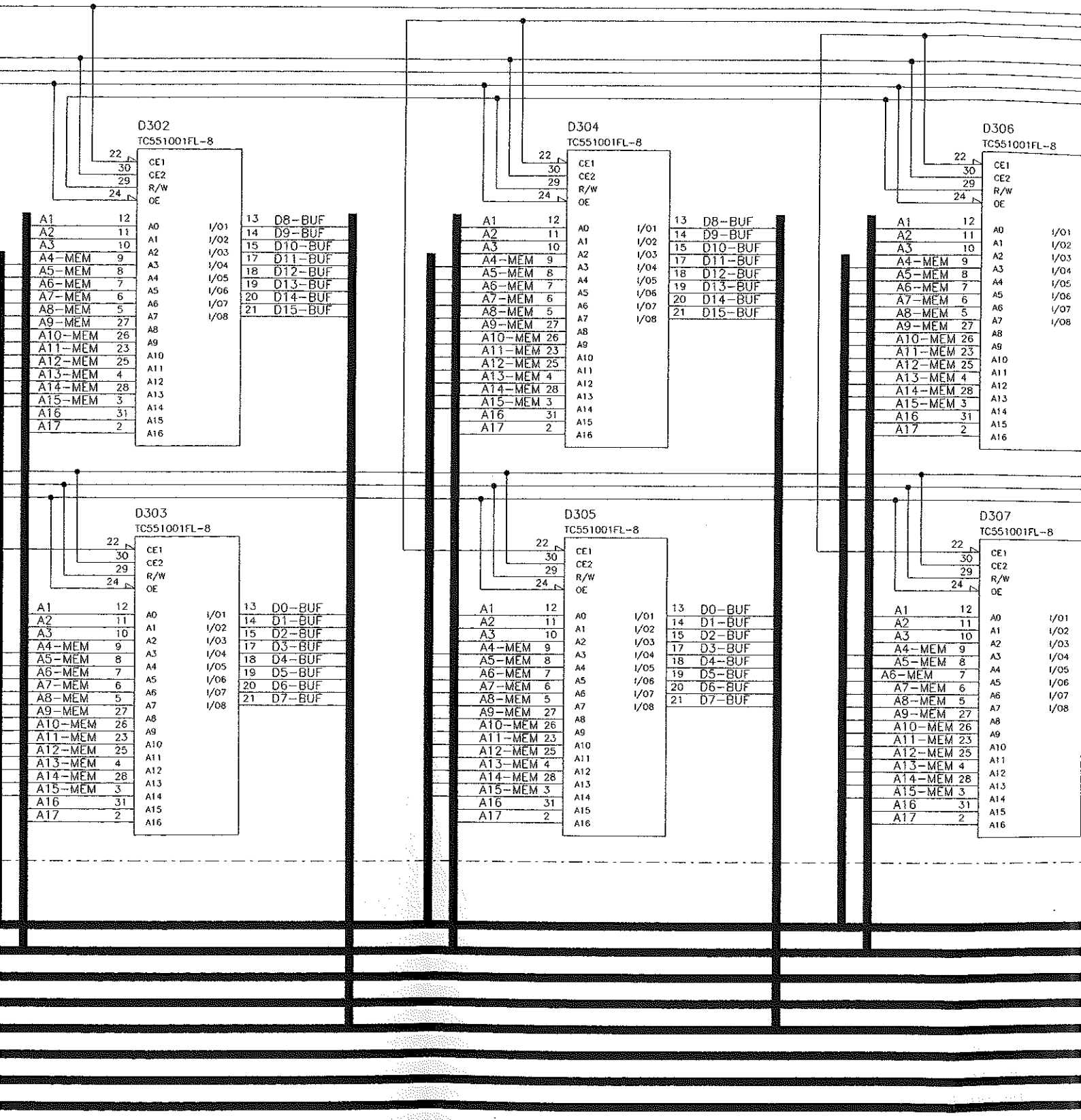


02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.8
				PLOTT	9.6.97	WEH	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

BLATT-NR. 8 + 47



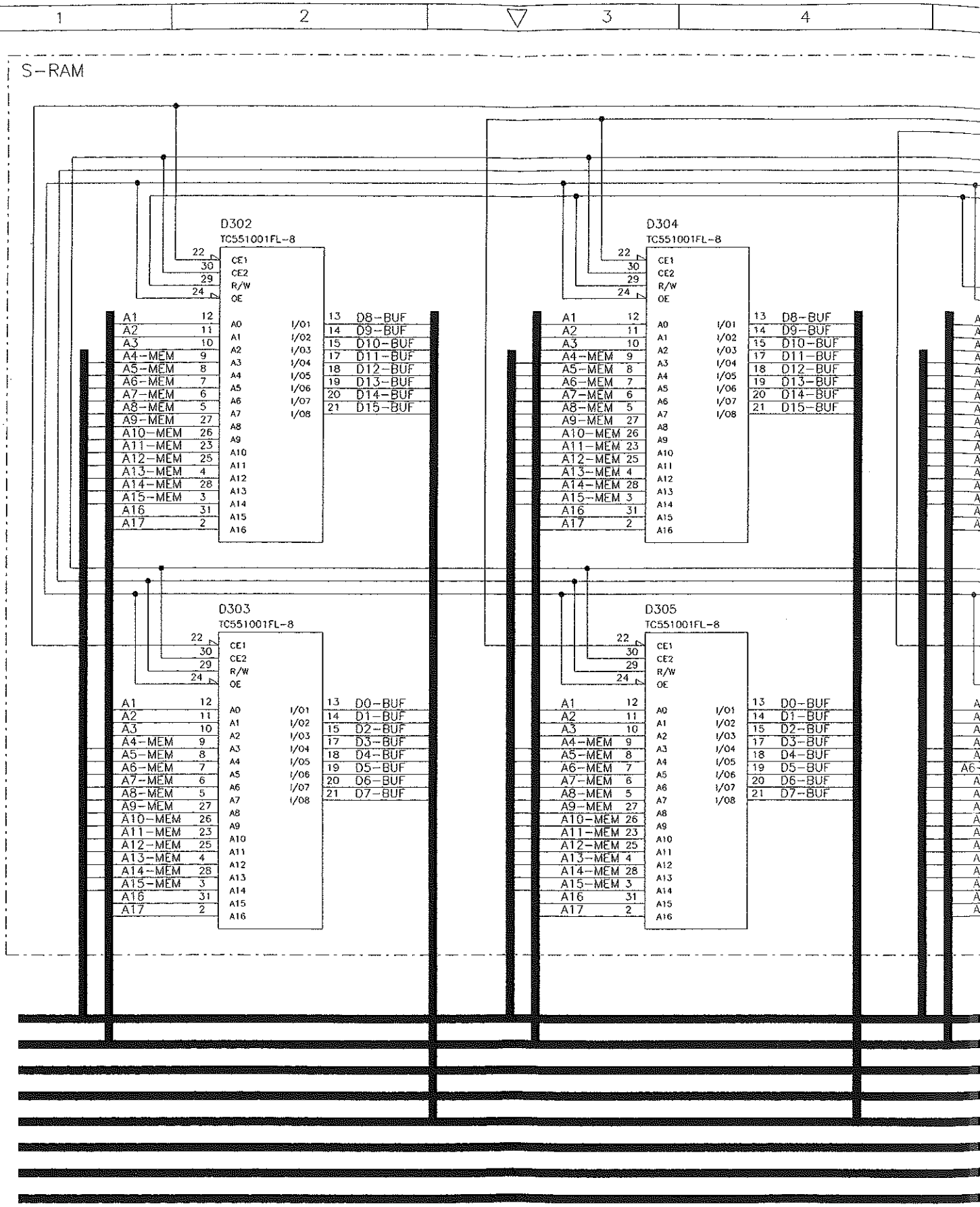
02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		



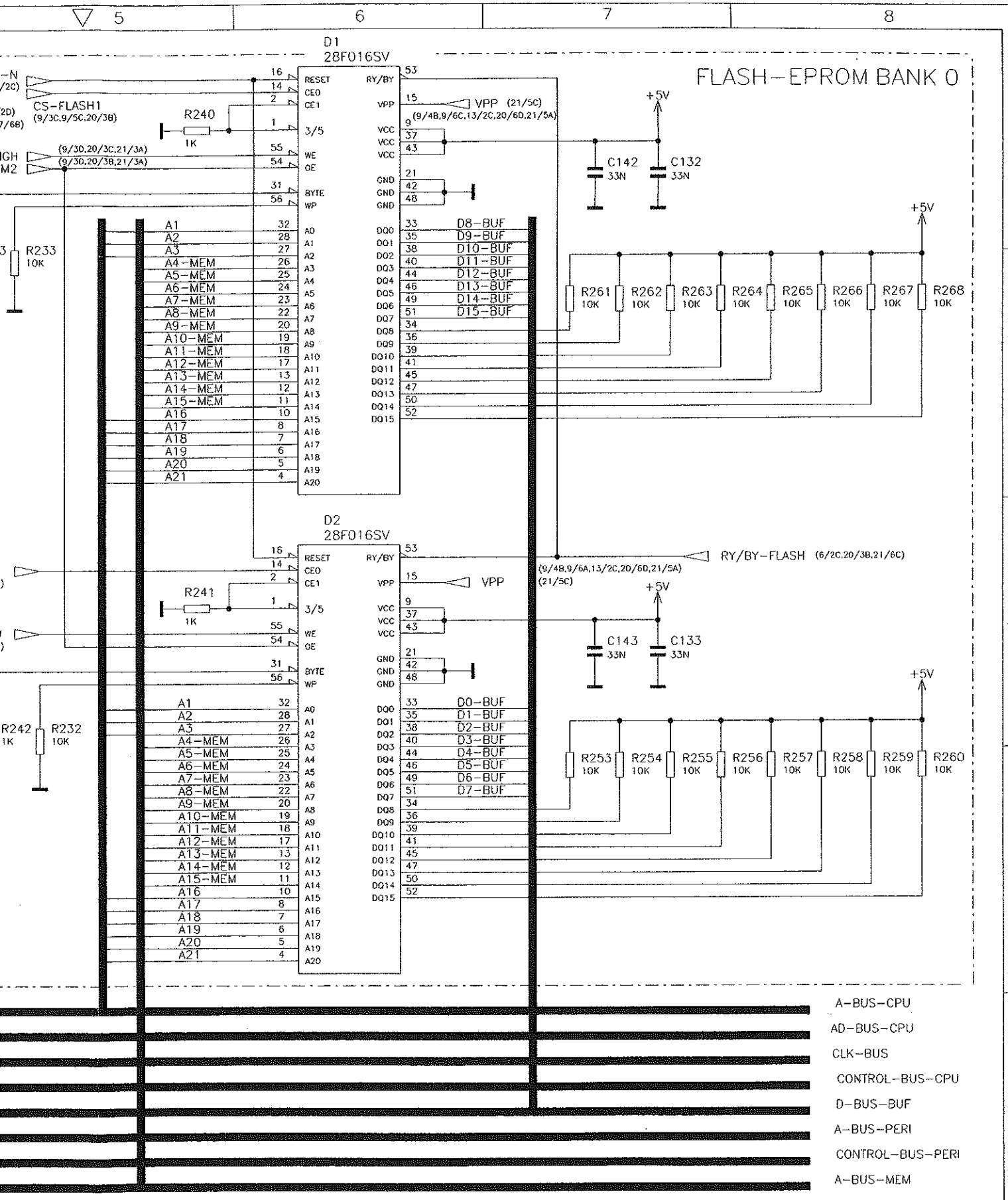
ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

02	
01	
AEND. IND.	AENDERUI MITTEILUN

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

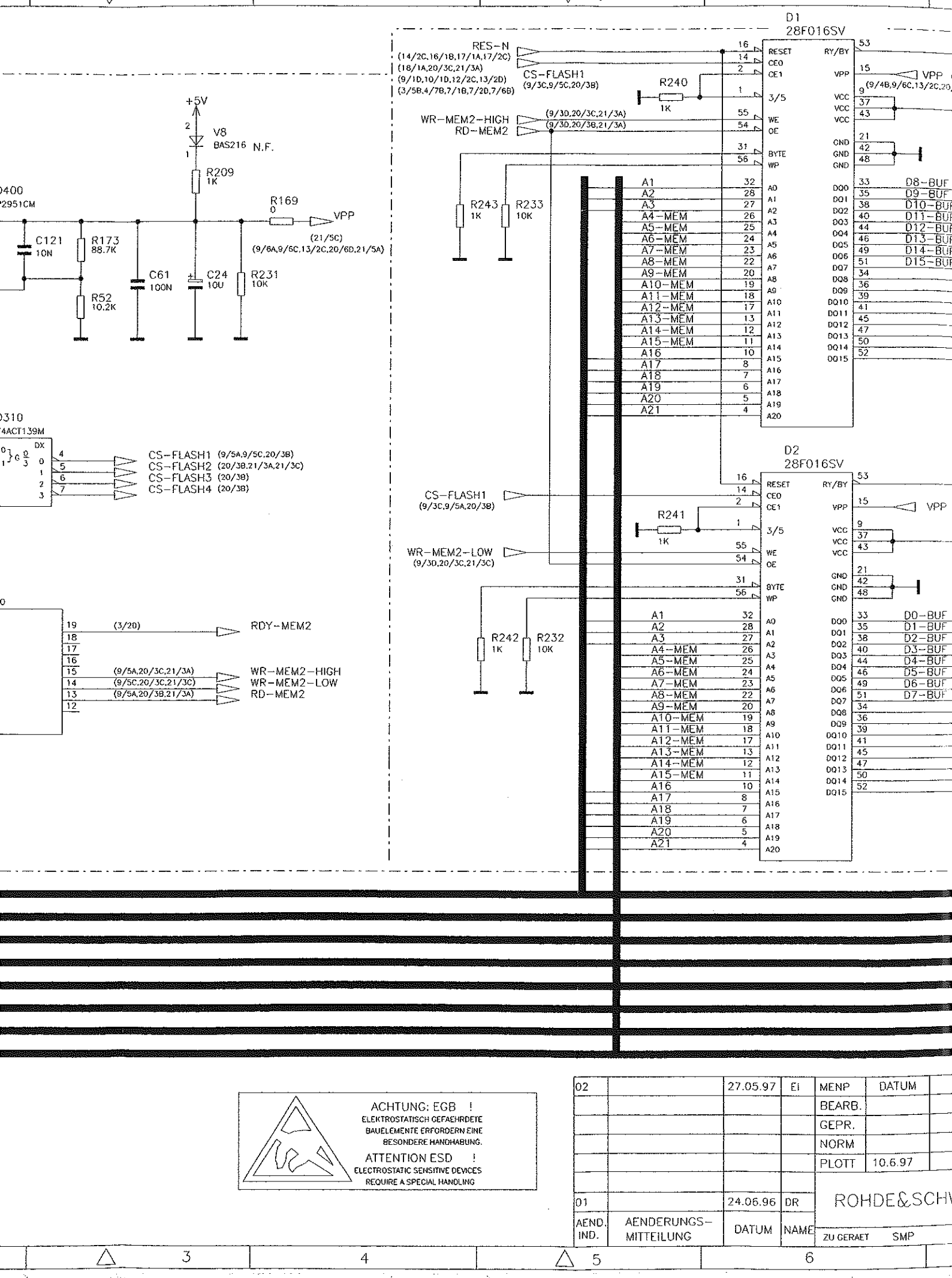


ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

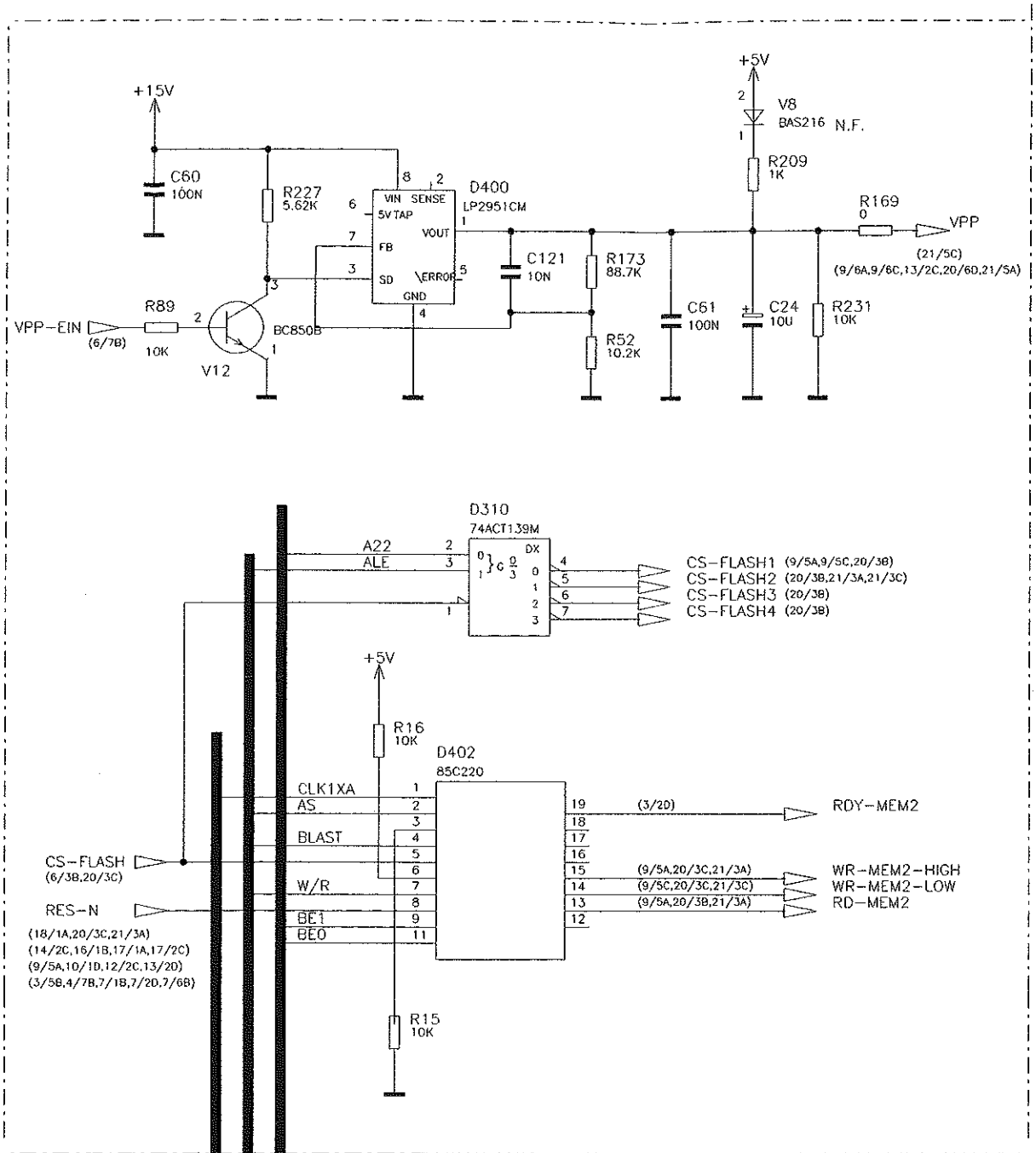


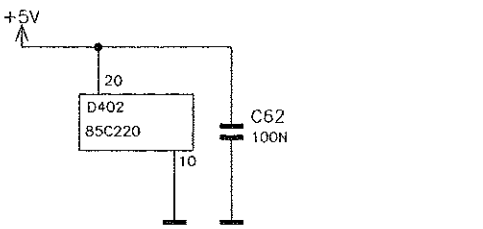
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.9
				PLOTT	10.6.97	WEH	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

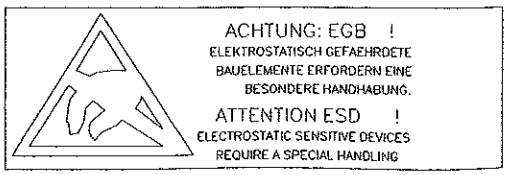
BLATT-NR. 9 + 47


3
4
5
6
D1
28F016SV
D2
28F016SV
RES-N
(14/2C,16/1B,17/1A,17/2C)
(18/1A,20/3C,21/3A)
(9/1D,10/1D,12/2C,13/2D)
(3/5B,4/7B,7/1B,7/2D,7/6B)
CS-FLASH1
(9/3C,9/5C,20/3B)
R240
1K
WR-MEM2-HIGH
RD-MEM2
(9/3D,20/3C,21/3A)
(9/3D,20/3B,21/3A)
R243
1K
R233
10K
R241
1K
R242
1K
R232
10K
WR-MEM2-LOW
(9/3D,20/3C,21/3C)
+5V
V8
BAS216 N.F.
R209
1K
R169
0
VPP
(21/5C)
(9/6A,9/6C,13/2C,20/6D,21/5A)
C121
10N
R173
88.7K
R52
10,2K
C61
100N
C24
10U
R231
10K
4ACT139M
CS-FLASH1 (9/5A,9/5C,20/3B)
CS-FLASH2 (20/3B,21/3A,21/3C)
CS-FLASH3 (20/3B)
CS-FLASH4 (20/3B)
(3/2D)
RDY-MEM2
(9/5A,20/3C,21/3A)
(9/5C,20/3C,21/3C)
(9/5A,20/3B,21/3A)
WR-MEM2-HIGH
WR-MEM2-LOW
RD-MEM2
A1
A2
A3
A4-MEM
A5-MEM
A6-MEM
A7-MEM
A8-MEM
A9-MEM
A10-MEM
A11-MEM
A12-MEM
A13-MEM
A14-MEM
A15-MEM
A16
A17
A18
A19
A20
A21
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING
02 27.05.97 EI
01 24.06.96 DR
AEND. IND.
AENDERUNGS-MITTEILUNG
DATUM
NAME
MENP
BEARB.
GEPR.
NORM
PLOTT 10.6.97
ROHDE&SCH
ZU GERAET SMP

BEHALTEN WIR UNS ALLE RECHTE VOR







D520
74ACT32M
≥1
WR-REG1-HIGH
(12/2C)

D520
74ACT32M
≥1
WR-REG2-HIGH
(13/2D)

D520
74ACT32M
≥1
WR-REG4-LOW
(17/2C)

VAR	R592	R593	R594
04		N.F.	
06		N.F.	N.F.

AEZ	R595	R596	R597	R598
01				N.F.
02			N.F.	
03			N.F.	N.F.
04		N.F.		
05		N.F.		N.F.
06		N.F.	N.F.	
07		N.F.	N.F.	N.F.
08	N.F.			
09	N.F.			N.F.
10	N.F.		N.F.	
11	N.F.		N.F.	N.F.
12	N.F.	N.F.		
13	N.F.	N.F.		N.F.
14	N.F.	N.F.	N.F.	
15	N.F.	N.F.	N.F.	N.F.

CS-REG5
(10/3B)
RD-PERI1
(10/3D,11/5B,13/6B)

D4
74HCT541T
1 &
19 EN
2 D7-BUF
3 D6-BUF
4 D5-BUF
5 D4-BUF
6 D3-BUF
7 D2-BUF
8 D1-BUF
9 D0-BUF

R81 47.5K
R80 47.5K
R79 47.5K
R78 47.5K
R77 47.5K
R76 47.5K

R593 1K
R594 1K
R595 1K
R596 1K
R597 1K
R598 1K

VAR
AEZ

A-BUS-CPU
AD-BUS-CPU
CONTROL-BUS-CPU
CLK-BUS
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME
02		27.05.97	EI
01		24.06.96	DR

	MENP	DATUM	NAME
BEARB.			EI
GEPR.			
NORM			
PLOTT	9.6.97		WEH

ROHDE&SCHWARZ

ZU GERAET SMP

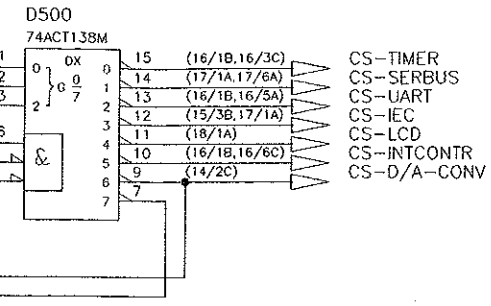
BENENNUNG
RECHNER
CPU
TOP/TOP.10

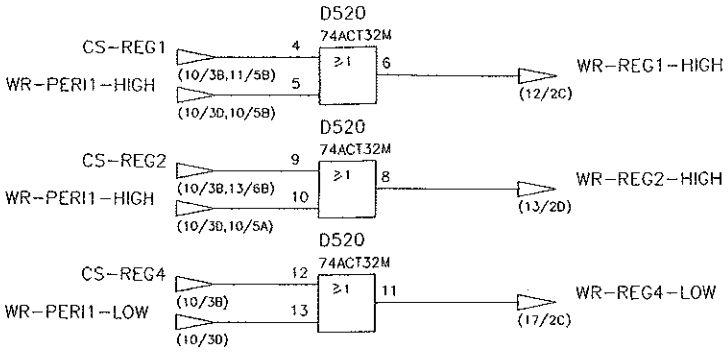
ZEICHN.-NR. 1035.7766.01 S

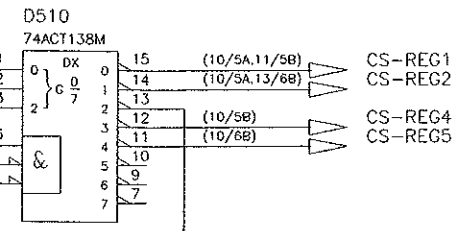
BLATT-NR. 10 + 47

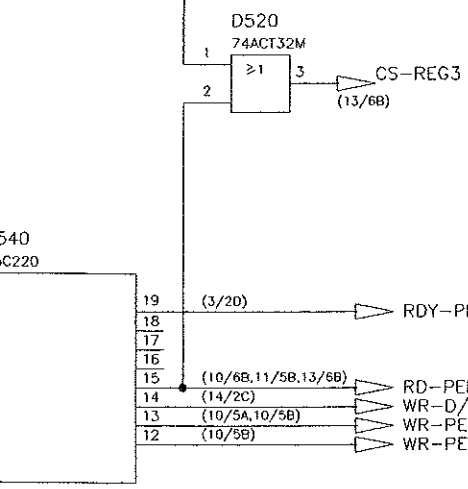
REG.I.V. 1035.5005

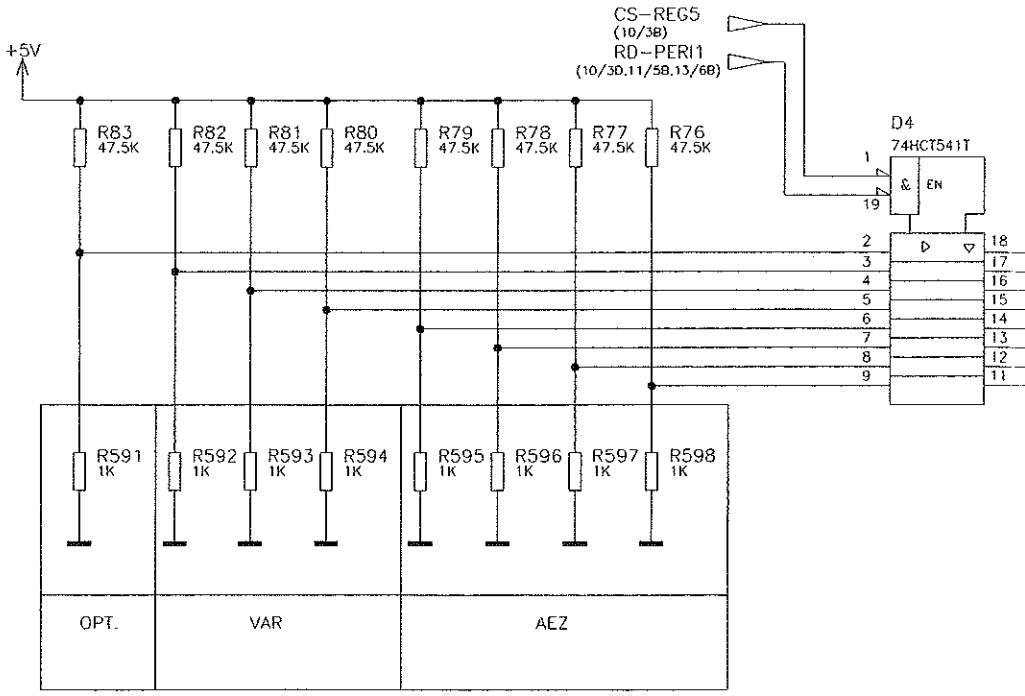
ERSTE Z. 1035.5440.01

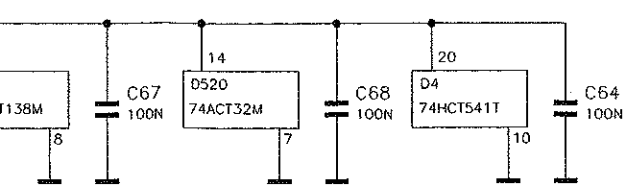












02		27.05.97	EI	MENP	DATUM	NAM
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEI
01		24.06.96	DR	ROHDE&SCHWAR		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

BEHALTEN WIR UNS ALLE RECHTE VOR

AEZ

D500
74ACT138M

A26
A27
A28
A31
A30
A29

CS-TIMER (16/1B,16/3C)
CS-SERBUS (17/1A,17/6A)
CS-UART (16/1B,16/5A)
CS-IEC (15/3B,17/1A)
CS-LCD (18/1A)
CS-INTCONTR (16/1B,16/6C)
CS-D/A-CONV (14/2C)

CS-REG1
WR-PERI1-HIGH
(10/3B,11/5B)
(10/3D,10/5B)

CS-REG2
WR-PERI1-HIGH
(10/3B,13/6B)
(10/3D,10/5A)

CS-REG4
WR-PERI1-LOW
(10/3B)
(10/3D)

D510
74ACT138M

A1-PERI
A2-PERI
A3-PERI
A31

CS-REG1 (10/5A,11/5B)
CS-REG2 (10/5A,13/6B)
CS-REG4 (10/5B)
CS-REG5 (10/6B)

R210
1K

+5V

R83 47.5K
R82 47.5K
R81 47.5

R591 1K
R592 1K
R59 1K

OPT.
VAR

D520
74ACT32M

CS-REG3
(13/6B)

D540
85C220

CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
BE1-PERI
BE0-PERI

RDY-PERI1 (3/2D)
RD-PERI1 (10/6B,11/5B,13/6B)
WR-D/A-CONV (14/2C)
WR-PERI1-HIGH (10/5A,10/5B)
WR-PERI1-LOW (10/5B)

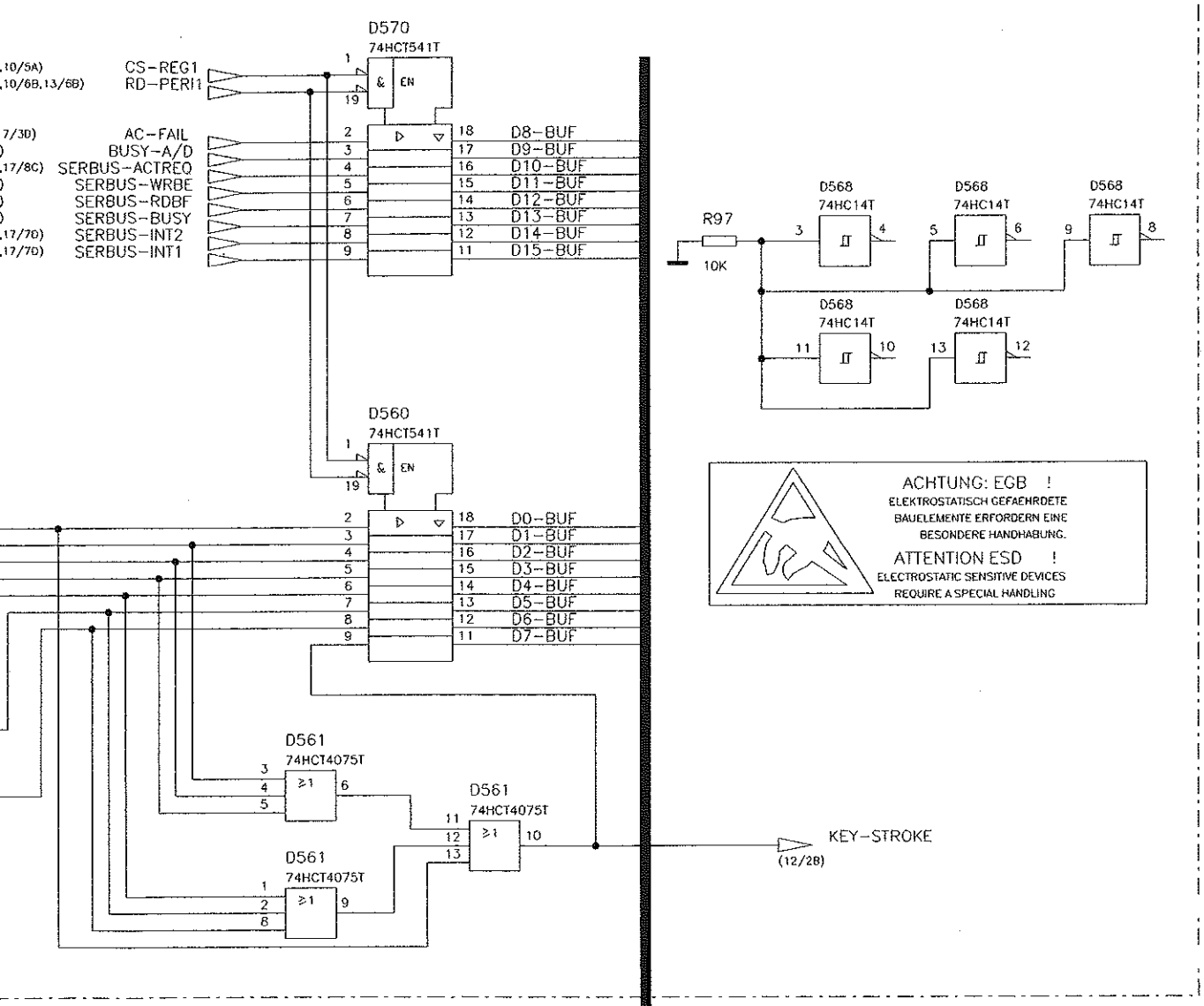
RES-N

R20
10K

(16/1B,17/1A,17/2C,18/1A)
(20/3C,21/3A)
(9/1D,9/5A,12/2C,13/2D,14/2C)
(3/5B,4/7B,7/1B,7/2D,7/6B)

+5V

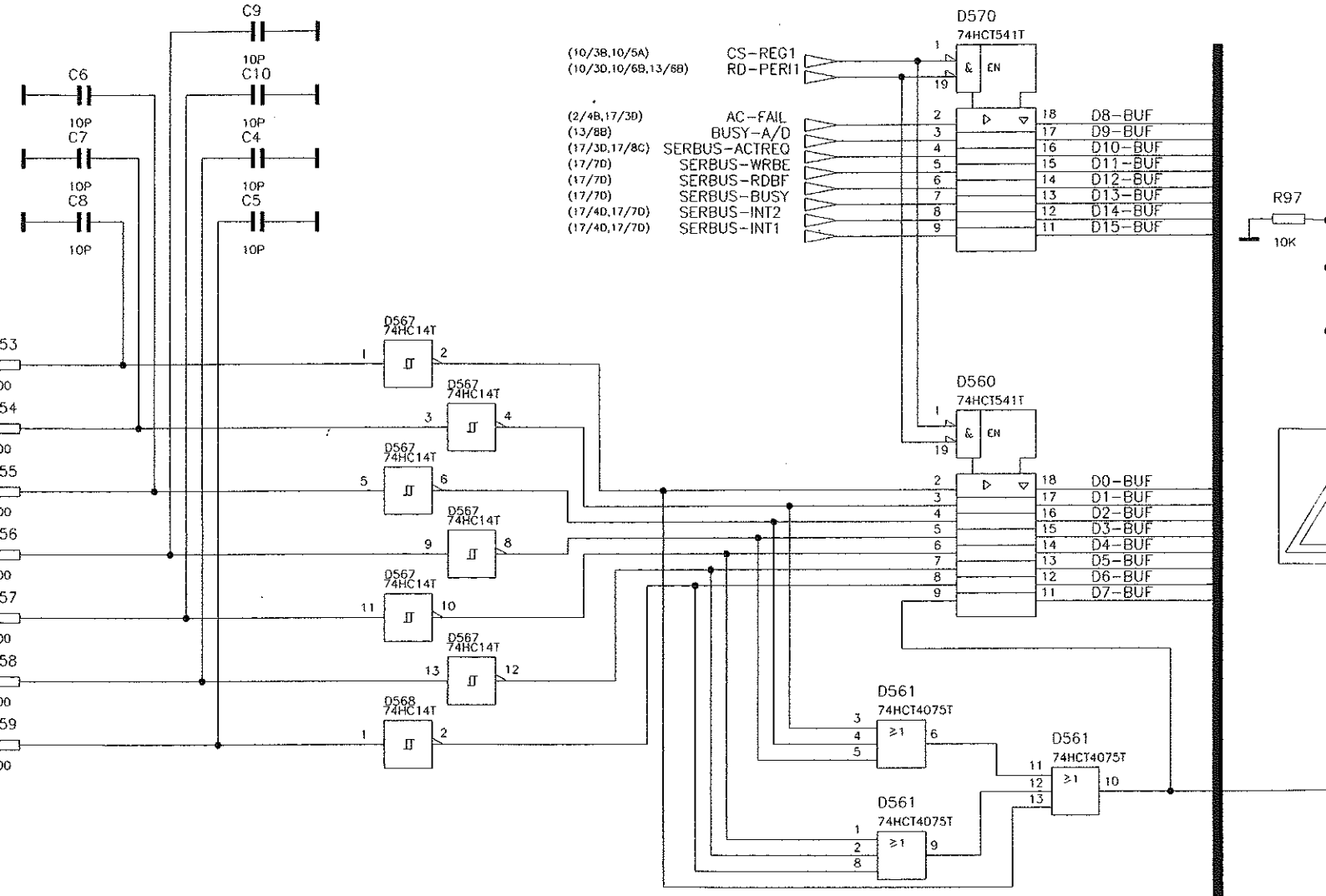
D540 85C220 (20) — C65 100N
D510 74ACT138M (16) — C66 100N
D500 74ACT138M (16) — C67 100N
D520 74ACT32M (14) — C68 100N
D4 74HCT541T (20) — C64 100N

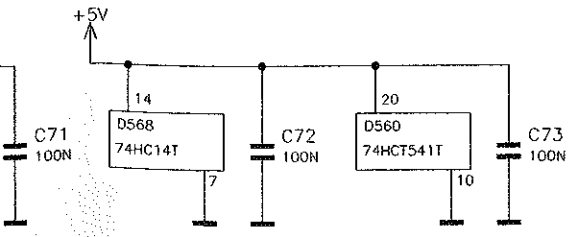


A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			
				PLOTT	9.6.97	WEH	TOP/TOP.11
01		24.06.96	OR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

BLATT-NR. 11 + 47

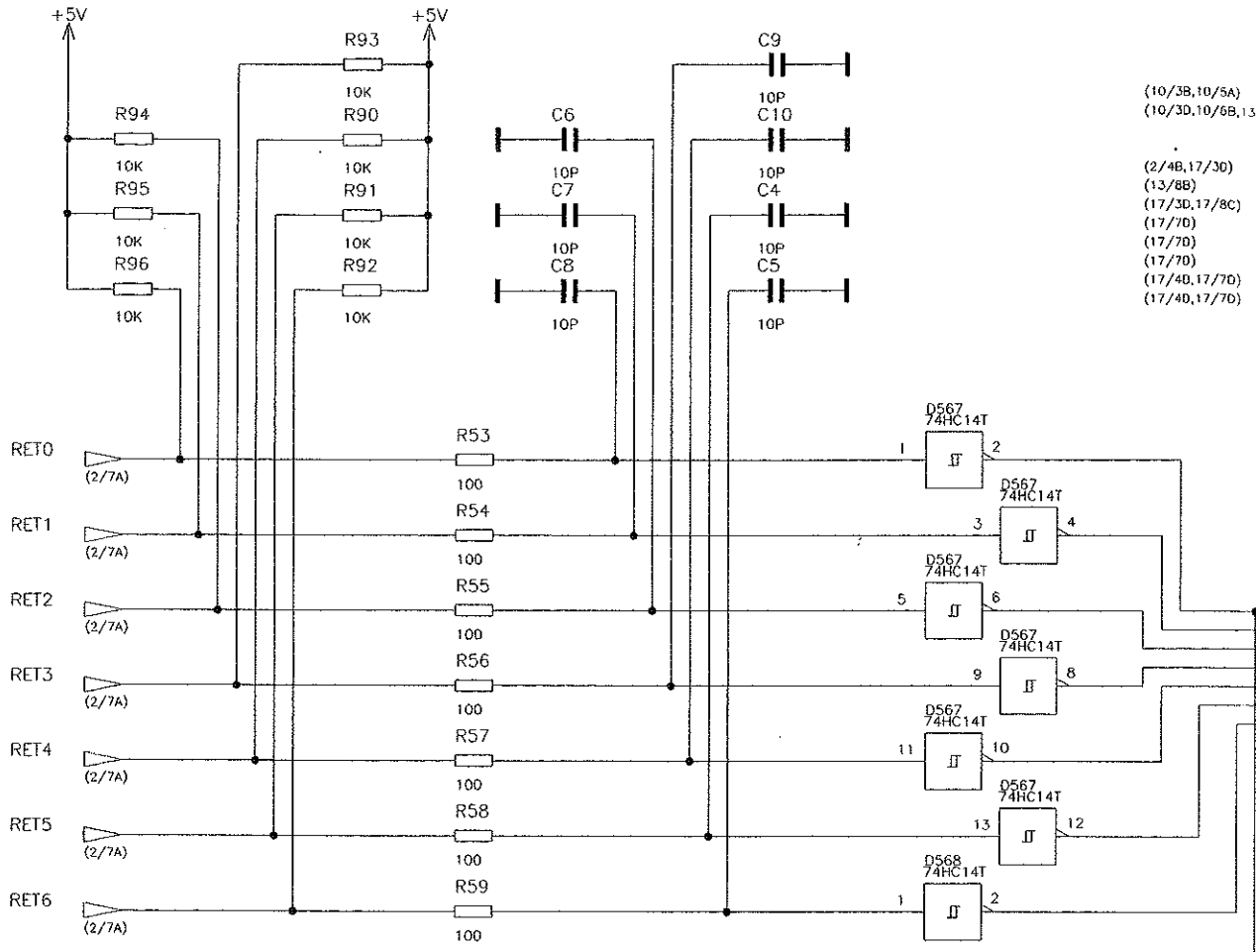


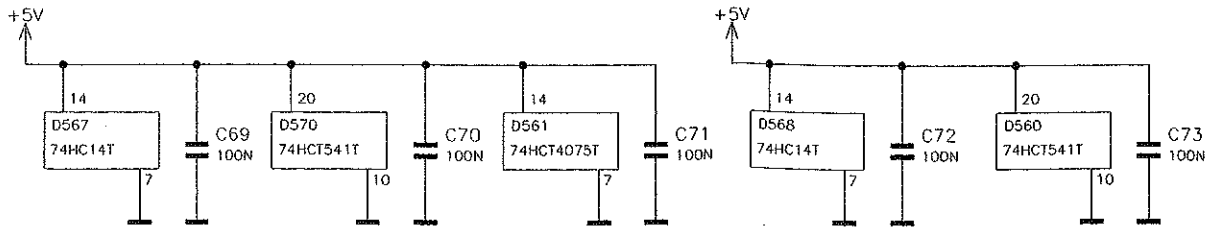


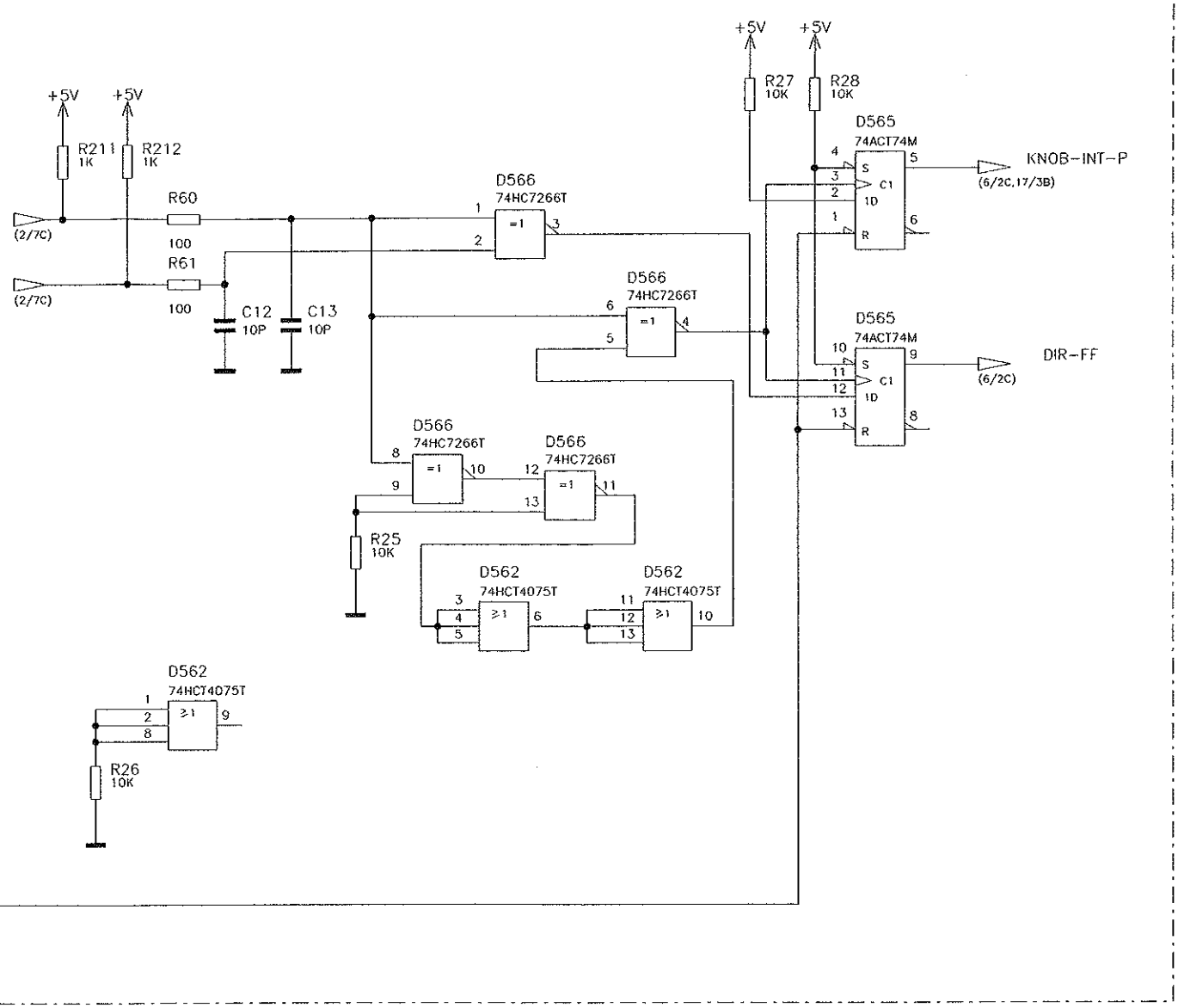
02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

KEY-INTERFACE

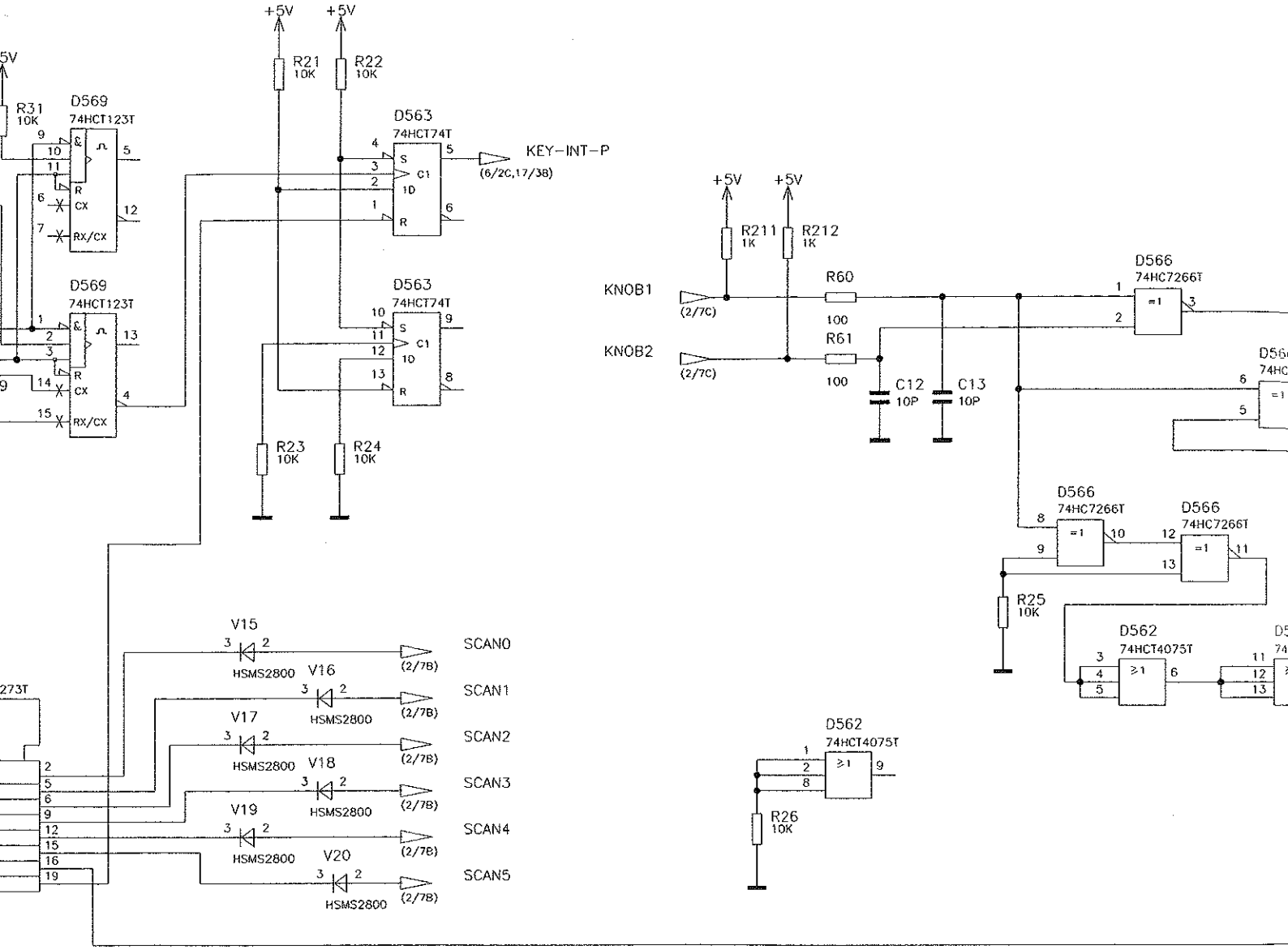


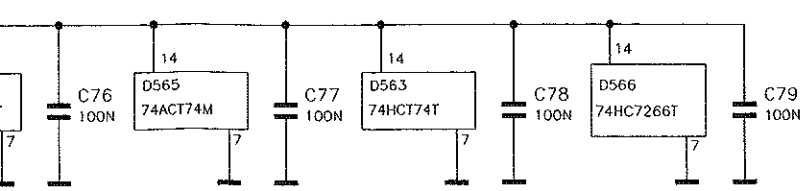




A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

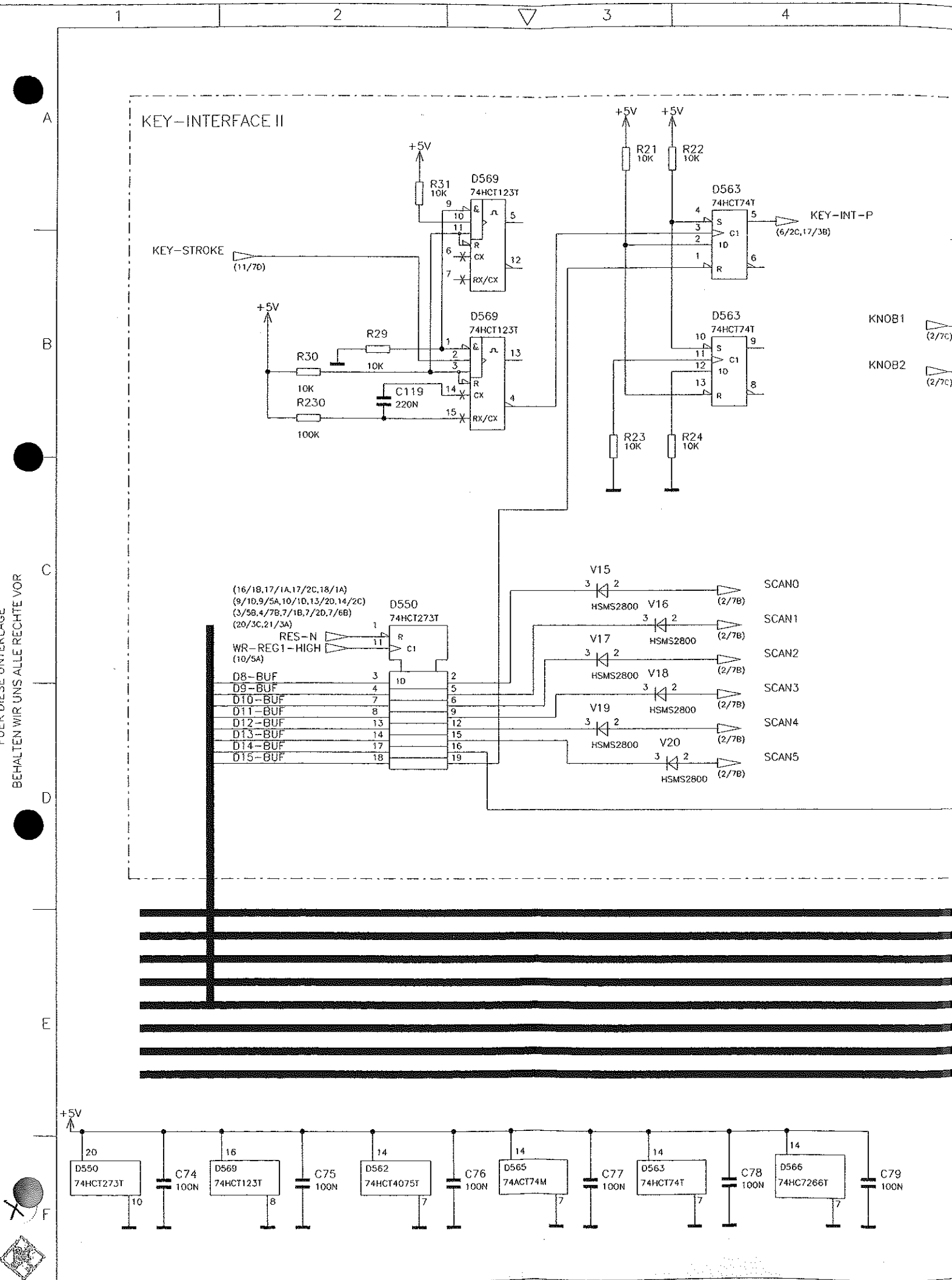
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.12
				PLOTT	9.6.97	WEH	
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR				BLATT-NR. 12 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01



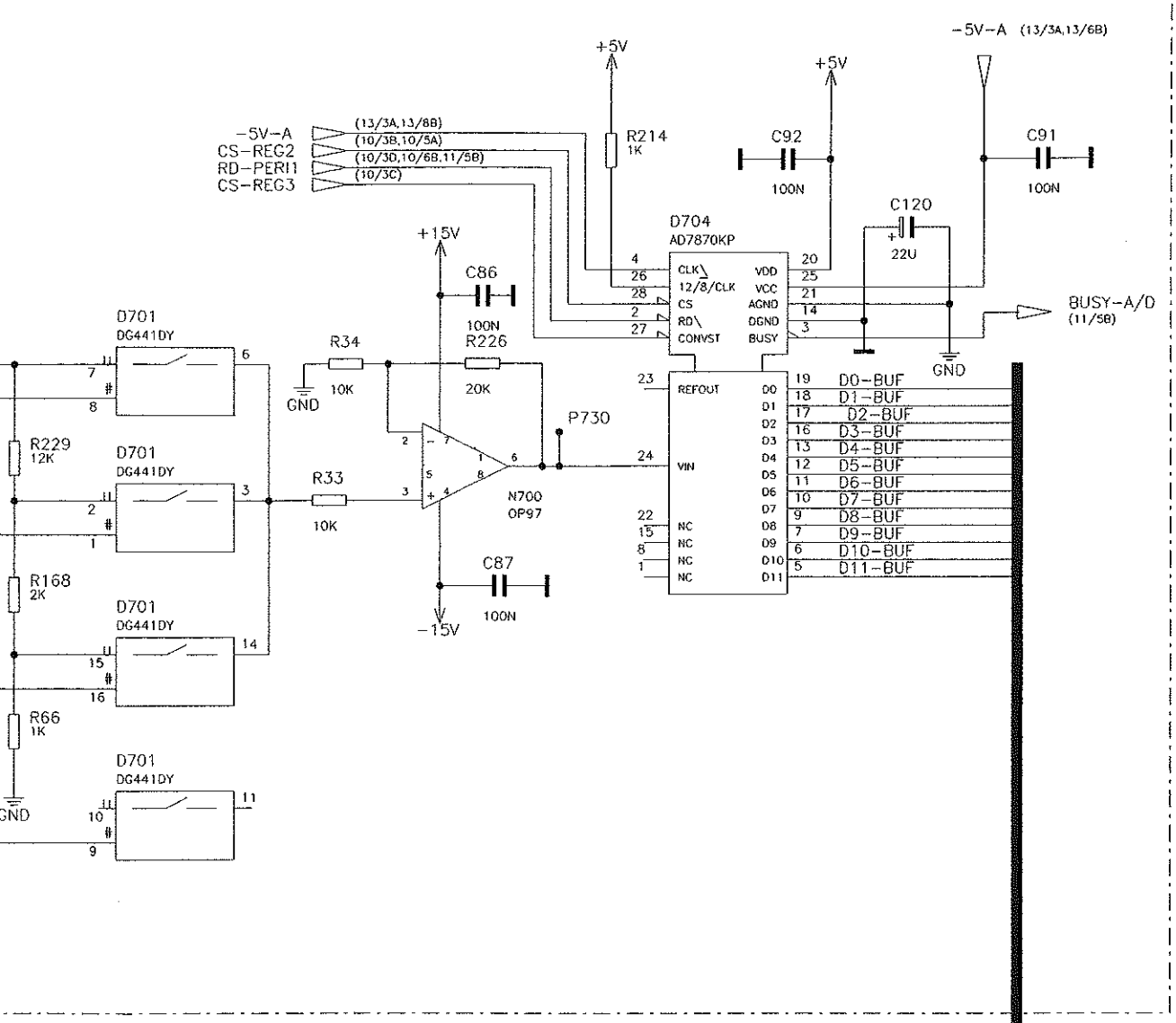


02		27.05.97	EI	MENP	DATUM
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	9.6.97
01		24.06.96	DR	ROHDE&SCH	
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR



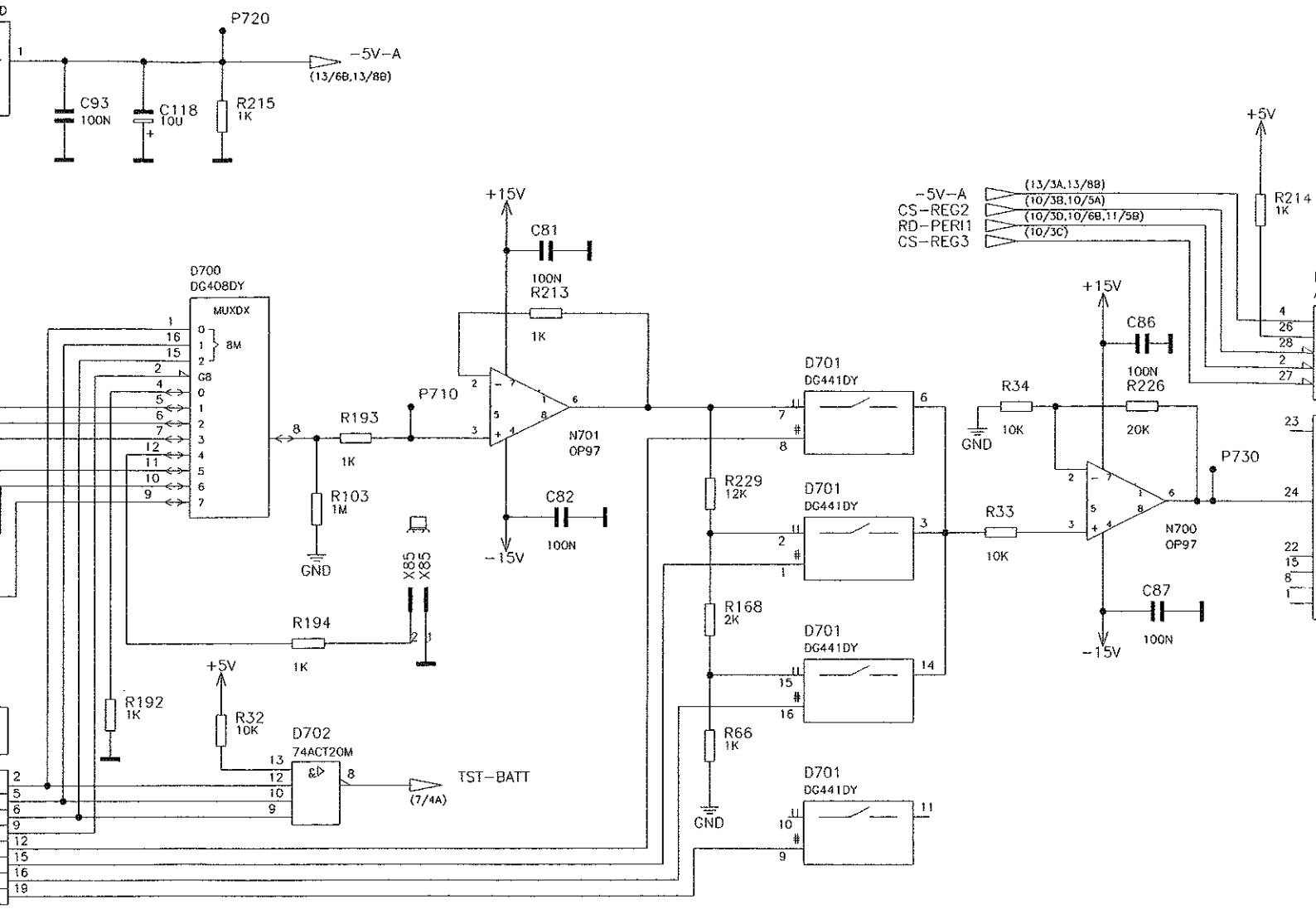
P730

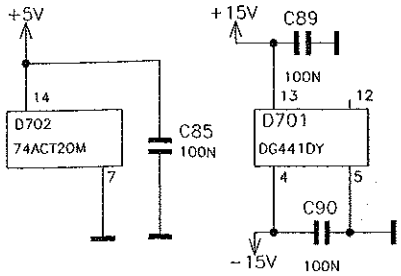


A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

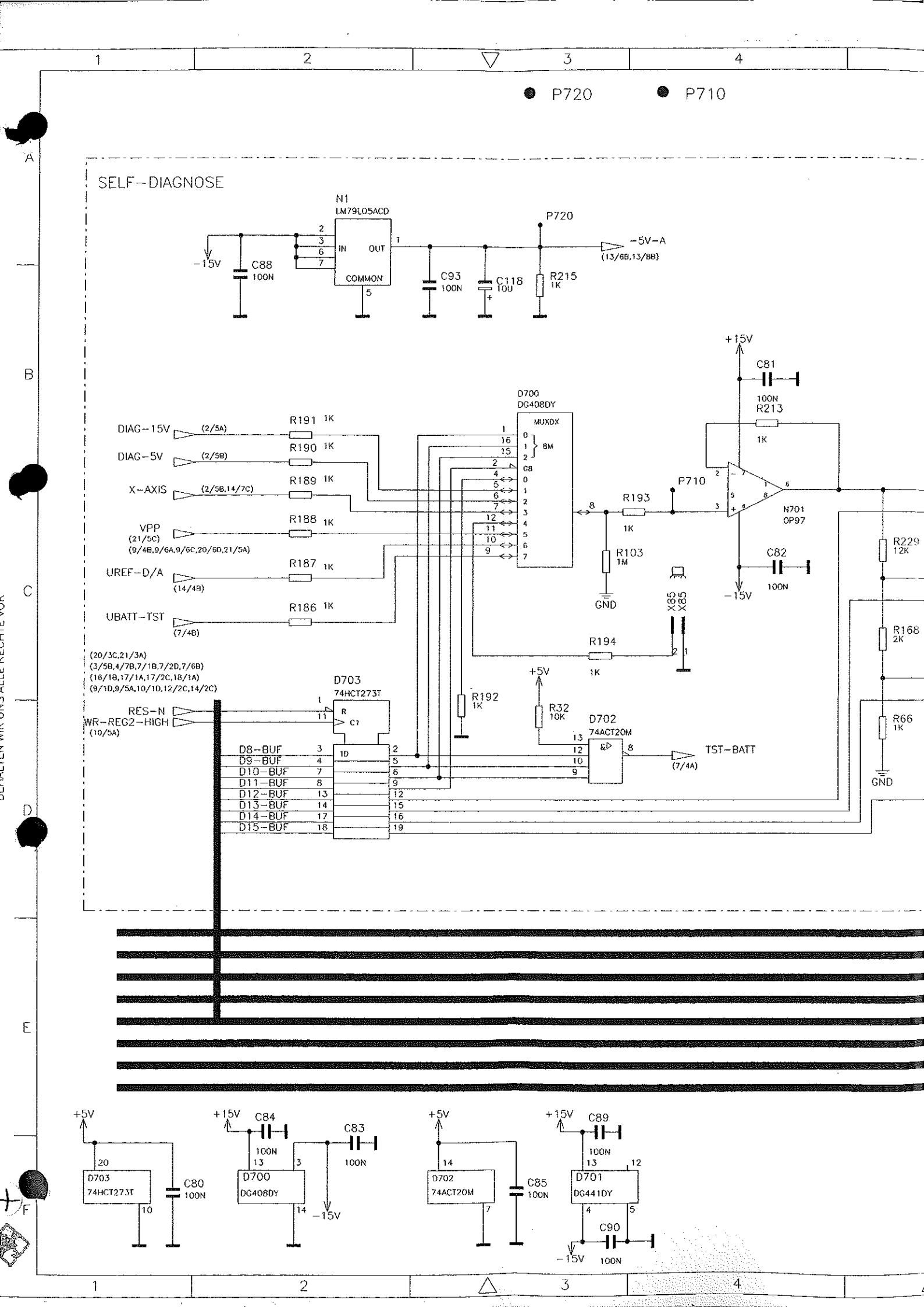
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG	
				BEARB.		EI	RECHNER	
				GEPR.			CPU	
				NORM			TOP/TOP.13	
				PLOTT	10.6.97	WEH		
							ZEICHN.-NR.	BLATT-NR.
01		24.06.96	DR	ROHDE&SCHWARZ			1035.7766.01 S	13 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005	ERSTE Z. 1035.5440.01

P720 P710 P730

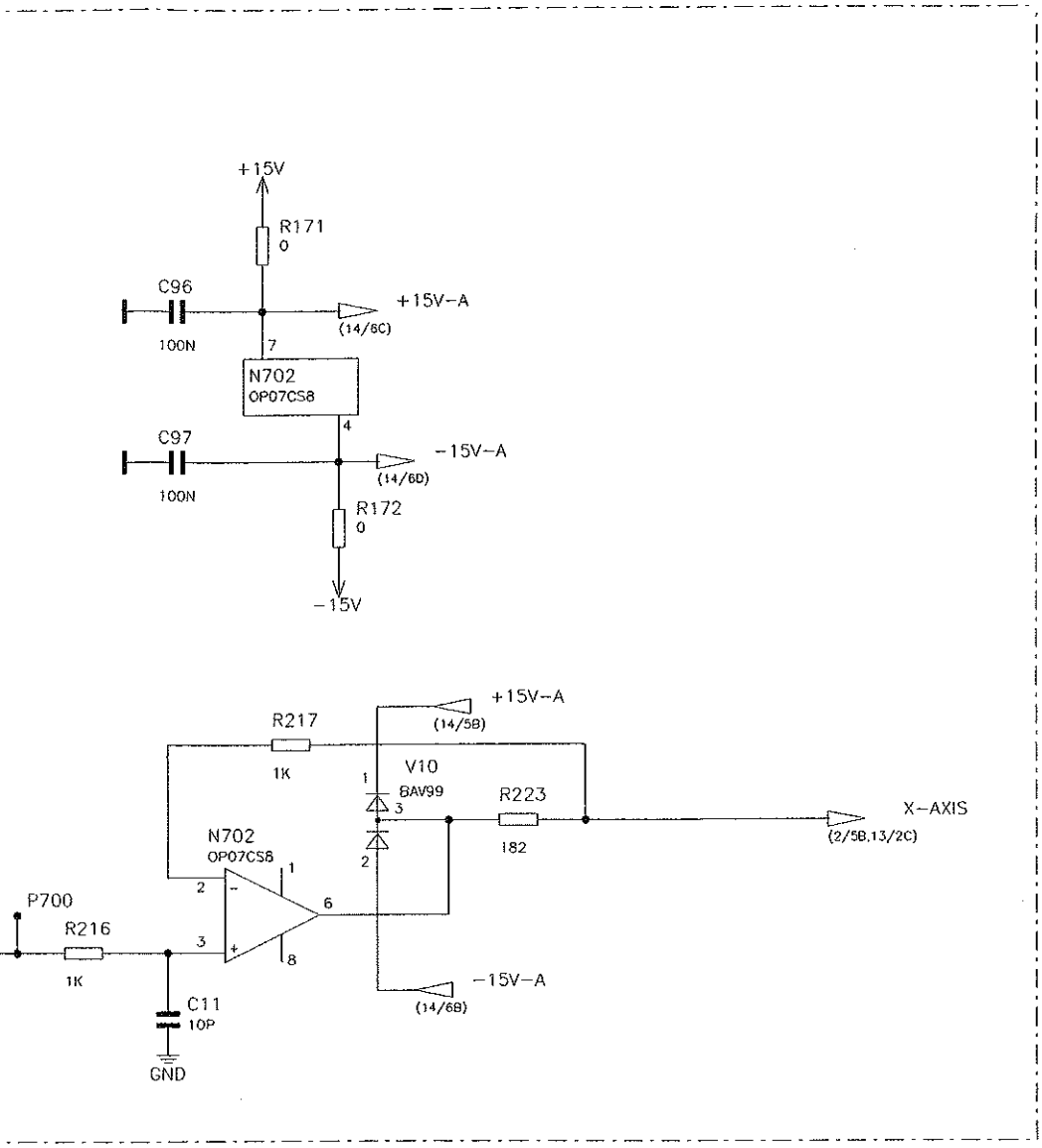




02		27.05.97	EI	MENP	DATUM
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	10.6.97
01		24.06.96	DR	ROHDE&SCHW	
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP	


P720
P710
SELF-DIAGNOSE
N1
LM79L05ACD
IN
OUT
COMMON
-15V
C88
100N
C93
100N
C118
10U
R215
1K
-5V-A
(13/6B,13/8B)
+15V
C81
100N
R213
1K
D700
DG408DY
MUXDX
8M
G8
DIAG-15V
(2/5A)
R191 1K
DIAG-5V
(2/5B)
R190 1K
X-AXIS
(2/5B,14/7C)
R189 1K
VPP
(21/5C)
(9/4B,9/6A,9/6C,20/6D,21/5A)
R188 1K
UREF-D/A
(14/4B)
R187 1K
UBATT-TST
(7/4B)
R186 1K
R193
1K
R103
1M
GND
P710
N701
OP97
C82
100N
-15V
X85
X85
R194
1K
R229
12K
R168
2K
R66
1K
GND
(20/3C,21/3A)
(3/5B,4/7B,7/1B,7/2D,7/6B)
(16/1B,17/1A,17/2C,18/1A)
(9/1D,9/5A,10/1D,12/2C,14/2C)
D703
74HCT273T
RES-N
WR-REG2-HIGH
(10/5A)
R192
1K
+5V
R32
10K
D702
74ACT20M
TST-BATT
(7/4A)
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
+5V
D703
74HCT273T
C80
100N
+15V
C84
100N
C83
100N
D700
DG408DY
-15V
+5V
D702
74ACT20M
C85
100N
+15V
C89
100N
D701
DG441DY
C90
100N
-15V

● P700

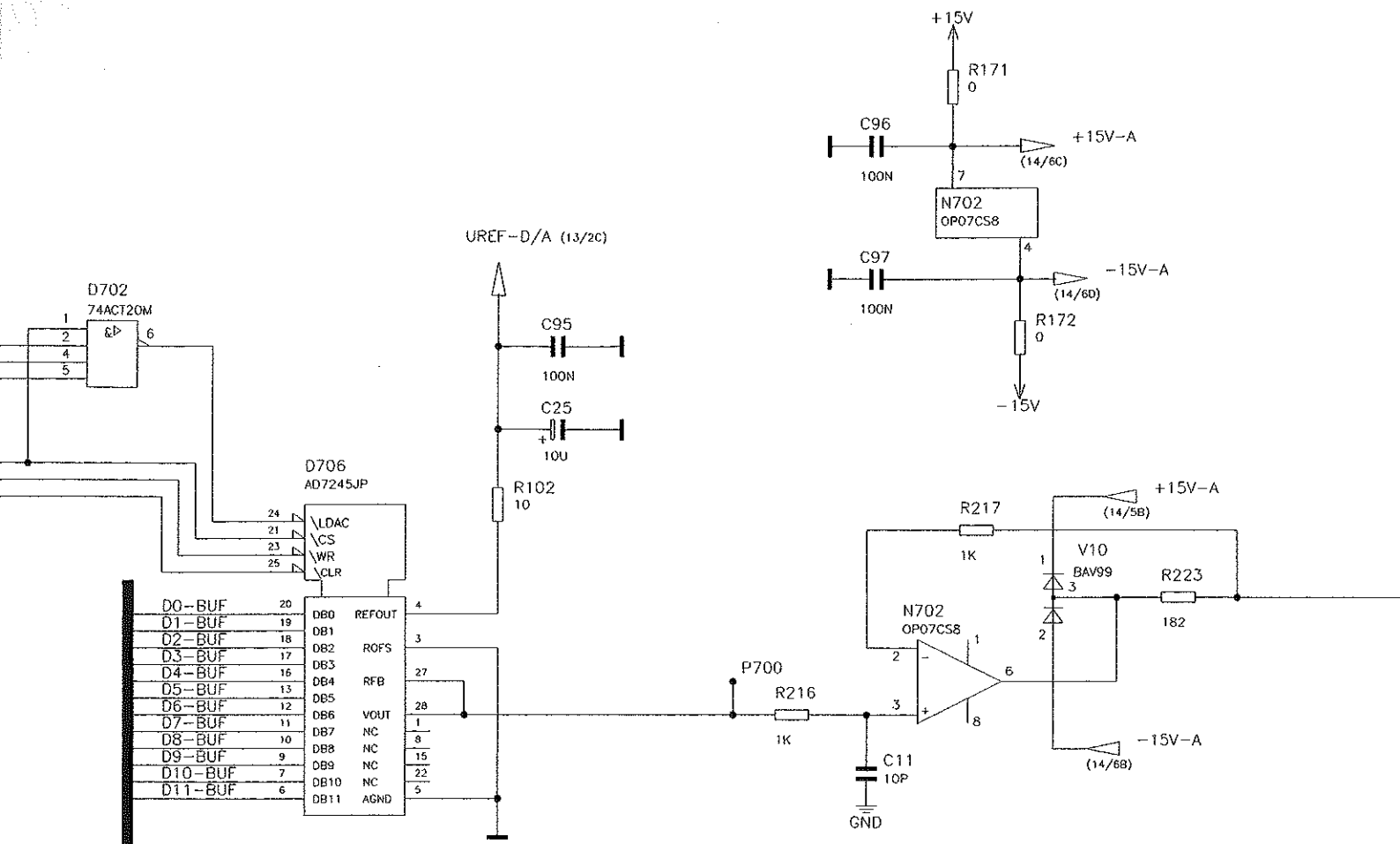


A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.14
				PLOTT	9.6.97	WEH	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

BLATT-NR. 14 + 47

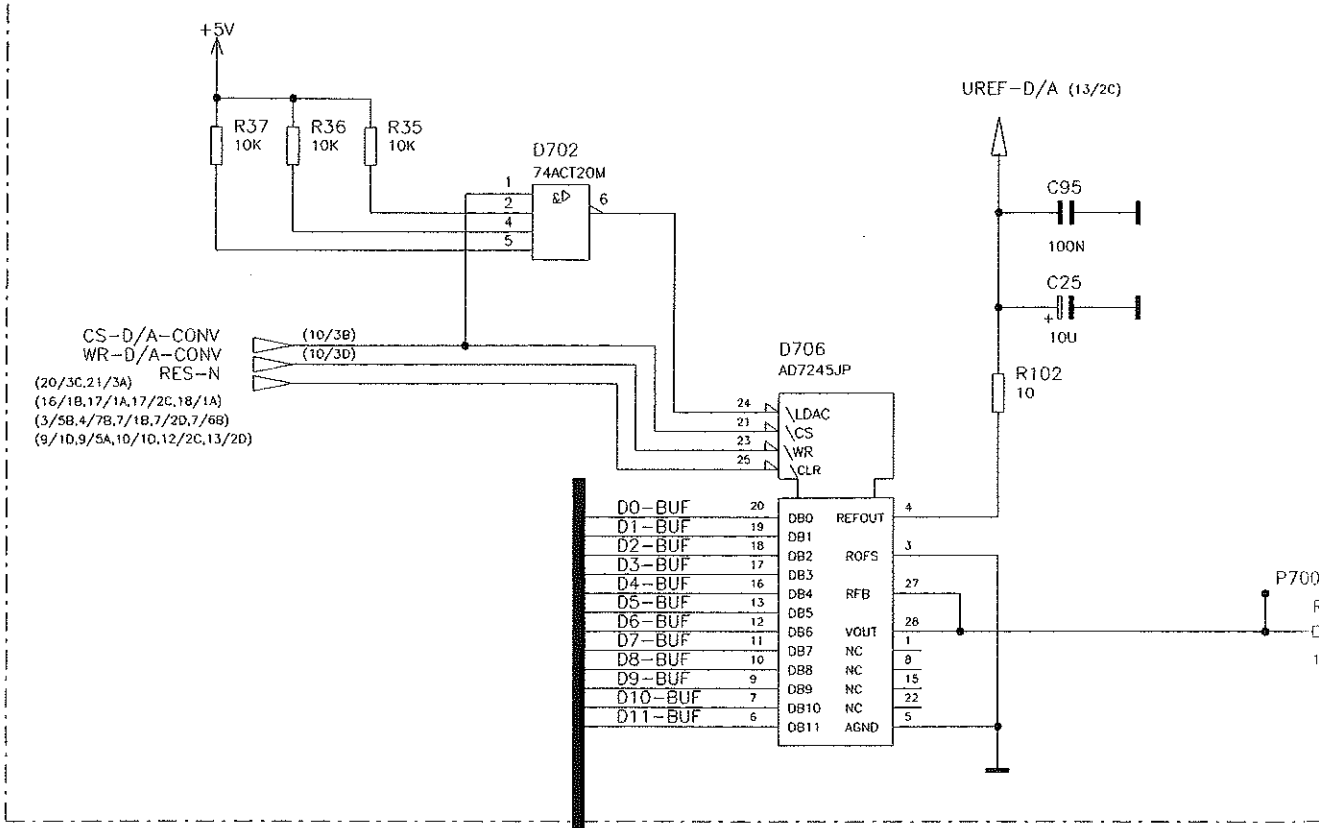
P700

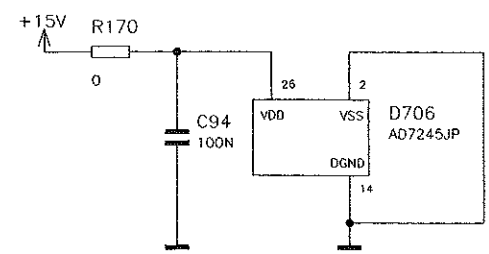


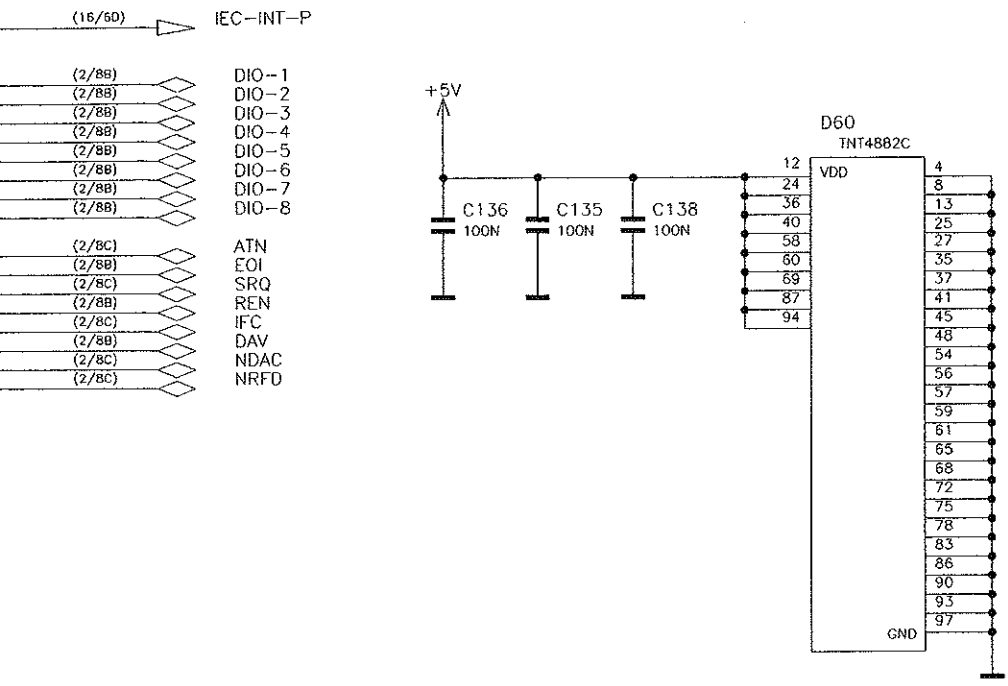
02		27.05.97	EI	MENP	DATUM	
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	9.6.97	
01		24.06.96	DR	ROHDE&SCHW		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

X-OUTPUT







A-BUS-CPU

AD-BUS-CPU

CLK-BUS

CONTROL-BUS-CPU

D-BUS-BUF

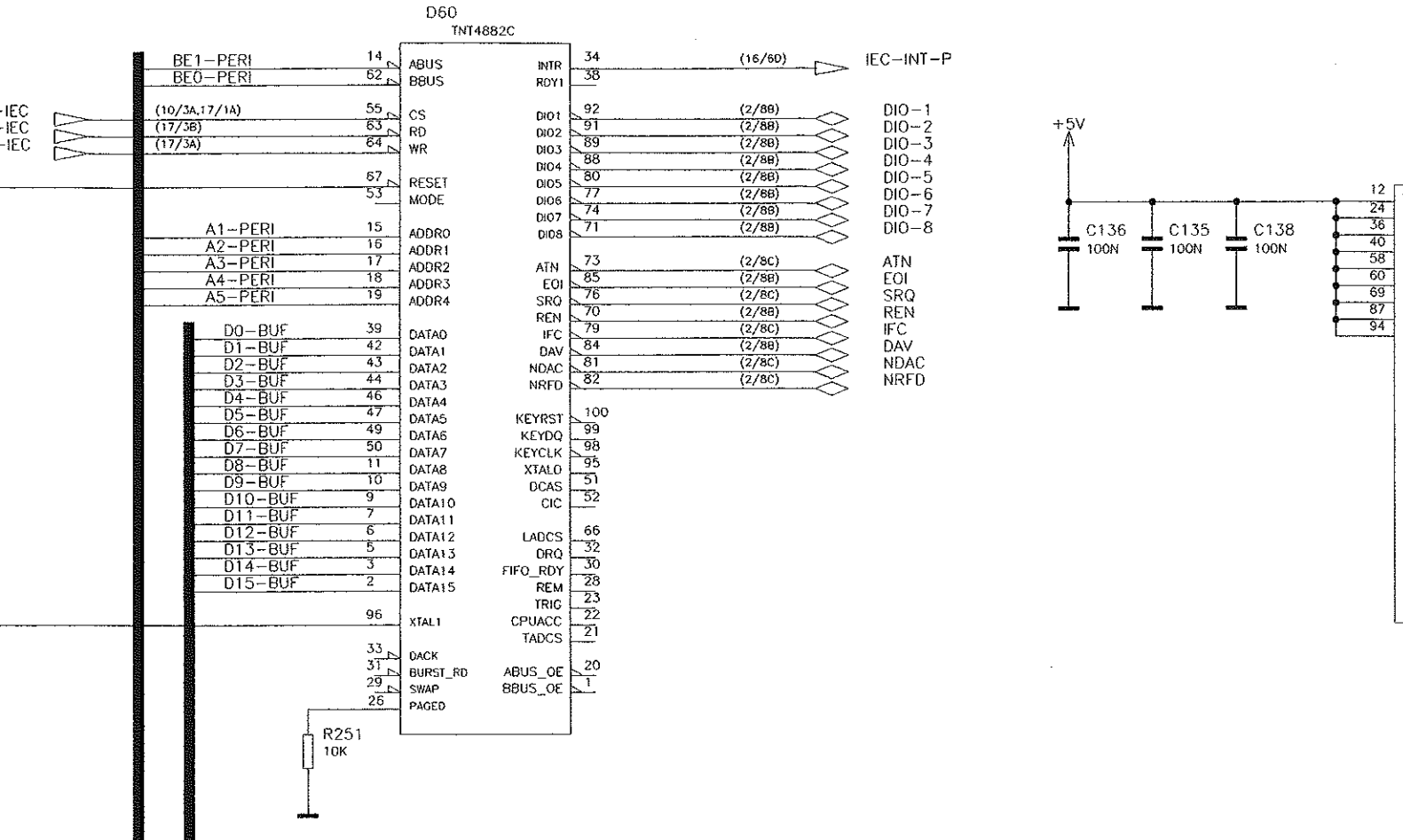
A-BUS-PERI

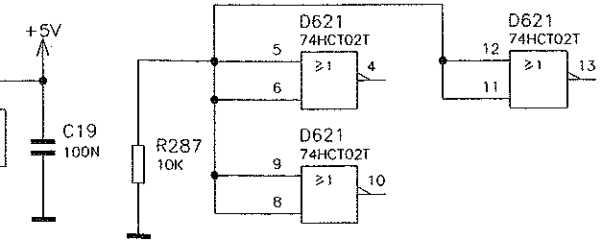
CONTROL-BUS-PERI

A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.15
				PLOTT	9.6.97	WEH	
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

BLATT-NR. 15 + 47





02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

WEITERGABE SOWIE VERVIELFÄLTIGUNG DIESER UNTERLAGE, VERWERTUNG UND MITTEILUNG IHRES INHALTS NICHT GESTATTET, SOWEIT NICHT AUSDRÜCKLICH ZUGESTANDEN. ZUWIDERHANDLUNGEN VERPFLICHTEN ZU SCHADENERSATZ. ALLE RECHTE VORBEHALTEN WIR UNS ALLE RECHTE VOR

IEC-BUS

D60
TNT4882C

(18/1A,20/38)
(2/2D,4/7C,6/5A,16/5A,17/6A)
RES-P
RES-IEC-P
(17/4C)
D621
74HCT02T

CS-IEC
RD-IEC
WR-IEC

BE1-PERI
BE0-PERI
(10/3A,17/1A)
(17/3B)
(17/3A)

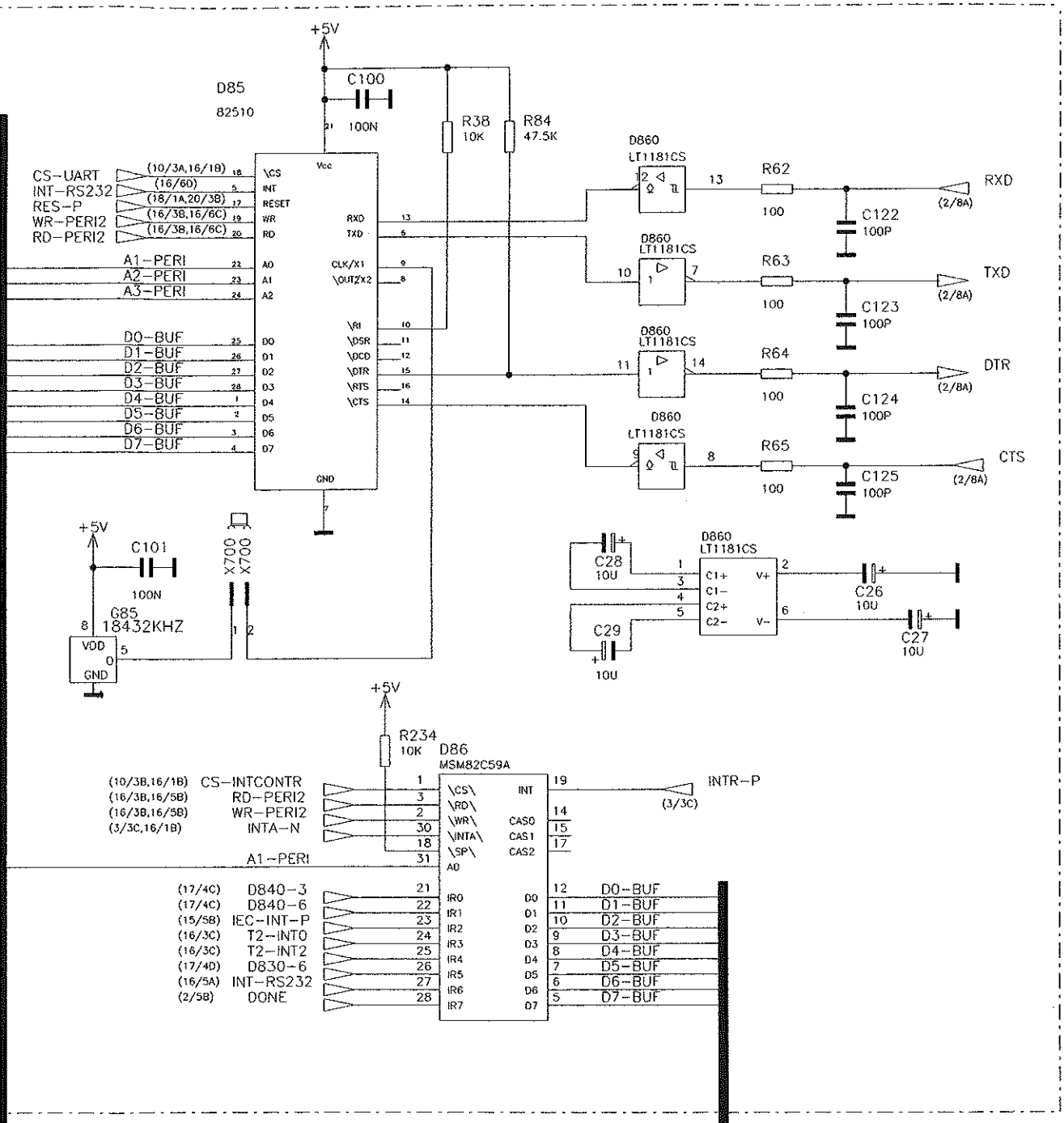
A1-PERI
A2-PERI
A3-PERI
A4-PERI
A5-PERI

D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF

+5V
C137
100N
B1
40.000MHZ
X600
X600

R251
10K

D621
74HCT02T
C19
100N
R287
10K



A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME			

BENENNUNG
RECHNER
CPU
TOP/TOP.16

ROHDE&SCHWARZ

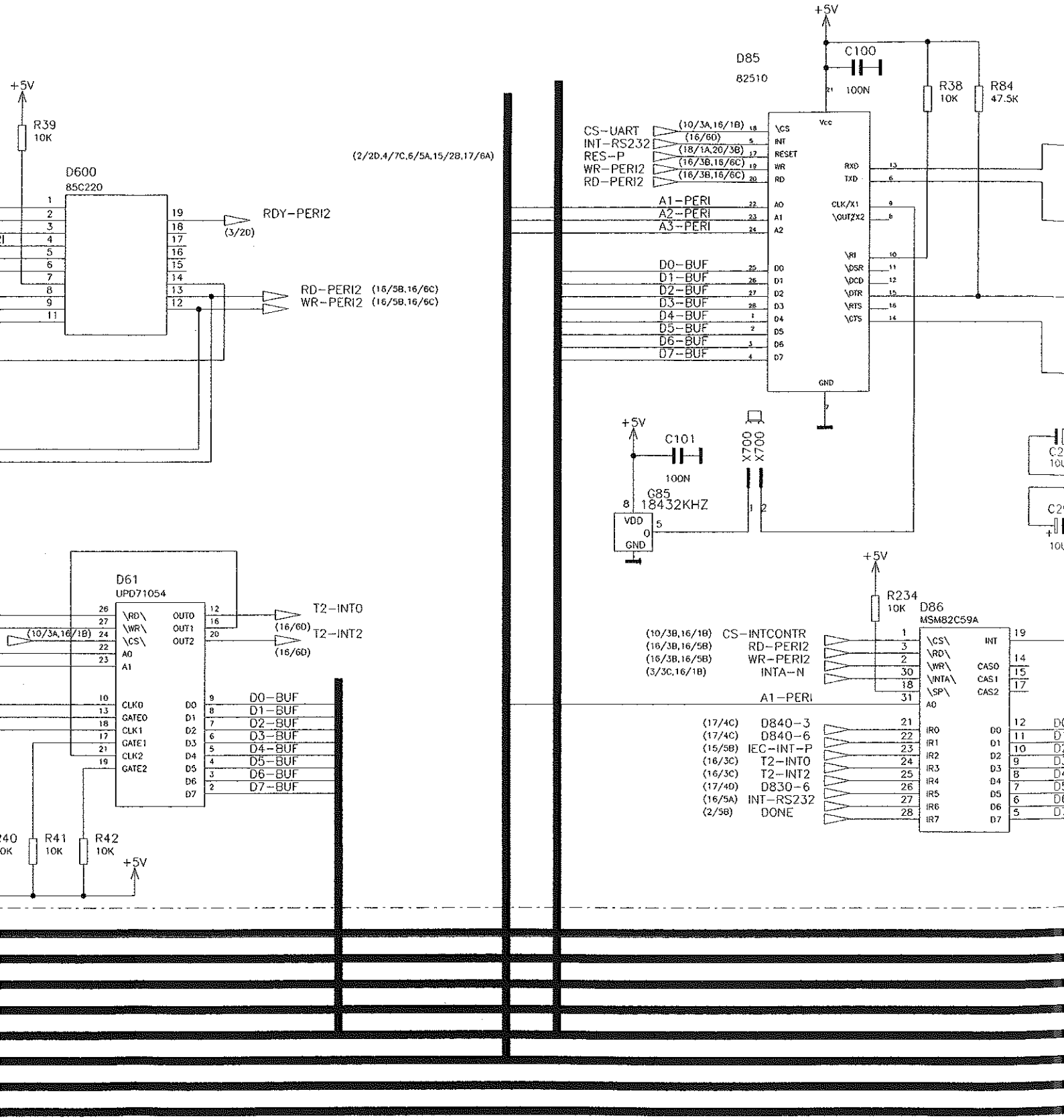
ZEICHN.-NR. 1035.7766.01 S

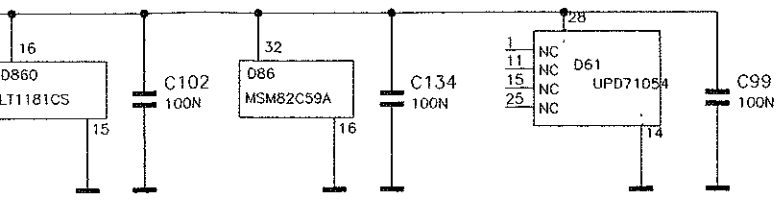
BLATT-NR. 16 + 47

ZU GERAET SMP

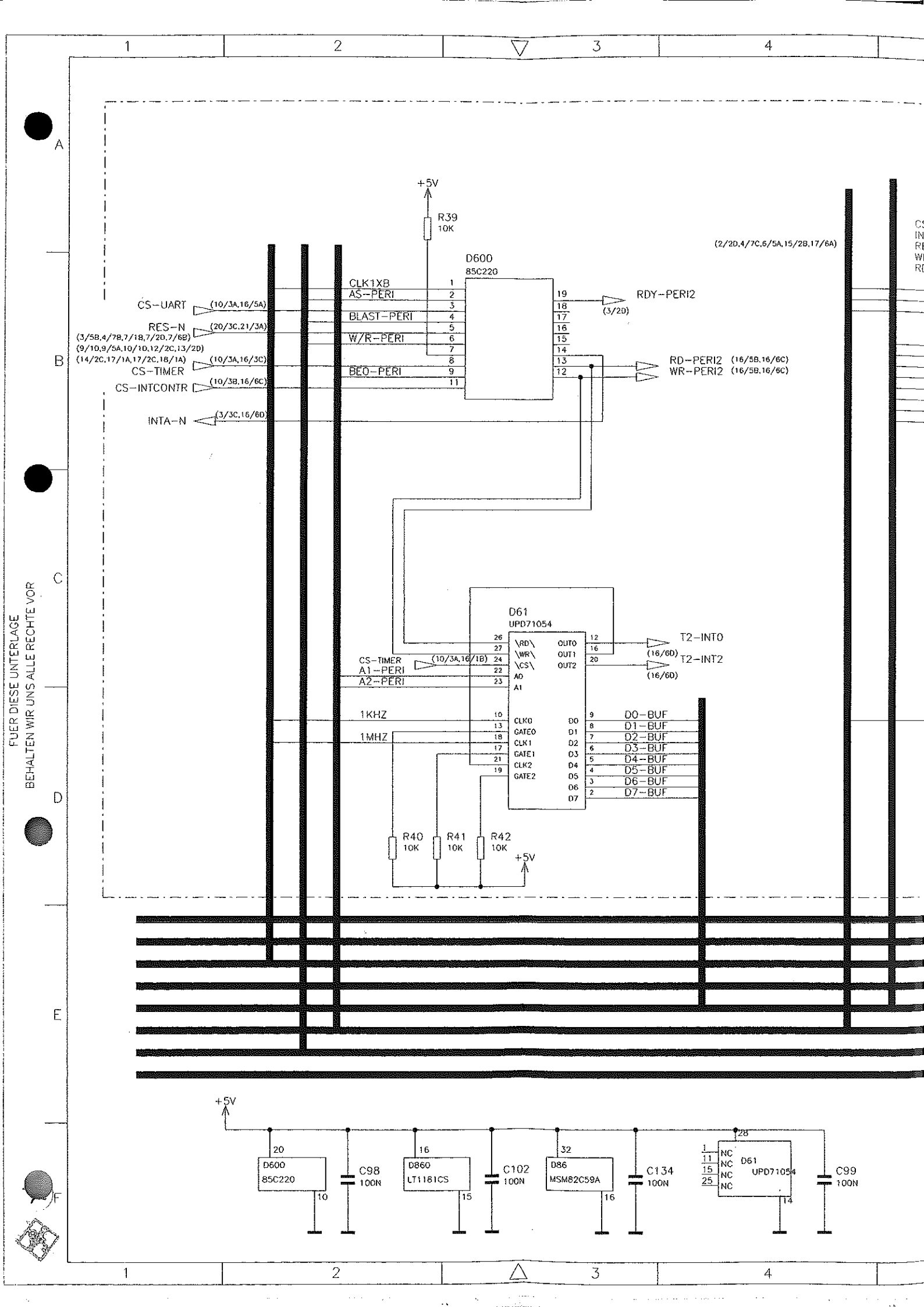
REG.I.V. 1035.5005

ERSTE Z. 1035.5440.01



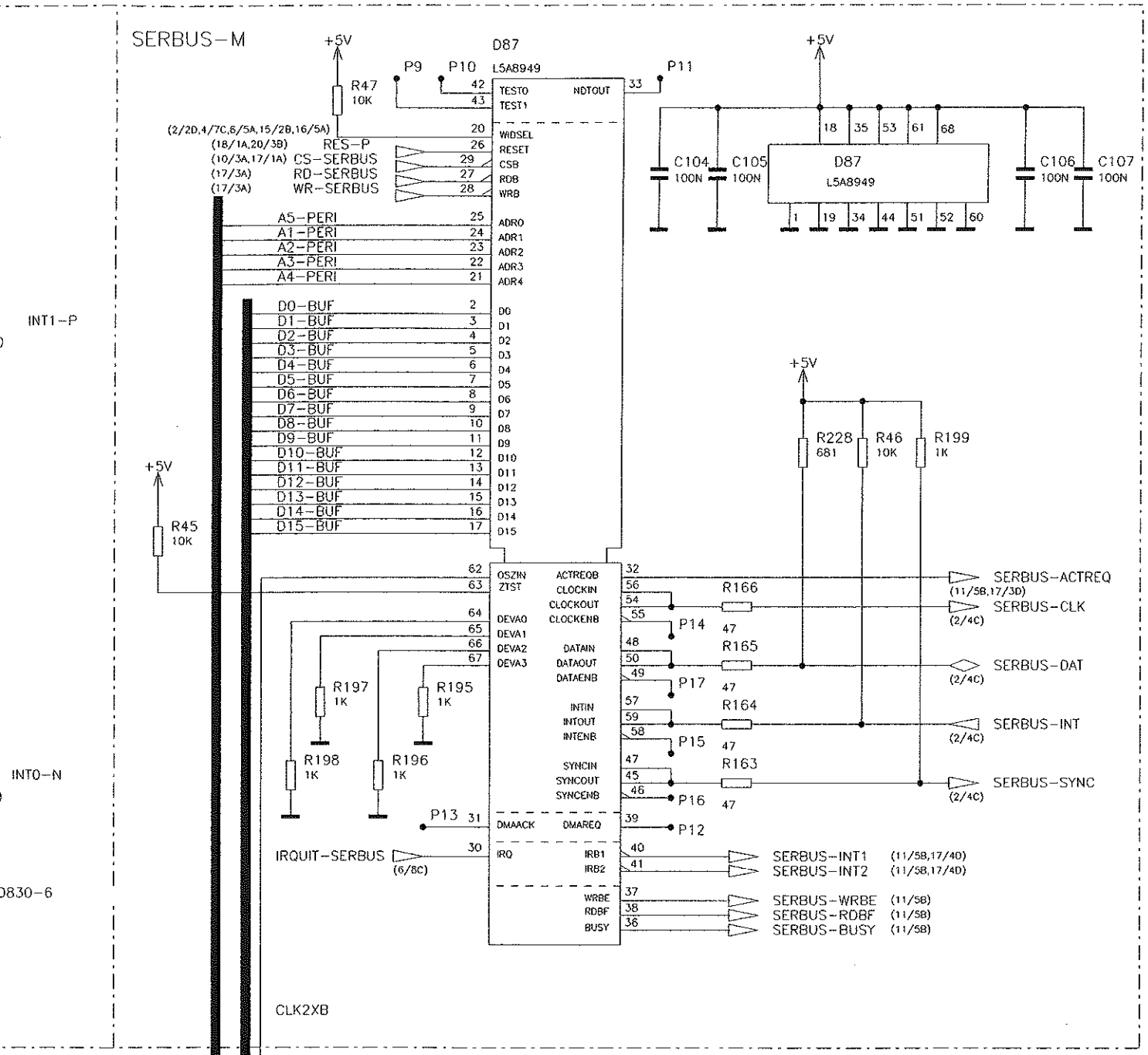


02		27.05.97	EI	MENP	DATUM
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	9.6.97
01		24.06.96	DR	ROHDE&SCH	
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

● P9, P10, P13 ● P11, P12, P14–P17



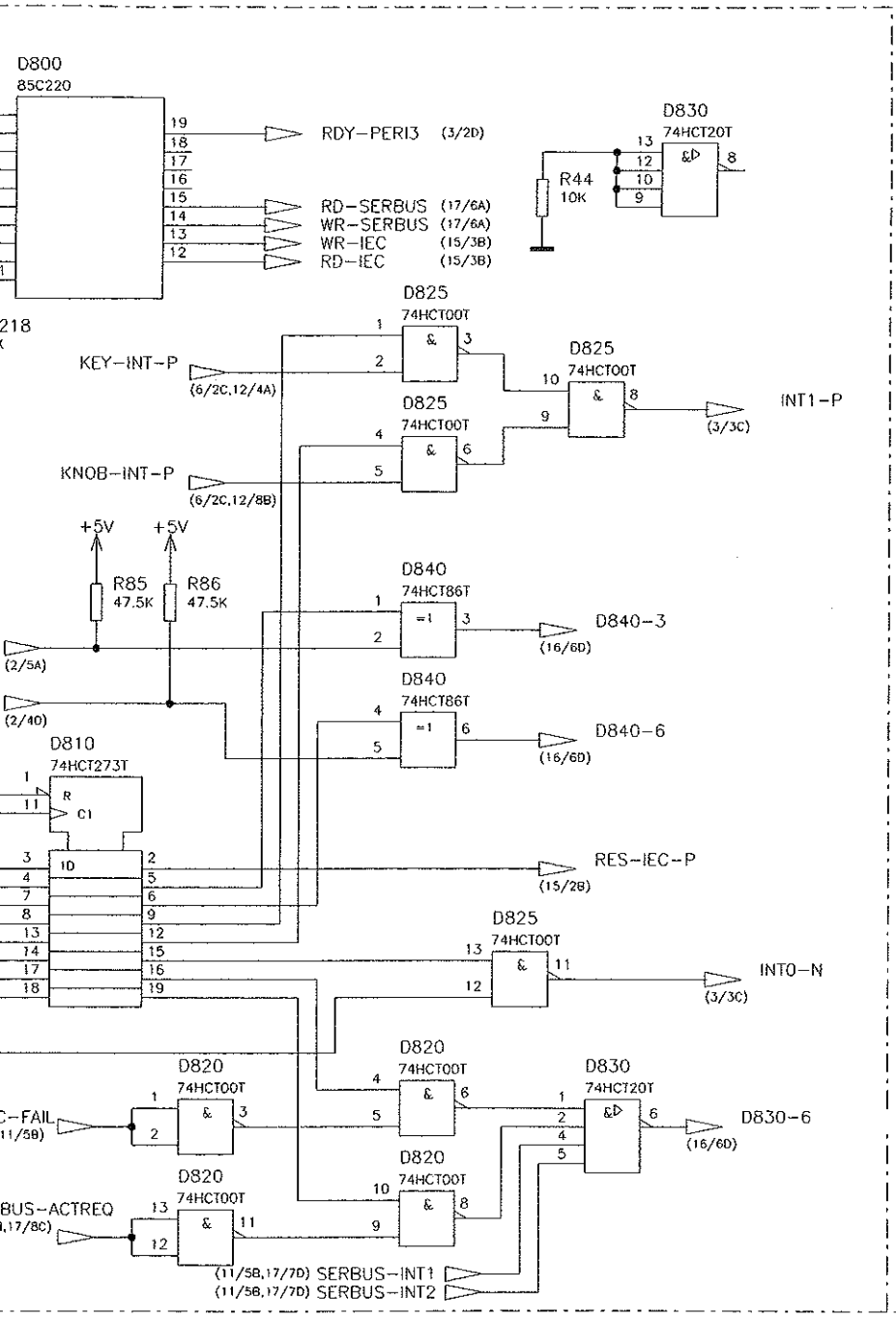
D840 74HCT86T =1

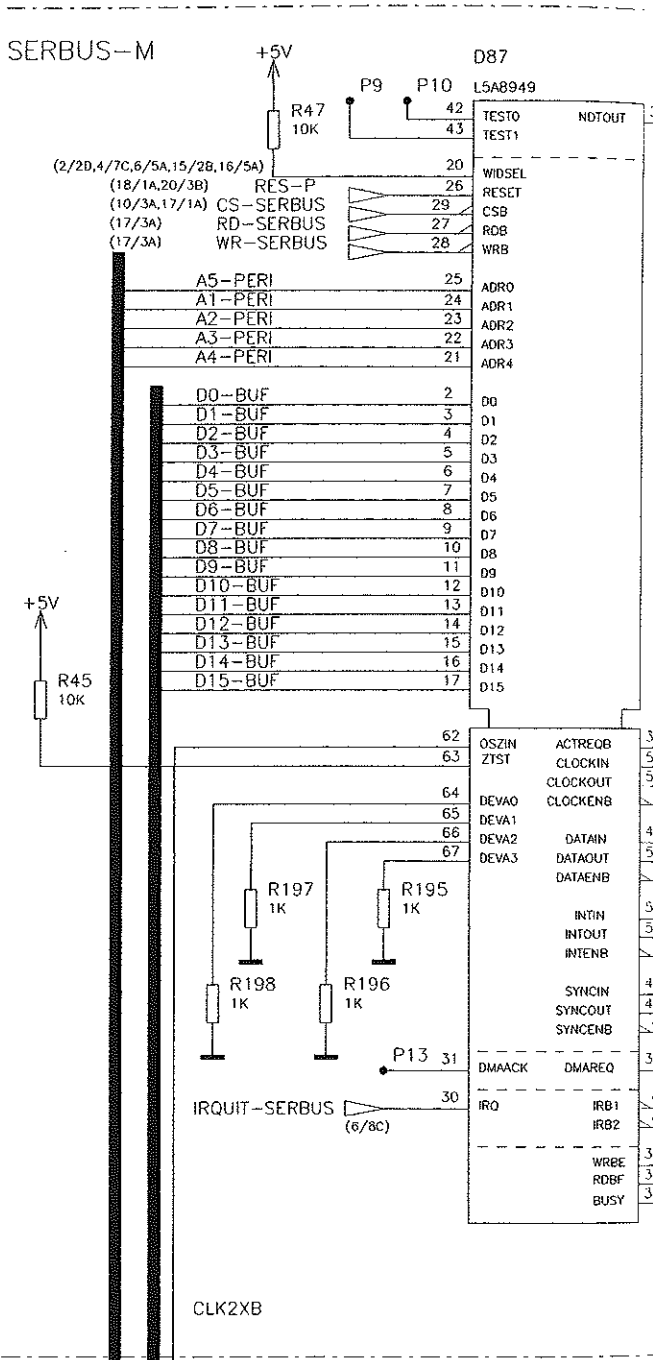
D840 74HCT86T =1

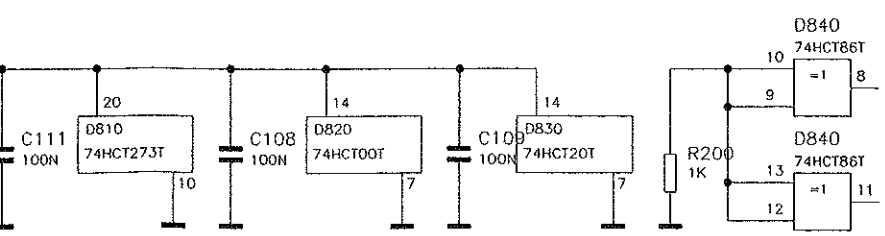
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			
				PLOTT	11.6.97	EICHFELD	TOP/TOP.17
01		24.06.96	DR	ROHDE&SCHWARZ			ZEICHN.–NR. 1035.7766.01 S
AEND. IND.	AENDERUNGS–MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

BLATT–NR. 17 + 47

● P9, P10, P13

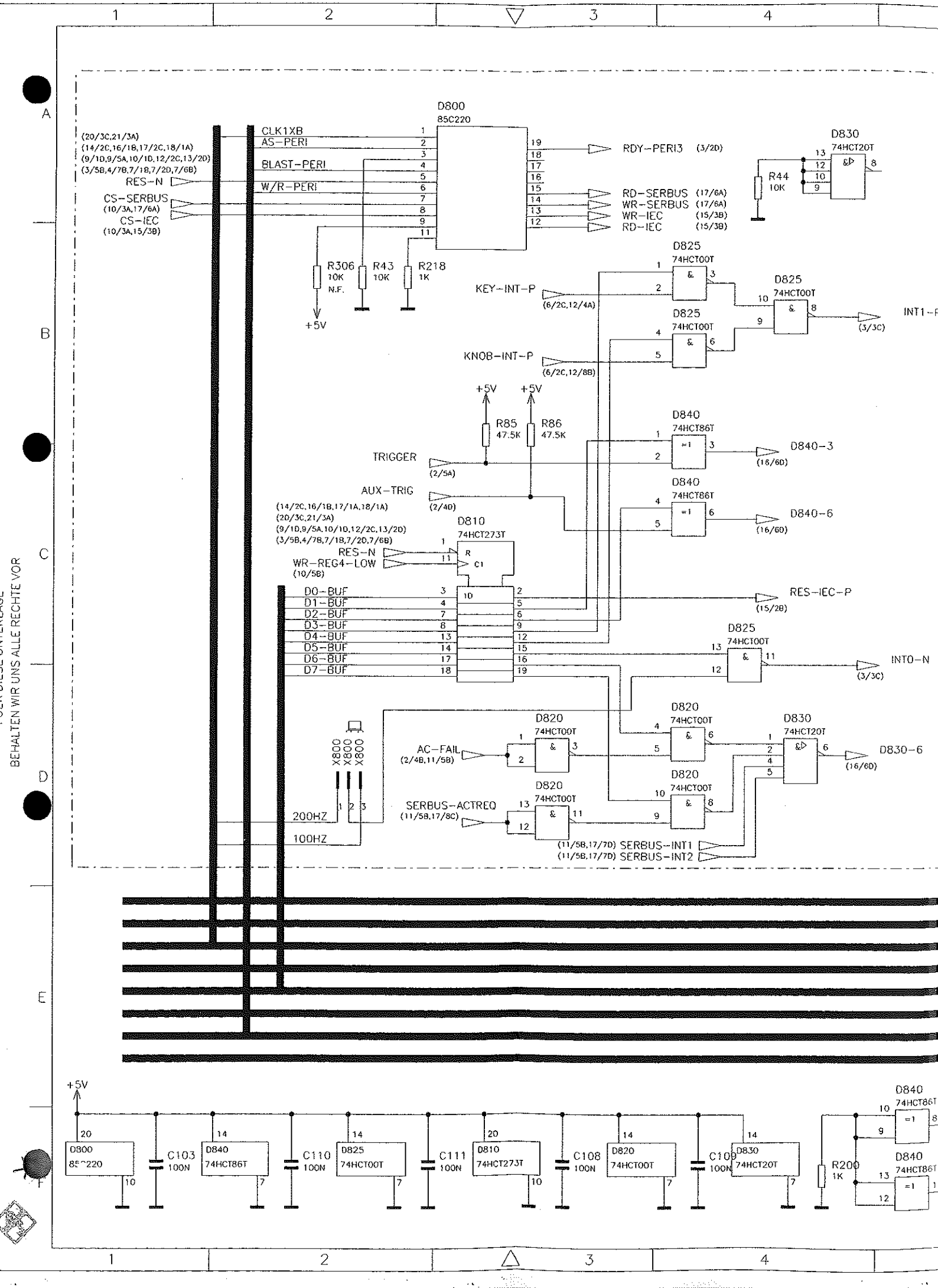


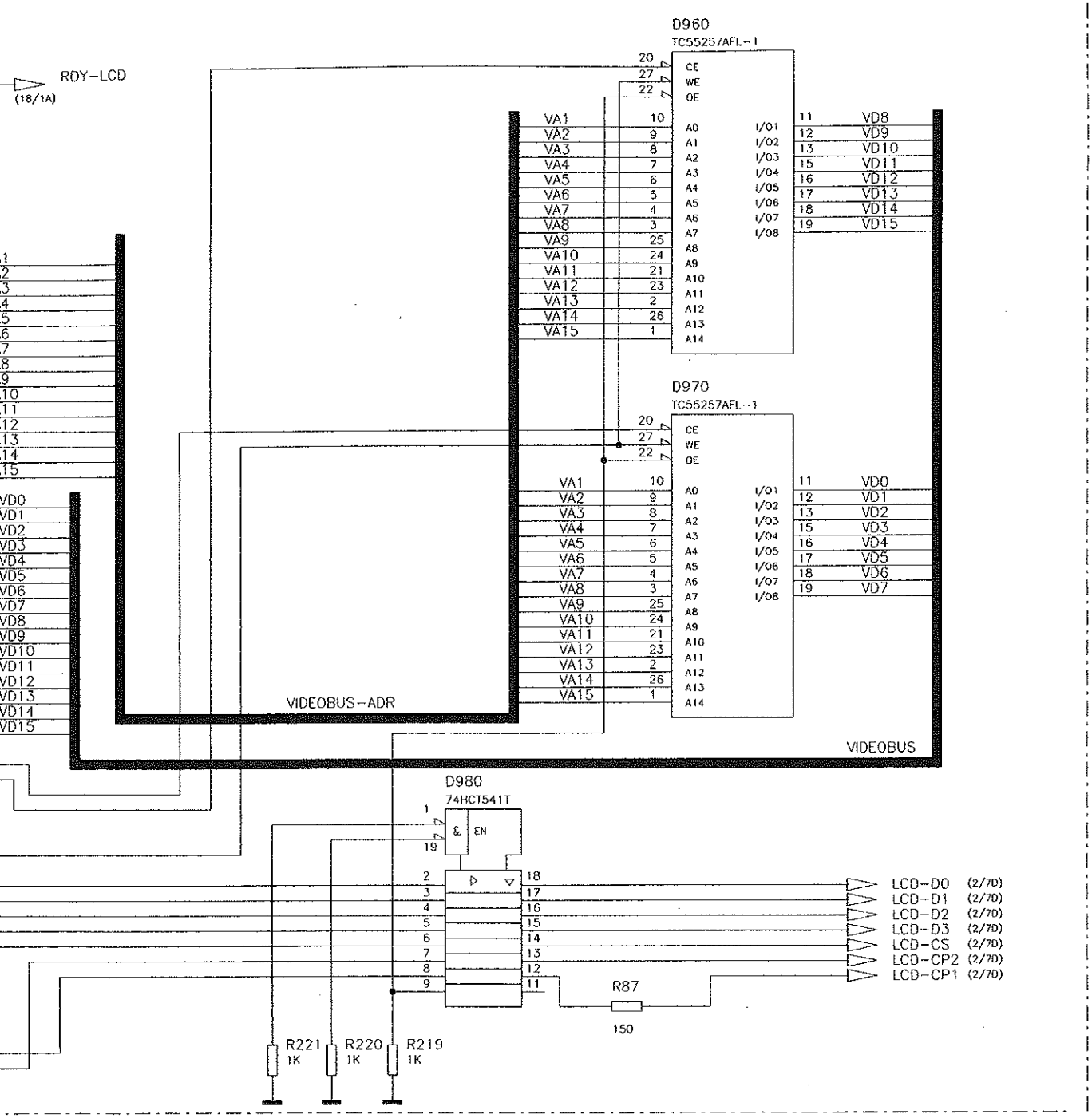




02		27.05.97	EI	MENP	DATUM	NAM
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	11.6.97	EICHE
01		24.06.96	DR	ROHDE&SCHWA		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

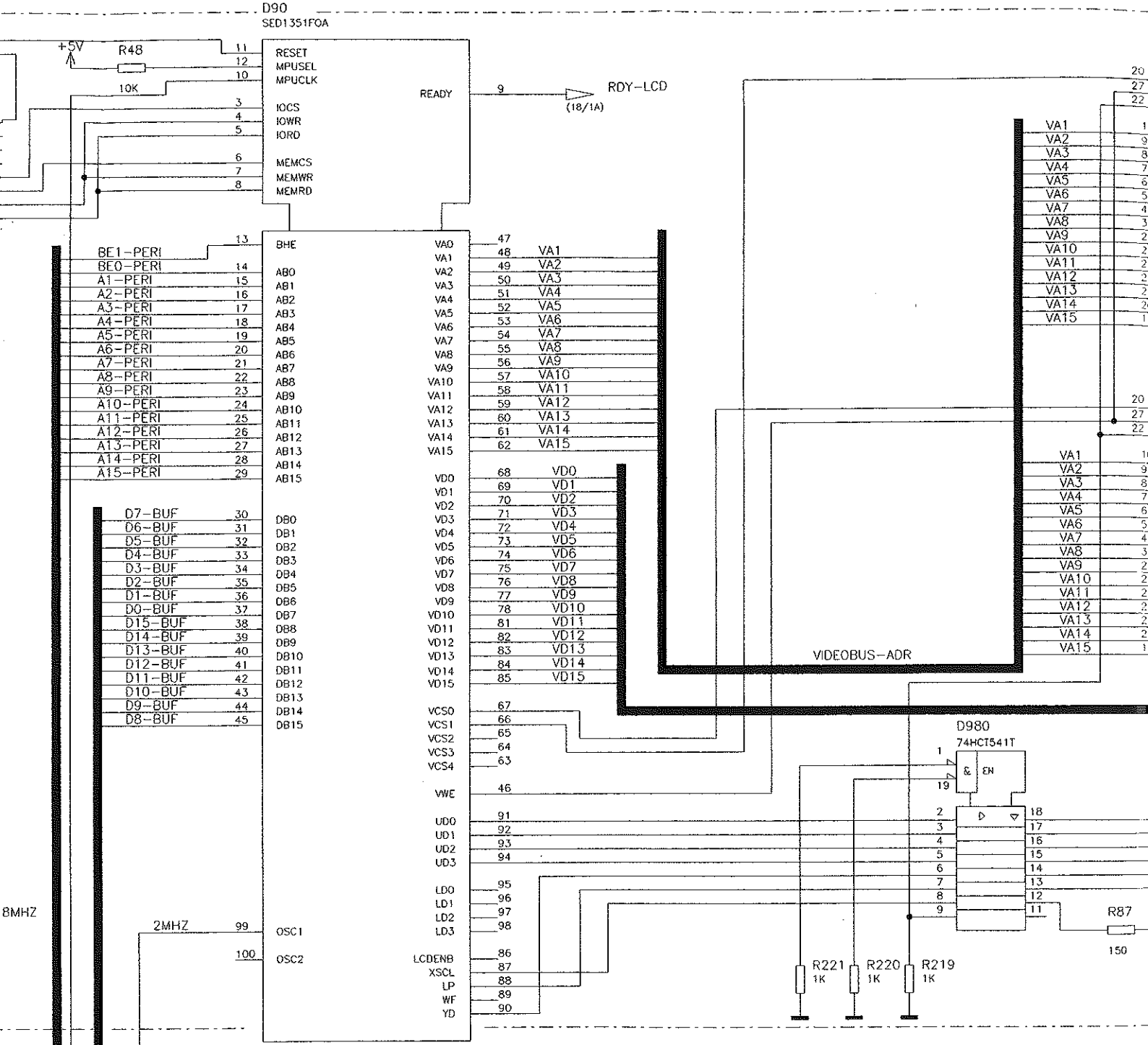
BEHALTEN WIR UNS ALLE RECHTE VOR

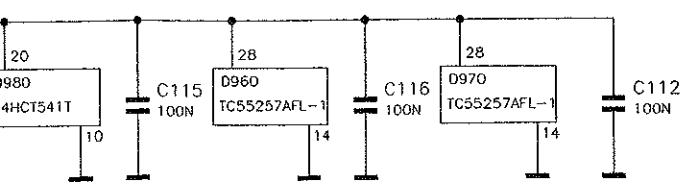




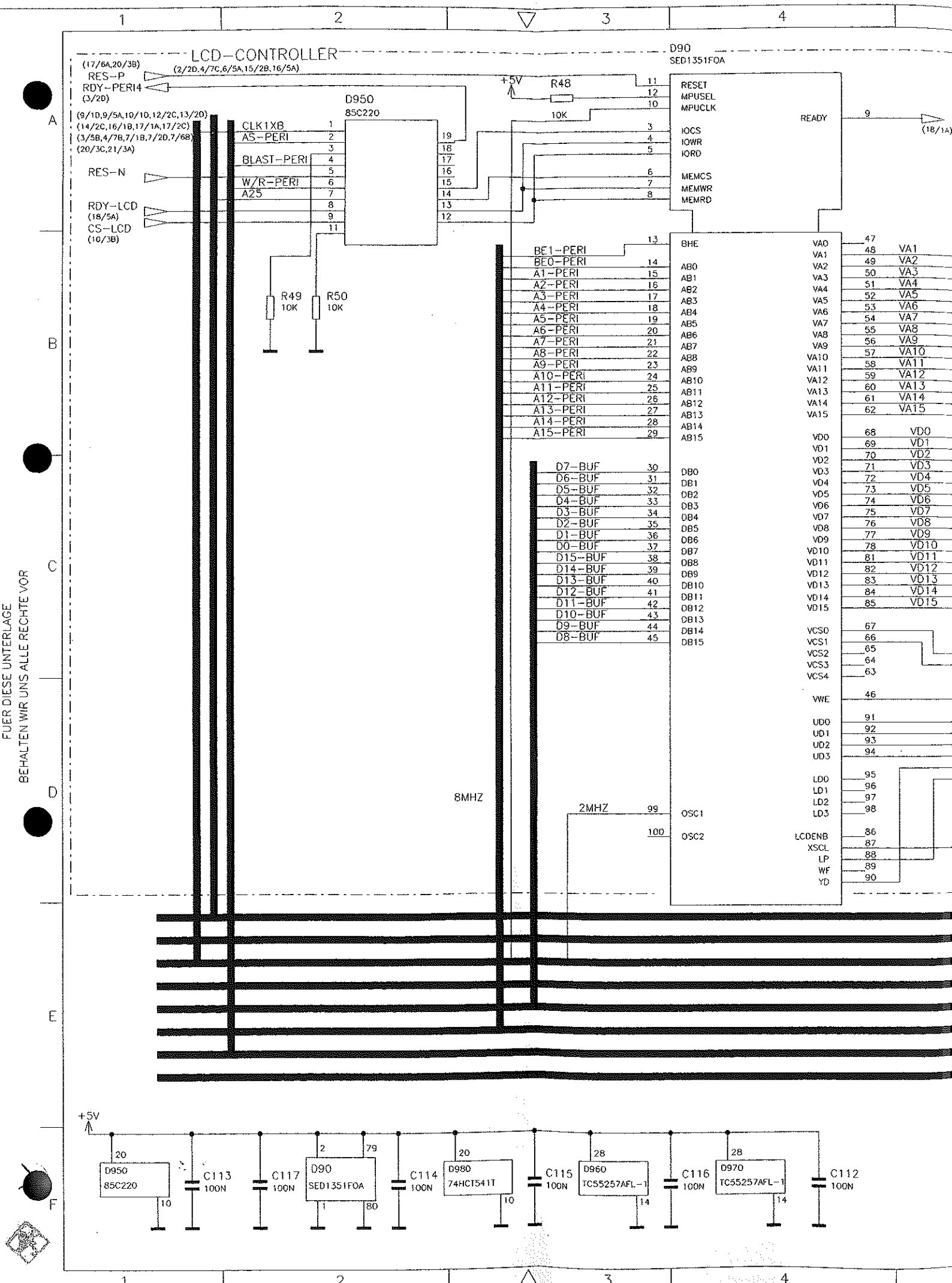
A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.18
				PLOTT	9.6.97	WEH	
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR				BLATT-NR. 18 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

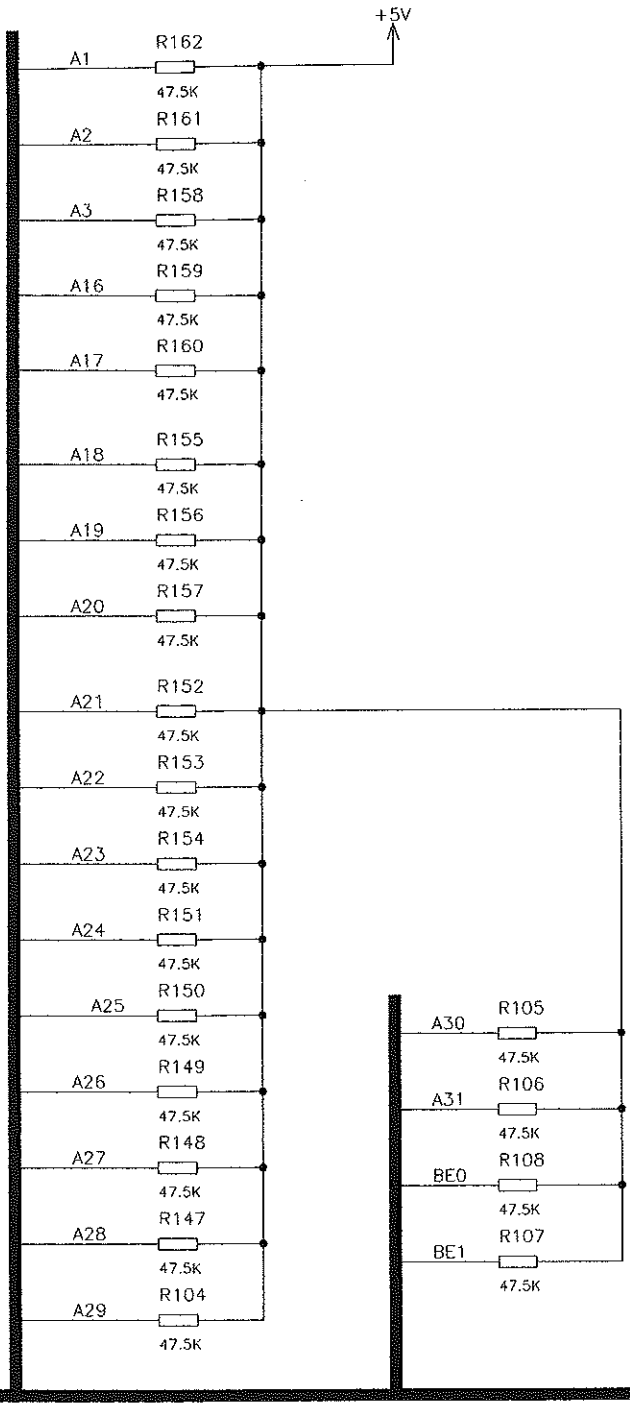




02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	9.6.97	WEH
01		24.06.96	DR	ROHDE&SCHWAR		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

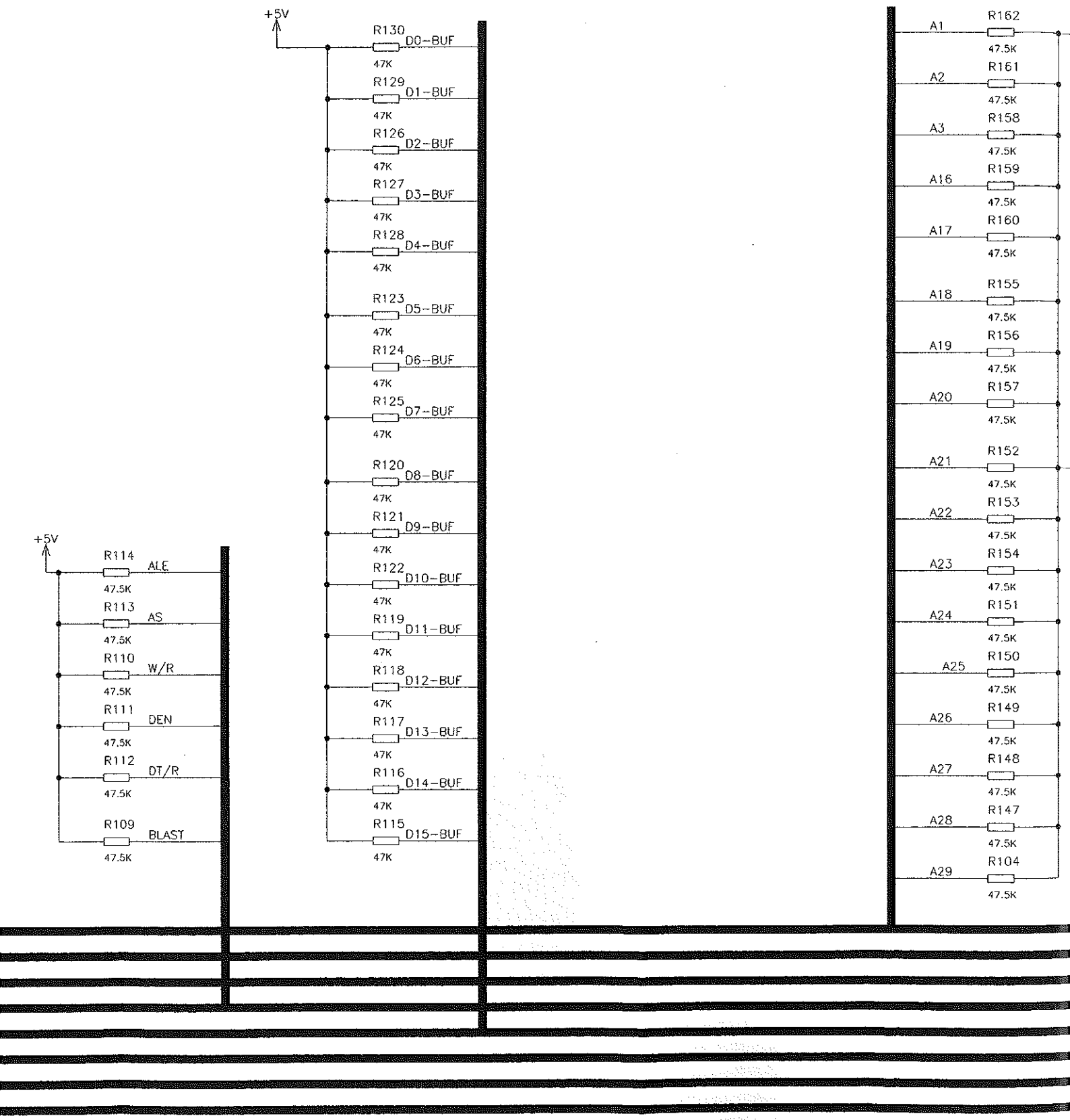


FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR



A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			
				PLOTT	9.6.97	WEH	TOP/TOP.19
							ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR	ROHDE&SCHWARZ			BLATT-NR. 19 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01

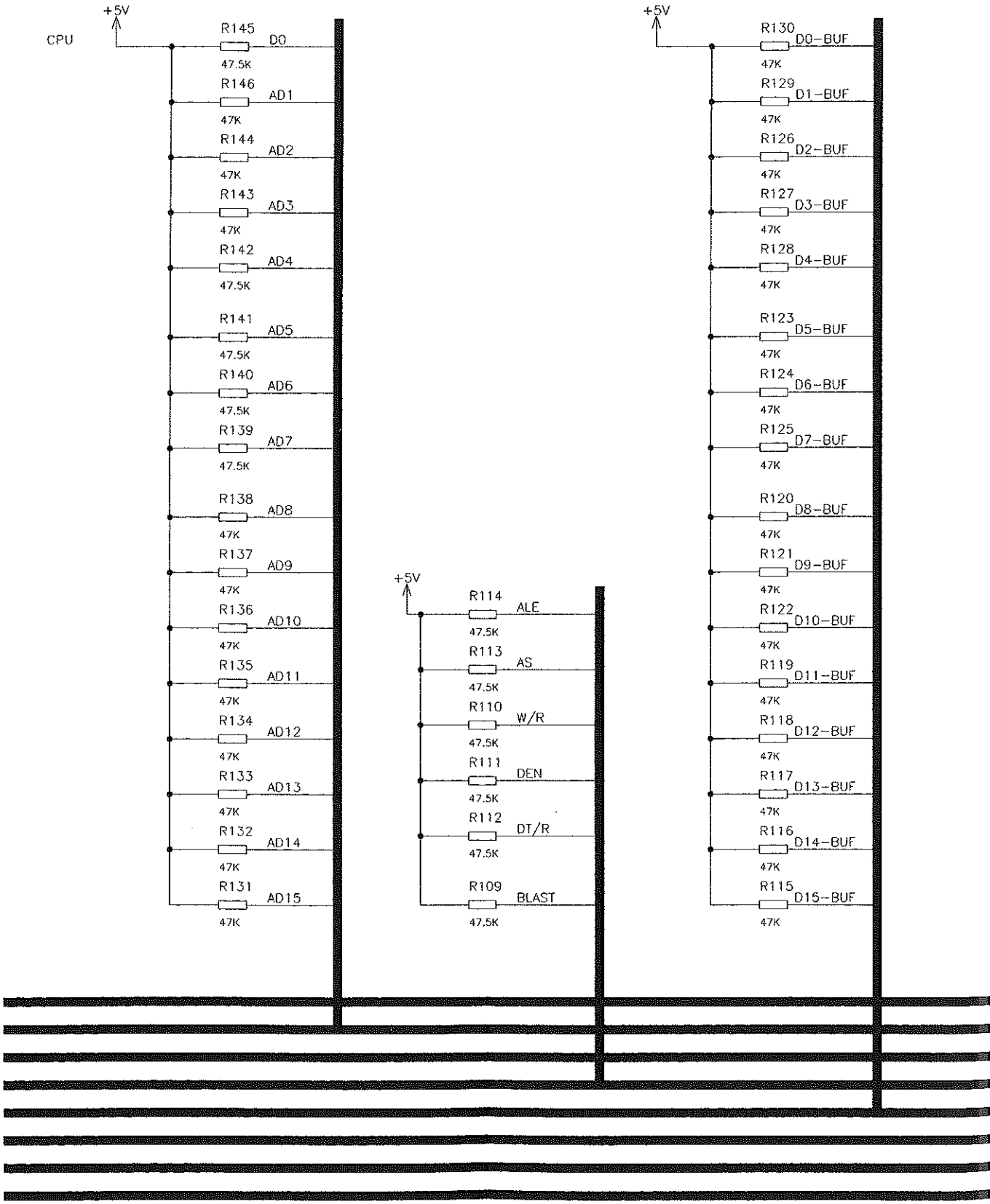


ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

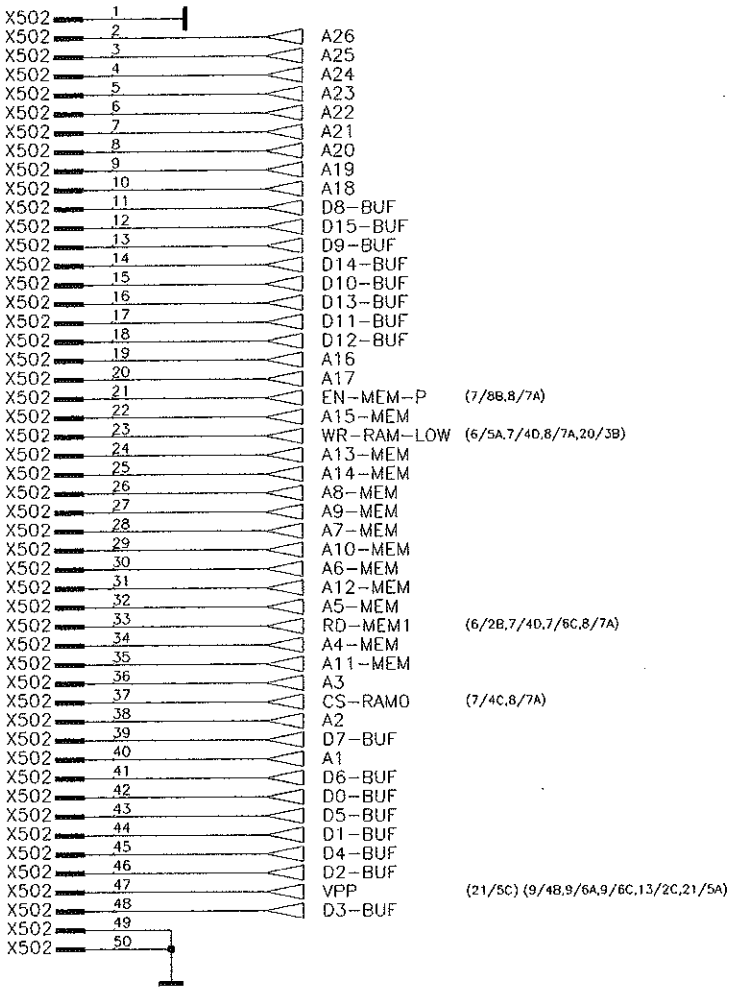
02		27.05.97	EI	MENP	DATUM	
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	9.6.97	
01		24.06.96	DR	ROHDE&SCHW		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

BUS & PULLUP & PULLDOWN



ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING



A-BUS-CPU

AD-BUS-CPU

CLK-BUS

CONTROL-BUS-CPU

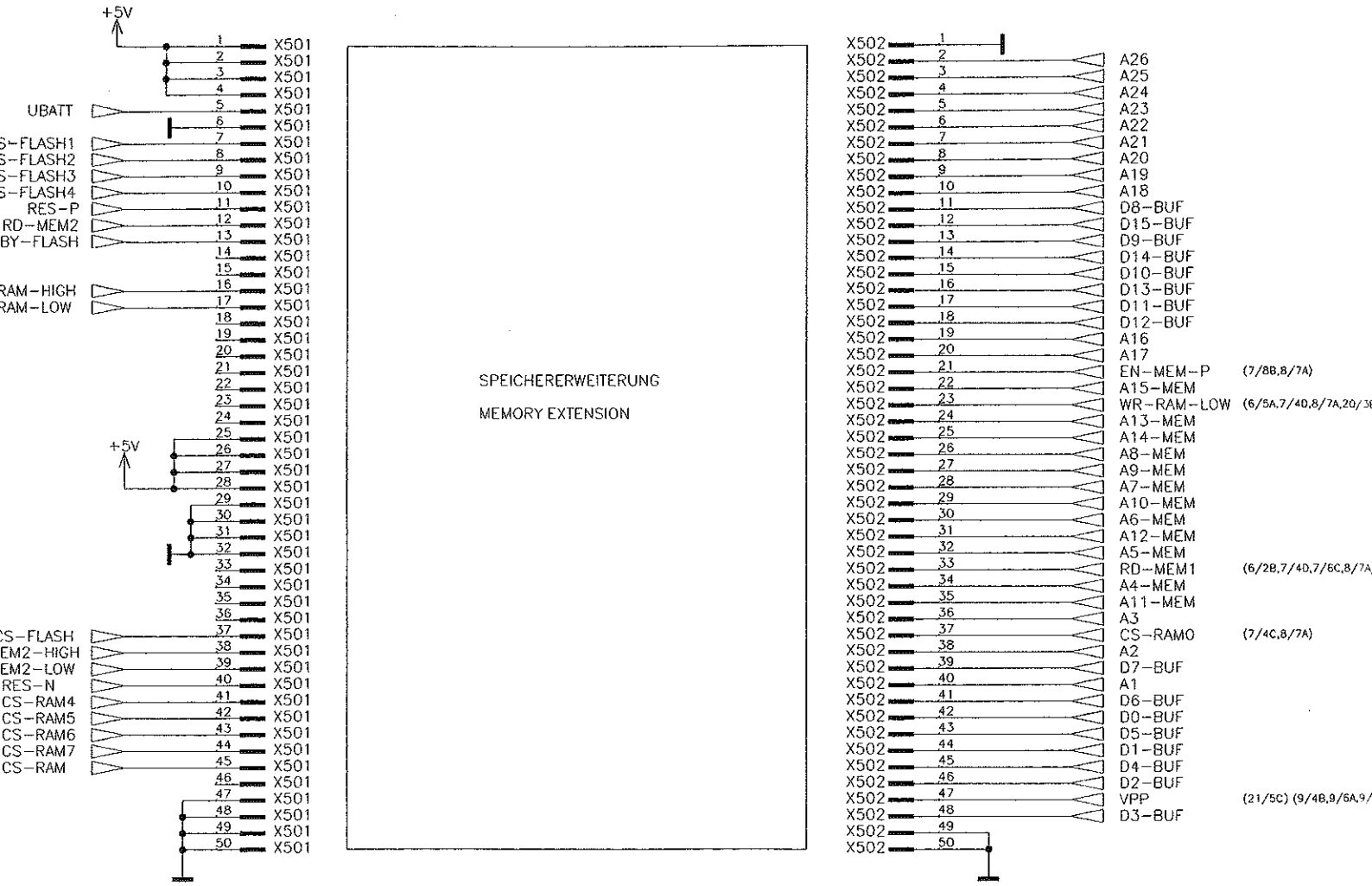
D-BUS-BUF

A-BUS-PERI

CONTROL-BUS-PERI

A-BUS-MEM

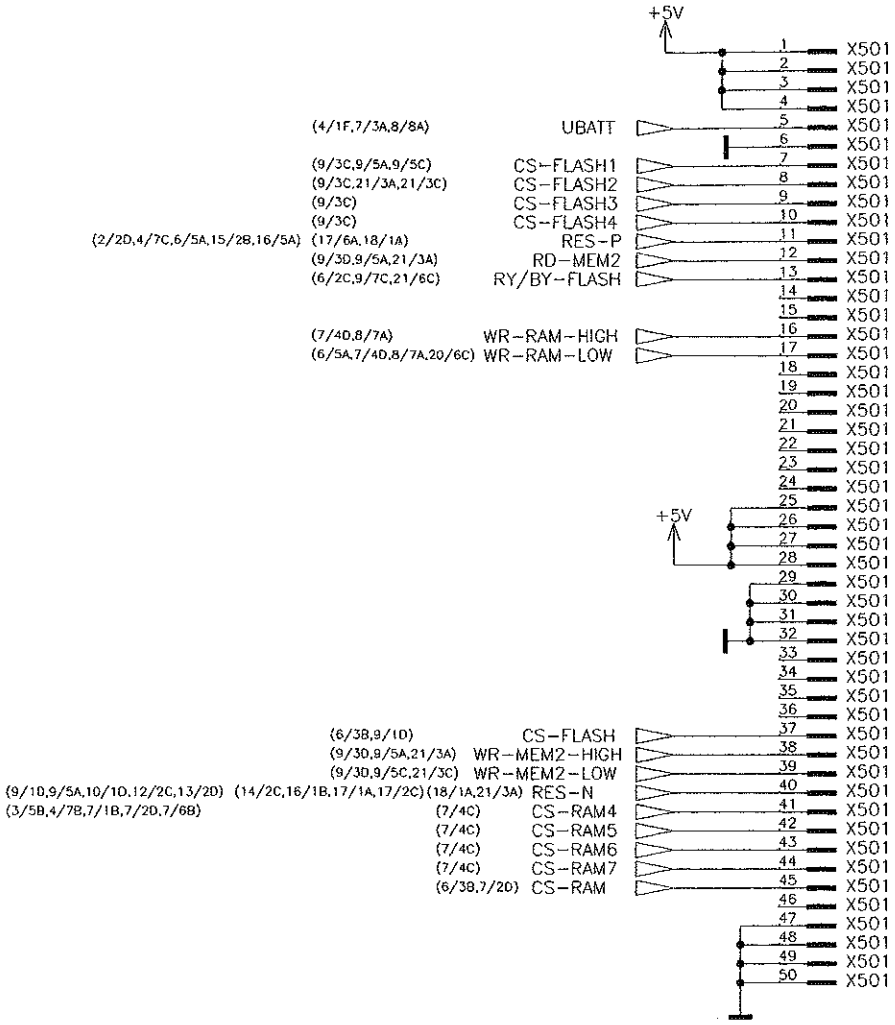
02		27.05.97	EI	MENP	DATUM	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			CPU
				NORM			TOP/TOP.20
				PLOTT	9.6.97	WEH	
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7766.01 S
01		24.06.96	DR				BLATT-NR. 20 + 47
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440.01



ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

02		27.05.97	EI	MENP	DATUM	N
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	9.6.97	W
01		24.06.96	DR	ROHDE&SCHWA		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	

MEMORY-OPTION



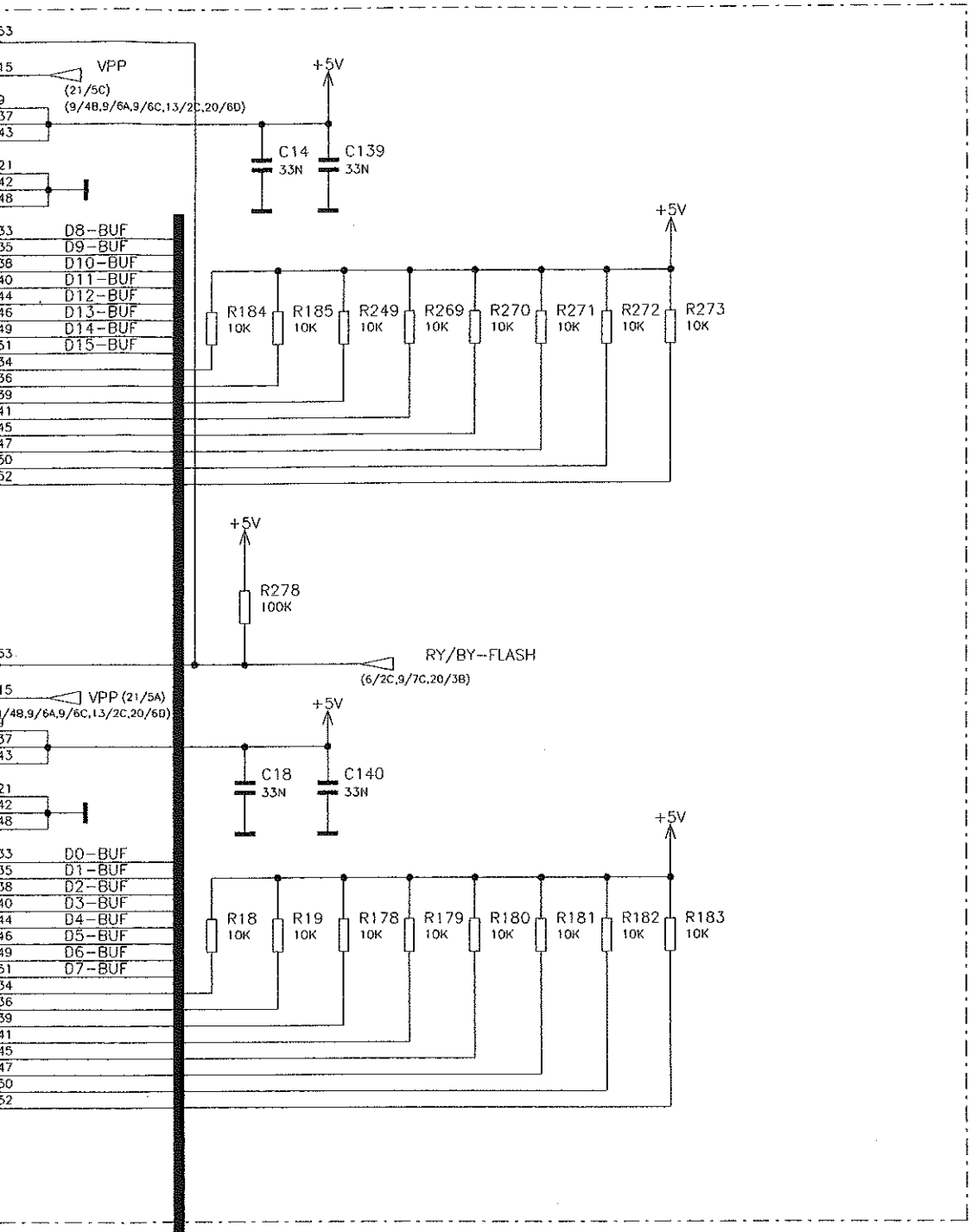
SPEICHERERWEITERUNG

MEMORY EXTENSION

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ACHTUNG: EGB !
ELEKTROSTATISCH GEFAEHRDETE
BAUELEMENTE ERFORDERN EINE
BESONDERE HANDHABUNG.

ATTENTION ESD !
ELECTROSTATIC SENSITIVE DEVICES
REQUIRE A SPECIAL HANDLING



A-BUS-CPU
AD-BUS-CPU
CLK-BUS
CONTROL-BUS-CPU
D-BUS-BUF
A-BUS-PERI
CONTROL-BUS-PERI
A-BUS-MEM

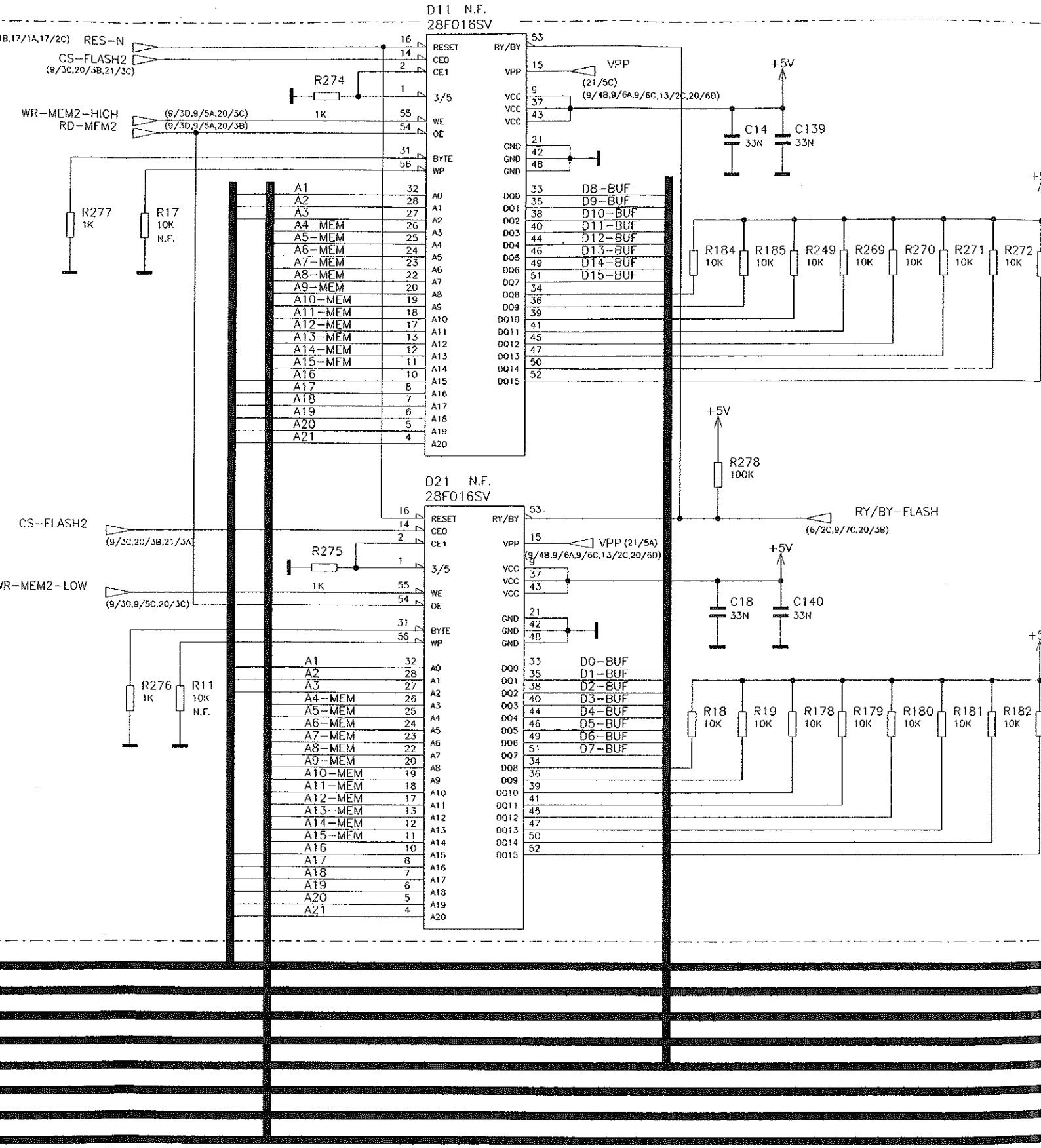
02		27.05.97	EI	MENP	DATUM	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	11.6.97	EICHFELD
01		24.06.96	DR	ROHDE&SCHWARZ		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP		

BENENNUNG
RECHNER
CPU
TOP/TOP.21

ZEICHN.-NR.
1035.7766.01 S

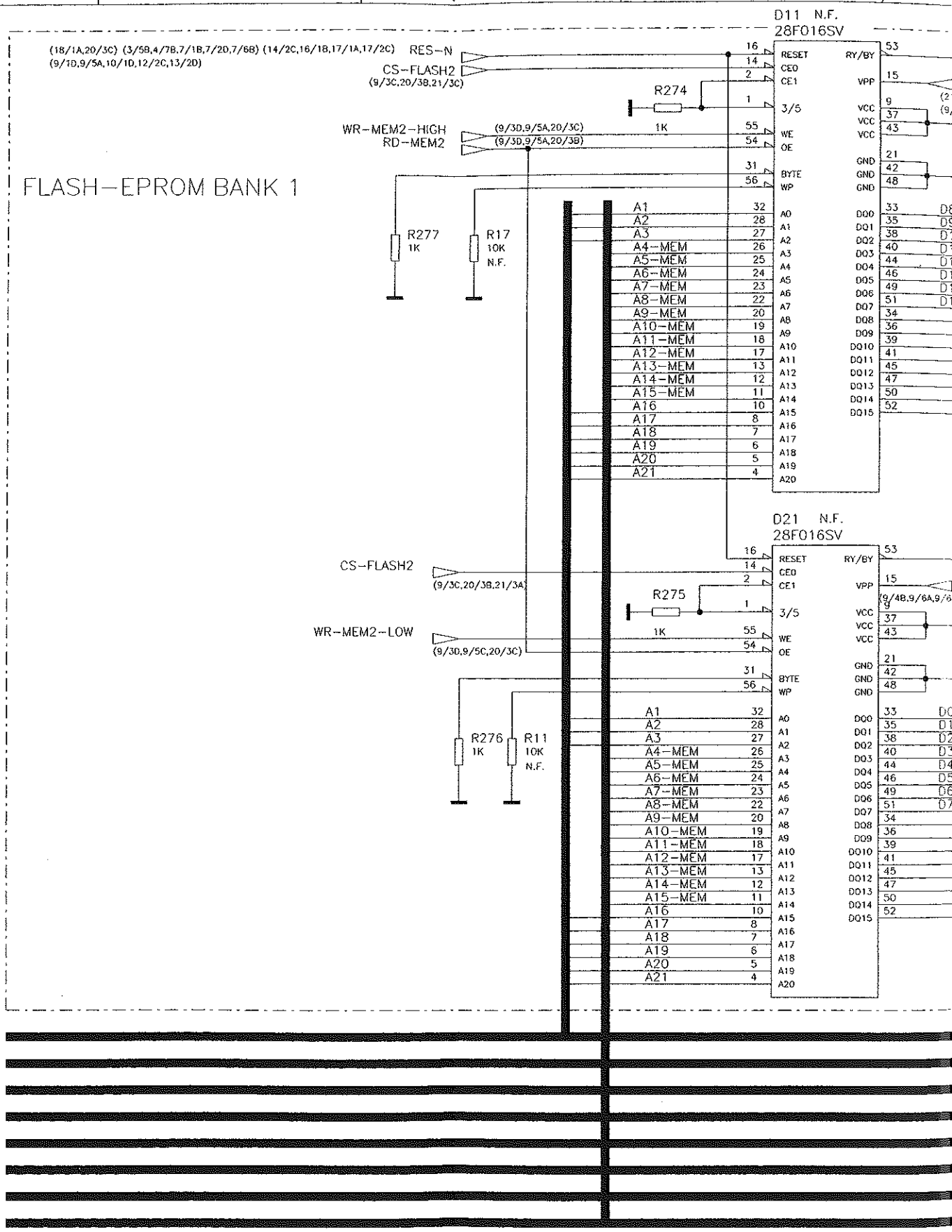
BLATT-NR.
21 + 47

REG.I.V. 1035.5005
ERSTE Z. 1035.5440.01



02		27.05.97	EI	MENP	DATUM
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	11.6.97
01		24.06.96	DR	ROHDE&SCH	
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP	

BEHALTEN WIR UNS ALLE RECHTE VOR



Bindende Angaben ueber Varianten,
Trimmwerte, Bauteile und
nicht bestueckte Bauteile siehe SA.

Bindende Angaben ueber Varianten,
Trimmwerte, Bauteile und
nicht bestueckte Bauteile siehe SA.